

Realtime feedback control of charge sensing for quantum dot qubits

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(Dated: January 6, 2022)

Measurement of charge configurations in few-electron quantum dots is a vital technique for spin-based quantum information processing. While fast and high-fidelity measurement is possible by using proximal quantum dot charge sensors, their operating range is limited and prone to electrical disturbances. Here we demonstrate realtime operation of a charge sensor in a feedback loop to maintain its sensitivity suitable for fast charge sensing in a Si/SiGe double quantum dot. Disturbances to the charge sensitivity, due to variation of gate voltages for operating the quantum dot and $1/f$ charge fluctuation, are compensated by a digital PID controller with the bandwidth of ≈ 100 kHz. The rapid automated tuning of a charge sensor enables unobstructed charge stability diagram measurement facilitating realtime quantum dot tuning and submicrosecond single-shot spin readout without compromising the performance of a charge sensor in time-consuming experiments for quantum information processing.

Recent remarkable advances in spin qubit experiments have been facilitated by the charge sensing technique that allows measurement of charge configurations in few-electron quantum dots (QDs). A charge configuration is typically detected by measuring the conductance change of a capacitively coupled sensor transistor[1]. Measurement of spin states is also realized by using the charge sensing in conjunction with the spin-dependent electron tunneling associated with the Zeeman splitting[2, 3], Pauli spin blockade (PSB)[4, 5], or quantum Hall edge states[6]. Single-shot spin readout can be made accurate and fast enough for fault-tolerant quantum information processing by leveraging the radio frequency (RF) reflectometry[7, 8] with the PSB mechanism[9–12]. However, there is a trade off between the charge sensitivity and the dynamic range of charge sensors. Because the operating window of charge sensors is narrower when it is operated in a few-electron regime to enhance the sensitivity, the charge sensing technique requires subtle tuning of the sensor electrostatic potential that is easily affected by the gate bias voltages and the charge fluctuation in QD devices. It is therefore necessary to perform dedicated calibration, leading to increased complexity in multi-qubit devices. This problem is partially resolved by the gate-based dispersive readout that does not require a sensor transistor[13–16], but it trades off the sensitivity. A spin readout technique suitable for the scalable spin qubit architecture is therefore still lacking.

In this Letter, we report on the automated realtime tuning of a charge sensor for spin-qubit experiments in a Si/SiGe double quantum dot (DQD), which allows

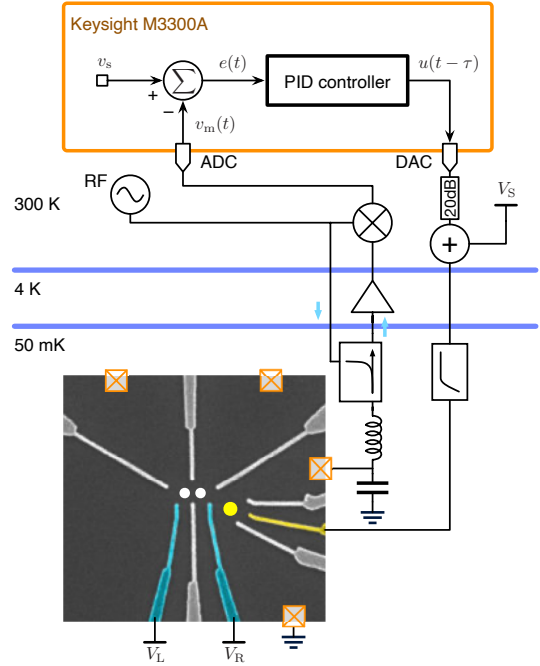


FIG. 1. Schematic of the experimental setup. The Si/SiGe QD sample is mounted on the mixing chamber plate of the dilution refrigerator, where the measured electron temperature is 50 mK (false-colored scanning electron microscope image). The charge arrangement of the electron spin qubits confined in the DQD (white circles) is controlled by the voltages $V_{L,R}$ applied to the plunger gates (blue), and it is detected by the QD charge sensor (yellow circle). The RF carrier reflected from the LC resonant circuit is sampled by the M3300A digitizer and processed by the digital PID controller. The control voltage $u(t - \tau)$ is combined with the DC voltage V_S and applied to the sensor plunger gate (yellow).

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for fine control of charge sensors integrated in many-qubit devices without user intervention. The conductance change of the charge sensor is monitored continuously and compensated by tuning the sensor plunger gate voltage[17]. This feedback loop maintains the charge sensor in sensitive conditions throughout the experiment by eliminating its unwanted variations caused by, e.g., the QD-sensor cross-capacitive coupling and the $1/f$ charge fluctuation. By using the RF readout of the sensor conductance and a digital PID controller, we obtain a set-

ting time of $2.2\mu\text{s}$ allowing for compensation of slow disturbances up to 100 kHz . This is fast enough for live stability diagram measurement, which significantly improves the throughput of manual tuning of QD parameters. The automated sensor tuning in the hardware loop also allows for acquisition of unmistakable charge stability diagrams that are readily used as the input data for software-automated tuning of quantum dot arrays[18–22]. Furthermore, we demonstrate the application of the feedback control to single-shot spin readout synchronized with qubit control pulses.

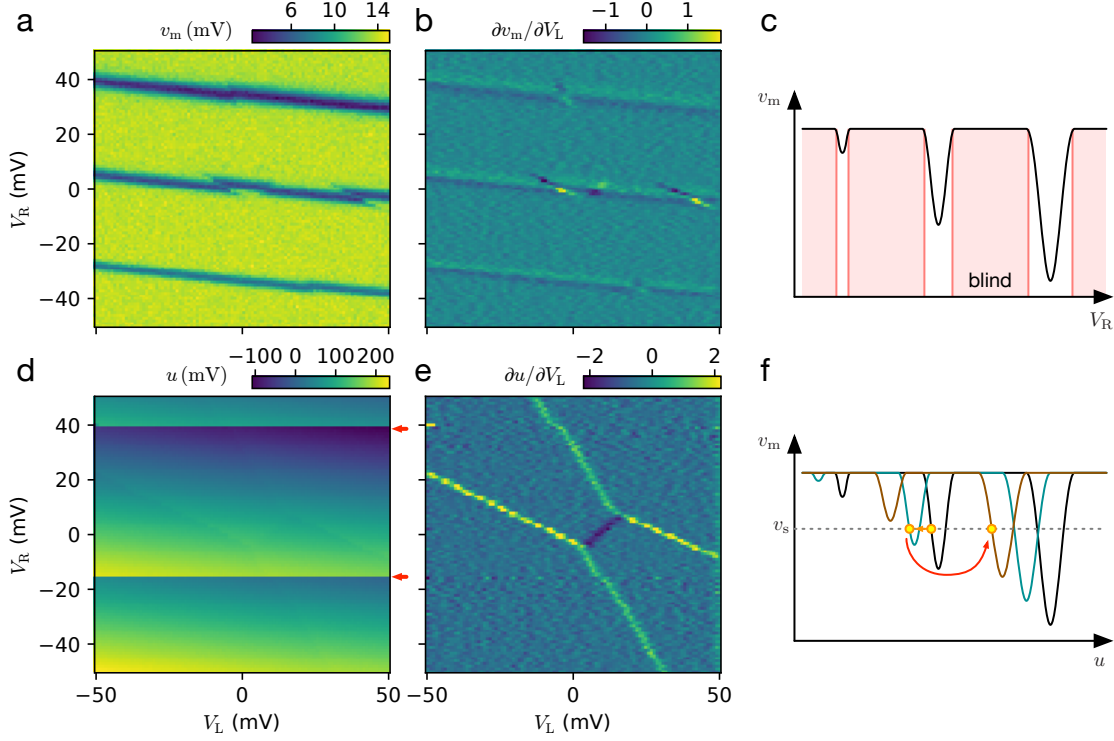


FIG. 2. Stability diagrams measured with the feedback control turned off (a-c) and on (d-f). The values of V_L and V_R are offset from their values at the center of each diagram. Signal is integrated for $14\mu\text{s}$ at each data point and the whole diagram is captured at a refresh rate of about 4 frames per second. (a,b) Reflectometry signal v_m as a function of V_L and V_R (a) and its derivative (b). (c) Illustration of the typical sensor signal trace during the stability diagram measurement. The sensor is sensitive to the charge arrangement only near the Coulomb peaks while it is mostly blind in the Coulomb blockade regime (red shaded region). (d,e) Control voltage u as a function of V_L and V_R (d) and its derivative (e), taken while the feedback control is active. The PID control is reset ($t = 0$) at the lower left corner and $V_{L,R}$ are swept upward. Red arrows in (d) denote jumps from one Coulomb peak to another in the charge sensor. (f) Illustration of the sensor signal traces as functions of u for different values of V_R . Yellow circles show the stable points toward which u is controlled to satisfy $v_m = v_s$. As V_R is increased, the signal trace shifts to the left (from black to green) and the value of u at the stable point decreases. The Coulomb peak height reduces as V_R is increased further (from green to brown), leading to the jumps of the stable point as indicated by the red arrows in (d).

We use a gate-defined DQD coupled with a proximal QD charge sensor fabricated on an undoped Si/SiGe quantum well wafer[23]. The charge sensing is performed by measuring the RF carrier reflected from an LC resonant circuit connected to a sensor ohmic contact[10] (see Fig. 1). The demodulated RF signal v_m is sampled at a rate of 100 MSa/s by a M3300A digitizer from Keysight Technologies. The sampled signal is fed to a tunable digital low-pass filter followed by the digital PID controller

implemented in the onboard field-programmable gate array (FPGA). The control voltage $u(t)$ is given by

$$u(t) = K_p e(t) + K_i \int_0^t e(s) ds + K_d \frac{de(t)}{dt}, \quad (1)$$

where $e(t) = v_s - v_m(t)$ represents the error between v_m and the desired setpoint v_s , while K_p , K_i , and K_d are the coefficients for the proportional (P), integral (I),

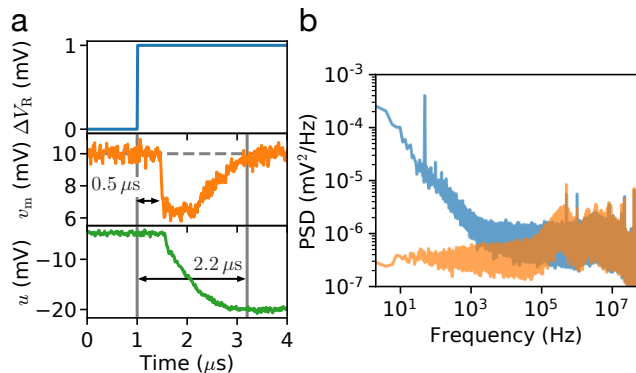


FIG. 3. Performance characterization of the feedback control. (a) Tuned-up step response of the charge sensor. A step waveform is applied to V_R (upper panel) and the responses of v_m (middle panel) and u (lower panel) are recorded. The traces are averaged for 1,000 trials. (b) Noise PSDs of the charge sensor signal v_m . When the feedback is off (blue), we observe the typical $1/f$ tail due to the conductance fluctuation of the charge sensor along with a 50 Hz peak of the power line cycles. These noise components are suppressed by using the feedback control (orange) up to frequencies above 100 kHz.

and derivative (D) terms. The actual output voltage is delayed by $\tau \approx 0.5 \mu\text{s}$ due to the I/O latency of the digitizer, the digital-to-analog converter (DAC) and the FPGA logic, and updated at a rate of 100 MSa/s. The output voltage $u(t - \tau)$ is then attenuated by 20 dB and added to the DC voltage V_S supplied from a digital to analog converter using a home-made summing amplifier circuit. The total voltage is filtered by a Mini-Circuits VLFX-1050+ low-pass filter and applied to the plunger gate of the charge sensor that modulates the reflected signal v_m , thereby closing the feedback loop.

The charge stability diagrams measured with the feedback control turned on and off are shown in Fig. 2. When the feedback is off (Figs. 2a-c), the electron occupation of the charge sensor is affected by V_R and V_L that are varied to control the charge arrangement in the DQD. The DQD charge transition lines are detectable only when the charge sensor is near the charge transitions. It is therefore often necessary to tune V_S to keep the charge sensor sensitive in a desired DQD gate bias condition. This effort of tuning V_S is automated by turning on the feedback control (Figs. 2d-f). As $V_{L,R}$ are varied, u is controlled to keep $v_m = v_s$ where the charge sensor is sensitive. The stability diagrams in Figs. 2d,e are obtained by monitoring the controlled output u , where we can clearly see the whole charge transition lines of the DQD.

The speed and stability of the feedback control is evaluated and optimized by observing the step response of the charge sensor as shown in Fig. 3a. When a step waveform ΔV_R is added to V_R , we observe the response of v_m after a delay of $0.5 \mu\text{s}$ mainly due to the I/O latency. Then the FPGA logic captures the error e and controls u to suppress the error. This effect is visible in v_m after another delay of $\tau \approx 0.5 \mu\text{s}$, where v_m starts to move to-

ward v_s . The response of u is settled and the change of v_m is suppressed by 90% in $2.2 \mu\text{s}$ after the step waveform is applied, with the PID parameters $K_p = -0.80$, $K_i = -0.038$, and $K_d = 0$. The response time could be further improved by decreasing K_p and K_i , but we chose to minimize the overshoot of v_m to avoid the instability caused by the nonlinearity of the charge sensor. Similarly, we chose $K_d = 0$ to avoid possible instability though we did not find a significant impact by changing K_d slightly.

The fast feedback control efficiently stabilizes the charge sensor by suppressing low-frequency disturbance due to the drift and $1/f$ conductance fluctuation caused by charge noise in the DQD device. As shown in Fig. 3b, the noise power spectral density (PSD) of v_m is significantly suppressed by the feedback control from dc to frequencies above 100 kHz, corresponding to the bandwidth of $\approx 1/2.2 \mu\text{s}^{-1}$. In the higher frequency range, the noise PSD only slightly increases due to the parasitic oscillation caused by the PID control. We note, however, that the suppression of the noise PSD in v_m does not immediately mean the improvement of the signal-to-noise ratio (SNR) as u varies to compensate the noise in v_m . Still, single-shot spin measurement can benefit from the stabilized charge sensing by decoupling signals from the low-frequency noise.

To evaluate the improvement in single-shot spin measurement, a singlet-triplet readout using the PSB effect is performed with the pulse cycle shown in Fig. 4a. Figure 4b shows typical histograms constructed from single-shot data taken by integrating v_m for $t_{\text{int}} = 5 \mu\text{s}$ at point M without the feedback control. The outcomes near the left (right) peak centered around $v_m = v_{11}$ (v_{02}) indicate the $(n_L, n_R) = (1, 1)$ ($(0, 2)$) charge arrangement that corresponds to spin triplet (singlet). Here n_L (n_R) indicates the number of electrons in the left (right) QD. The separation of the two peaks is large enough to distinguish singlet and triplet outcomes, but the values of v_{11} and v_{02} fluctuate due to the first-order drift of the charge sensor. In addition, their separation $v_{02} - v_{11}$ is also affected by the second-order drift. Since the sensor drift accumulates in a long experiment, it leads to broadening of the histogram and degradation of the charge sensitivity.

In principle, one can resolve this problem by using the feedback control and measuring u instead of v_m as demonstrated for the stability diagram measurement in Fig. 2. However, this approach has a few drawbacks in practice. First, the settling time of u is an order of magnitude longer than the shortest single-shot measurement time achievable in a similar setup[25], though it is still fast enough for high-fidelity spin measurements[3, 26]. Second, application of the control pulse such as the one in Fig. 4a may cause a steep response in the charge sensor and failure of the feedback loop. To avoid these problems while taking advantage of the feedback, we interrupt the PID control synchronously with the control pulse cycle depicted in Fig. 4a. In this scheme, the single-shot measurement is performed by taking v_m at point M while

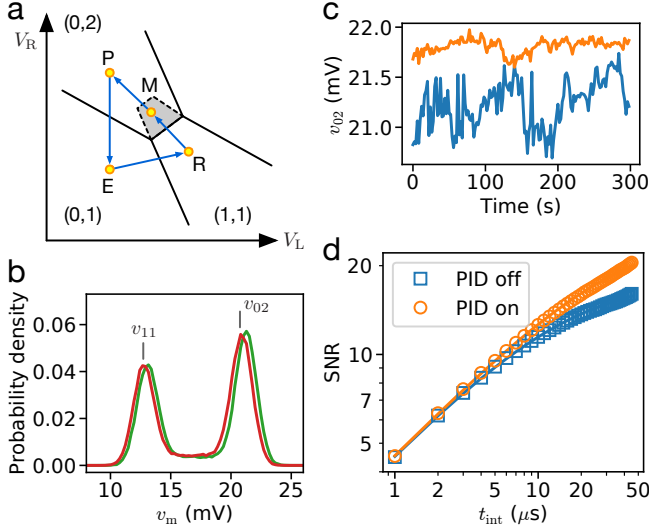


FIG. 4. Single-shot spin measurement with the feedback-controlled charge sensor. (a) Pulse cycle for the spin measurement using PSB[24]. Solid black lines show the DQD charge transitions. The electron in the left QD is emptied (E), reloaded (R), measured (M), and parked (P) at the points marked by yellow circles. The dwell times at these points are $10\ \mu\text{s}$ (E), $10\ \mu\text{s}$ (R), $445\ \mu\text{s}$ (M), and $35\ \mu\text{s}$ (P), respectively. PSB is lifted outside the gray shaded region. The PID control is enabled or disabled at point P, while it is disabled elsewhere. (b) Histograms of two sets of the single-shot measurement carried out in succession without the feedback control (the PID control is disabled at point P). Each histogram is constructed from 100,000 measurement outcomes taken by integrating v_m for $t_{\text{int}} = 5\ \mu\text{s}$. The left (right) peak shows the outcomes indicating the (1,1) ((0,2)) charge arrangement that corresponds to spin triplet (singlet). The peak positions v_{11} and v_{02} fluctuate with time. (c) Time traces of v_{02} taken by turning on (orange) or off (blue) the feedback control. The values of v_{02} are extracted by fitting the histograms similar to those in (b)[25]. (d) SNRs of the single-shot measurement as functions of the integration time t_{int} with the feedback control on (orange circles) or off (blue squares). Solid curves are the fits with $\sigma_0 = 1.81 \pm 0.01\ \text{Vs}^{-\frac{1}{2}}$ and $t_0 = 0.03 \pm 0.01\ \mu\text{s}$ (see the main text).

the PID control is disabled. In the next step, the PID control is enabled at point P, where PSB is lifted and the charge sensor is stabilized for sensing the (0,2) charge arrangement. Then the emptying and reloading steps follow with the PID control disabled again. Figure 4c shows that the fluctuation of v_{02} is successfully suppressed in this scheme.

The noise suppression improves the SNR in the single-shot measurement defined as $\text{SNR} = |v_{02} - v_{11}|/\sigma$, where σ^2 is the variance of each peak in the histogram. Figure 4d shows the SNRs with the feedback turned on or off as functions of the integration time t_{int} . We fit the SNR curves to $\sigma = \sqrt{\sigma_0^2/[t_{\text{int}} + t_0] + \sigma_d^2(t_{\text{acq}})}$, where σ_0^2 represents the white-noise broadening, t_0 accounts for the measurement bandwidth[25], and $\sigma_d^2(t_{\text{acq}})$ is the contri-

bution from the low-frequency noise which increases with the total data acquisition time t_{acq} . For $t_{\text{acq}} = 300\text{ s}$ used in the present experiment, we find that the SNR eventually saturates for longer t_{int} with $\sigma_d^2 = 0.20\ \text{mV}^2$ when the feedback is turned off. If the ideal $1/f$ noise persists in a longer experiment of $t_{\text{acq}} = 24\text{ hours}$, σ_d^2 amounts to $0.27\ \text{mV}^2$. The feedback control reduces the low-frequency noise contribution to $\sigma_d^2 = 0.10\ \text{mV}^2$ and brings notable improvement in the SNR. This improvement, however, does not have a significant impact on the single-shot readout fidelity as long as the SNR is large enough. On the other hand, the reduction of σ_d^2 is important for a smaller SNR, which is the case when a charge sensor probes a charge arrangement in farther quantum dots or an azimuthal charge movement. As an example, consider single-shot spin measurement with $|v_{02} - v_{11}| = 2\ \text{mV}$ for $t_{\text{int}} = 100\ \mu\text{s}$ (shorter than the typical spin lifetime $T_1 > 1\ \text{ms}$), which is a setup used to probe a weak charge sensing signal. In this case, the readout error is as large as 1.9% for $t_{\text{acq}} = 300\text{ s}$ and 3.5% for $t_{\text{acq}} = 24\text{ hours}$ without the feedback, while it can be improved to 0.3% with the feedback. In addition to the $1/f$ noise, a charge sensor may be affected by random switching noise in a long experiment. The switching noise is also compensated by the feedback control shown in Fig. 4. This feedback can be done with little overhead because the feedback settling time of $2.2\ \mu\text{s}$ is shorter than typical control pulse cycles (ranging from a few μs to tens of ms). The feedback control eliminates those possibilities of the fidelity degradation and allows for stable measurement without the need of routine calibration steps. The same feedback protocol is applicable to other single-shot measurement schemes including the one relying on the energy-selective tunneling to reservoirs[2, 3].

In conclusion, we have developed a digital feedback control system for stable and reliable charge sensing in quantum dot devices. We have shown that a charge sensor can be maintained in a sensitive condition despite the crosstalk of gate voltages and charge fluctuation, exempting qubit operators from the labors of calibration. The settling time as short as $2.2\ \mu\text{s}$ allows us to perform fast charge stability diagram measurements and single-shot spin readouts without bothered by noise slower than $100\ \text{kHz}$. We expect that the feedback control of charge sensors is particularly useful in demanding computational tasks requiring a longer calculation time and a larger qubit system.

ACKNOWLEDGMENTS

We thank RIKEN CEMS Emergent Matter Science Research Support Team for technical assistance. Part of this work was financially supported by JST CREST Grant Numbers JPMJCR15N2 and JPMJCR1675, JST PRESTO Grant Number JPMJPR2017, MEXT Quantum Leap Flagship Program (MEXT Q-LEAP) Grant Number JPMXS0118069228, JSPS KAKENHI Grant

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