

A 64-pixel mid-infrared single-photon imager based on superconducting nanowire detectors

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A large-format mid-infrared single-photon imager with very low dark count rates would enable a broad range of applications in fields like astronomy and chemistry. Superconducting nanowire single-photon detectors (SNSPDs) are a mature photon-counting technology as demonstrated by their figures of merit. However, scaling SNSPDs to large array sizes for mid-infrared applications requires sophisticated readout architectures in addition to superconducting materials development. In this work, an SNSPD array design that combines a thermally coupled row-column multiplexing architecture with a thermally coupled time-of-flight transmission line was developed for mid-infrared applications. The design requires only six cables and can be scaled to larger array sizes. The demonstration of a 64-pixel array shows promising results for wavelengths between $3.4\mu\text{m}$ and $10\mu\text{m}$, which will enable the use of this single-photon detector technology for a broad range of new applications.

Superconducting nanowire single-photon detectors (SNSPDs) have matured into a widespread photon-counting technology for applications in fields like quantum optics,¹ quantum computing,² and quantum communications³. Some of their outstanding metrics are very high system detection efficiencies of over 98%,⁴ low timing jitter below 3 ps,⁵ dark count rates below 1 count per day,⁶ high maximum count rates up to 1.5 gigahertz,⁷ zero readout noise, and sensitivity over a broad range of wavelengths from the ultraviolet (UV)⁸ to the mid-infrared⁹.

Cameras capable of counting single mid-infrared photons could be an important technological development for applications in astronomy, in particular for the spectroscopy of exoplanet atmospheres in future space telescopes.¹⁰ Mid-infrared cameras used in current space telescopes such as the James Webb Space Telescope utilize blocked impurity band (BIB) and mercury cadmium telluride (MCT) detectors. Although they operate at higher temperatures than SNSPDs (4 K – 10 K), these detectors do not have sufficient sensitivity to count single photons. BIB detectors also suffer from drawbacks such as reset anomalies, last-frame effects, droop, and drift which all lead to instability, making them less desirable for applications such as exoplanet spectroscopy which requires stability on the order of only a few ppm over several hours.^{11,12} Both BIB and MCT detectors also exhibit significantly higher noise and dark count rates in comparison to SNSPDs. In addition to applications in astronomy, mid-infrared-optimized SNSPDs have shown promise in the field of physical chemistry and vibrational spectroscopy.¹³

While there are several potential applications for mid-infrared-optimized single-photon detector arrays, in the recent past SNSPDs have only been used at telecommunications and visible wavelengths. The optimization of

SNSPDs for mid-infrared wavelengths is challenging due to the materials development needed to achieve high detection efficiencies of photons with lower energies. Both the superconducting material used to create the SNSPD as well as the materials used in the optical stack which enhances the absorption in the SNSPD must be modified compared to telecom and shorter wavelengths. Recently, we demonstrated that SNSPDs based on the amorphous superconductor WSi are capable of detecting single photons up to a wavelength of $10\mu\text{m}$.^{9,14} This was achieved by increasing the silicon content of the WSi resulting in a lower superconducting gap energy and higher resistivity.

In addition to the challenges inherent in improving the long-wavelength sensitivity of single pixels, large-format SNSPD arrays are still at an early stage of development even in the near-infrared. Various multiplexing techniques have been demonstrated over the past decade including row-column readout,^{15,16} thermal row-column,¹⁷ and thermally-coupled imager (TCI)^{18,19}. To date, the largest array of 400 kilopixels was demonstrated using the TCI architecture,¹⁹ although this array was only efficient at detecting photons in the visible.

The high-resistivity WSi films and narrow nanowire widths used in the fabrication of mid-infrared-optimized SNSPDs result in very low bias currents on the order of $1\mu\text{A}$.⁹ This makes multiplexing schemes such as TCI more challenging, primarily because this scheme relies on thermal coupling between the SNSPDs and a superconducting transmission line bus while the amount of heating scales quadratically with the SNSPD bias current. With typical near-infrared SNSPDs having bias currents on the order of $10\mu\text{A}$, mid-infrared-optimized SNSPDs will generate only 1% the amount of Joule heating compared to near-infrared SNSPDs when a photon is detected, which may not be enough to thermally trigger the bus. Thus, the TCI multiplexing scheme must be significantly modified to develop SNSPD arrays that can be used as mid-infrared cameras in telescopes for astronomy or physical chemistry.

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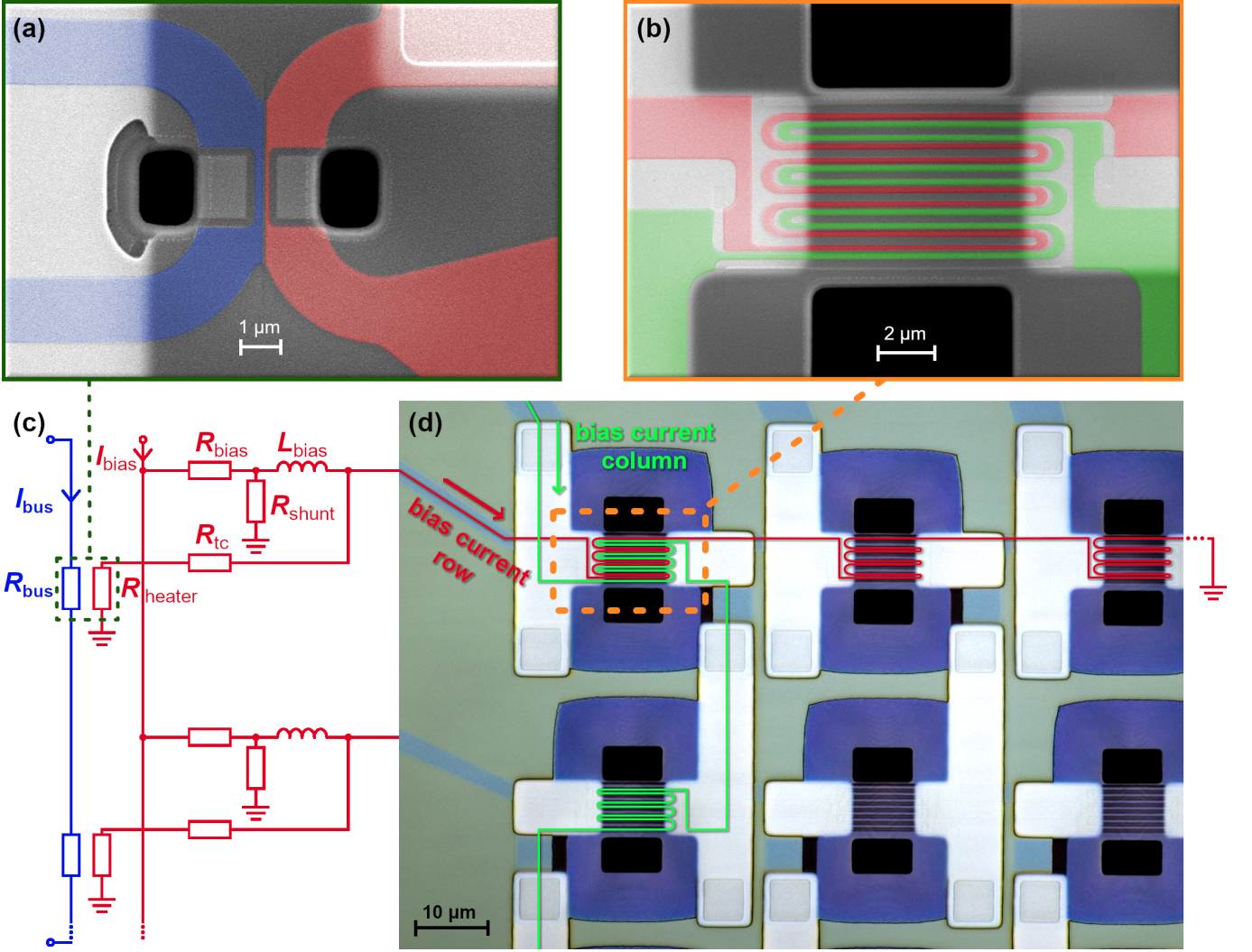


FIG. 1: Micrographs of a 64-pixel mid-infrared-optimized SNSPD array. (a) Scanning electron micrograph (SEM) of the thermal coupling from the SNSPDs (false colored in red) to the transmission line (false colored in blue). (b) SEM of a single pixel with interleaved row (false colored in red) and column (false colored in green) SNSPDs. (c) Circuit diagram of the bias current network that connects to the SNSPD array (red) and is thermally coupled to the transmission line (blue). (d) Photomicrograph of six SNSPD pixels with highlighted current paths for one column (green) and one row (red).

The SNSPD array that is presented in this work was designed combining the thermally-coupled row-column multiplexing architecture¹⁷ with a thermally-coupled imager architecture.^{18,19} The details of the design are depicted in Fig. 1. Each pixel consists of two interleaved SNSPDs, which are false colored in red and green in the scanning electron micrograph (SEM) in Fig. 1(b) highlighting the co-wound row and column SNSPD, respectively. All SNSPDs in a single row (red) or column (green) are wired in series as shown in Fig. 1(d). All rows are current-biased in parallel supplied through a single cable as depicted in red in Fig. 1(c). The current I_{bias} is divided into all the rows of the full array and biases the row SNSPDs through the bias resistors R_{bias} and the large bias inductors L_{bias} . Note that only the row bias and readout is shown in Fig. 1 for clarity. This

bias and readout network is also present for the columns but is not shown.

When a photon is absorbed in one of the co-wound nanowires (either row (red) or column (green)), it generates a resistive hotspot. The Joule heating resulting from the bias current flowing through the resistive domain of the nanowire generates phonons which are absorbed by the neighboring interleaved nanowire, resulting in the formation of a hotspot in that nanowire as well. Thus, a “click” in a row (or column) nanowire always results in a corresponding “click” in the corresponding column (or row) nanowire at that particular pixel. The SNSPDs are patterned on silicon dioxide (SiO_2) membranes to enhance this thermal coupling. The breakdown of superconductivity in the SNSPDs results in a sudden change of resistance for the bias current which happens over a

timescale of several picoseconds. The resulting current pulse is redirected out of the SNSPD into the circuit shown in red in Fig. 1(c). Since the impedance of the bias inductor L_{bias} is very large over the timescale of the pulse, the current pulse is primarily driven into the path of the thermal coupling resistance R_{tc} and a heater resistance R_{heater} . The heater resistance is a constriction in a superconducting strip, which can be seen false colored in red in the SEM micrograph in Fig. 1(a). The redirected bias current will exceed the critical current density in this constriction, superconductivity will break down, and the finite resistance will cause a localized heating in the constriction area. The phonons generated by the Joule heating are then relayed to an adjacent constriction in the superconducting transmission line bus seen false colored in blue in the SEM micrograph in Fig. 1(a). The large series inductance L_{bias} ensures that sufficient energy is dissipated in the constriction to trigger a detection event on the transmission line bus, as this energy scales linearly with L_{bias} and quadratically with the bias current. The thermal coupling resistance R_{tc} sets the electrical recovery time of the bias current back into the SNSPDs after a detection event and is used to prevent latching of the SNSPDs. Finally, the shunt resistor R_{shunt} provides a path to ground for any leakage current through the large inductor L_{bias} . This helps to mitigate redistribution of bias current to other rows or columns on the same bias line during a detection event.

The readout busses for the row and column SNSPDs are designed as superconducting microstrip transmission lines and are current-biased with I_{bus} . Their impedance is transformed to match the cable impedance of $50\ \Omega$ by using Hecken tapers²⁰ at each end of the line. The sudden increase in resistance which occurs when a constriction in the transmission line switches to the normal state results in a positive voltage pulse traveling to one end of the transmission line and a negative voltage pulse traveling to the other. These voltage pulses can be amplified and subsequently measured with a time tagger. The time difference between the positive and negative voltage pulse is finally used to determine which row or column had a detection event. The combination of the results for the row and the column transmission lines allows one to infer which pixel absorbed a photon. This design enables readout of the whole array with only six cables independent of array size. Two cables are used to bias the row and column SNSPDs, two cables for the row transmission line, and two cables for the column transmission line.

A 64-pixel SNSPD array optimized for the mid-infrared was fabricated in the NIST Boulder Microfabrication Facility using the design described above. Further fabrication details are available in the Supplementary Material.

The cryogenic measurements were performed in an adiabatic demagnetization refrigerator (ADR) at a temperature of 350 mK. The connection to the SNSPD bias network and the transmission lines was made by coaxial cables, which were connected to six bias-tees at room temperature outside the cryostat. The row and column

SNSPDs were biased separately through the dc ports of two bias-tees. Each of the transmission lines was also biased through the dc port of one of the connected bias-tees, while the dc port of the bias-tee at the other side of the transmission line was terminated with a $50\ \Omega$ resistor. The ac ports of the transmission line bias-tees were amplified with two amplifiers with a total gain of 44 dB and can be connected to either a counter or a time tagger.

The 64-pixel array was flood-illuminated with a broadband thermal light source, which was filtered by an exchangeable narrow bandpass mid-infrared filter. A calcium fluoride (CaF_2) window acted as a $10\ \mu\text{m}$ -lowpass filter to further attenuate any long wavelength blackbody radiation that is not perfectly blocked by the narrow-band filter. The thermal light source was mounted at the 4 K-stage of the cryostat. An additional copper aperture with a diameter of approximately 3 mm was mounted in front of the filter stack and thermal source without making direct contact to the thermal source assembly. This shielding was also mounted at the 4 K-stage and further reduced unwanted blackbody radiation from the thermal source irradiating the array. The array chip was mounted in a gold-coated copper housing with another aperture of 2.5 mm diameter. There, the chip was bonded to connectors for the coaxial cables.

The characterization of the photon count rate (PCR) as a function of bias current through the SNSPDs for different narrow bandpass filters is depicted in Fig. 2. The transmission lines were biased with a current of $2.8\ \mu\text{A}$ for all measurements as the count rates on both transmission lines were in a saturated regime at this value when the rows and columns were also biased. The bias currents for the SNSPDs were varied for different measurements. The thermal light source was pulsed with a function generator at a repetition rate of 1 kHz and duty cycle of 50 %. Its power was adjusted for each of the narrow bandpass filters so that a PCR of 100 kcps (counts per second) could be measured for the row SNSPDs biased at $30\ \mu\text{A}$, while the column SNSPDs were not biased. All mentioned bias currents must be divided by a factor of 8 to obtain the bias current through a single SNSPD as all row and all column SNSPDs were biased in parallel through two bias cables as explained above.

Each measurement started with a characterization of the dark count rate (DCR). For this, the thermal light source was switched off, the bias current was ramped up, and the count rates were measured with a counter at one end of the corresponding transmission line for 1 s per bias current value. The results are shown under each of the measurements on a logarithmic scale in Fig. 2. A large part of the observed DCR can be attributed to unwanted blackbody radiation of the measurement setup. The measured DCR values were subtracted from the PCR values in all measurements.

In Fig. 2(a), the bias current for the row SNSPDs is varied, while the column SNSPDs are not biased at all. The PCR curve for the $3.4\ \mu\text{m}$ narrow bandpass filter forms a plateau regime with only a small slope between

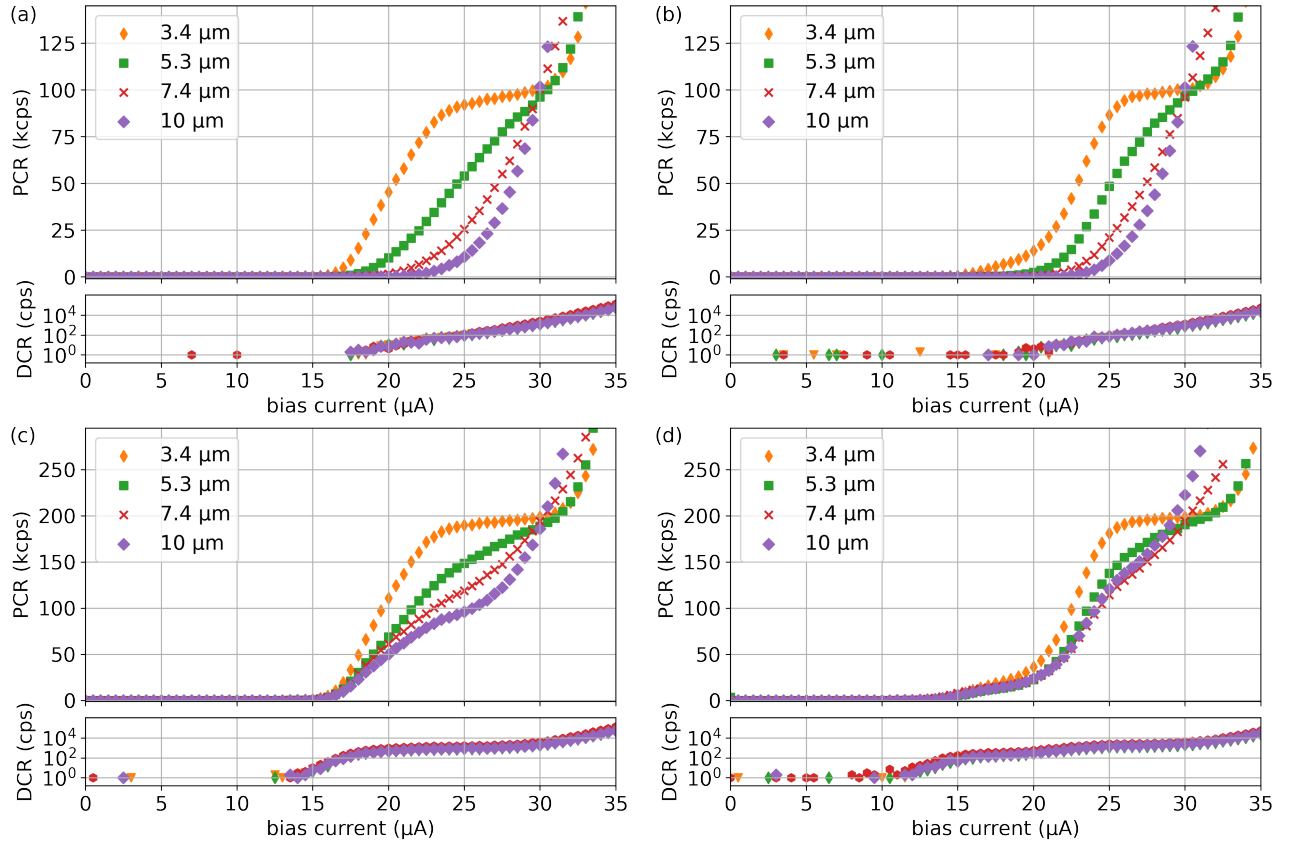


FIG. 2: Photon count rate (PCR) and dark count rate (DCR) in counts per second (cps). Panels (a) and (c) show measurements on the row transmission line with (a) row bias varied, columns unbiased and (c) row bias varied, columns biased at 30 μA . Panels (b) and (d) show measurements on the column transmission line with (b) column bias varied, rows unbiased and (d) column bias varied, rows biased at 30 μA .

23 μA and 31 μA . Such a plateau is very desirable as it is usually interpreted as a regime of saturated internal detection efficiency of the SNSPDs. The slope in the plateau regime indicates the detection of unwanted black-body radiation from the thermal light source assembly that is not filtered and reaches the array due to reflections inside the cryostat. The 5.3 μm PCR curve has no pronounced plateau, but the curve has a distinct inflection point which indicates that the SNSPDs almost reach the regime of saturated internal detection efficiency. This is not true for the 7.4 μm and the 10 μm curve. This makes the detection efficiency of the array very prone to bias current fluctuations and indicates a reduced internal detection efficiency at these wavelengths, but the array can still be operated at these wavelengths. A very similar behavior can be observed in Fig. 2(b), where only the column SNSPDs are biased and the count rates are measured at the column transmission line.

The measurement in Fig. 2(c) is similar to Fig. 2(a), but the column SNSPDs are biased with a fixed current of 30 μA in addition to the varying bias current in the row SNSPDs. Fig. 2(d) can finally be compared to Fig. 2(b) but biasing the row SNSPDs at 30 μA in addition to the varying bias current in the column SNSPDs. The PCR

doubles for both of these measurement settings compared to the measurements in Fig. 2(a) and (b), where only one of the two nanowire meanders in each pixel was biased (either row or column meander but not both). This behavior is expected as each detection event on either a row or the column nanowire leads also to a detection event on the co-wound nanowire due to thermal coupling. The fact that the PCR doubles is therefore a good indication that the thermal coupling works as intended. The elevated DCR in the measurement settings of Fig. 2(c) and (d) is due to the fact that either the row or column SNSPDs are biased at 30 μA with a correspondingly high DCR that translates to a higher DCR due to thermal coupling between the co-wound SNSPDs.

Measurements with a time tagger are shown in Fig. 3. Both the row and column SNSPDs are biased with a current of 30 μA , which corresponds to a bias current of 3.75 μA for each SNSPD, while both transmission lines are biased at 2.8 μA . The thermal light source was adjusted as described above to attain a PCR of 100 keps for each wavelength. Both ends of both transmission lines are connected to the time tagger, which is configured to generate time stamps on a positive or negative voltage pulse, depending on the side of the transmission line.

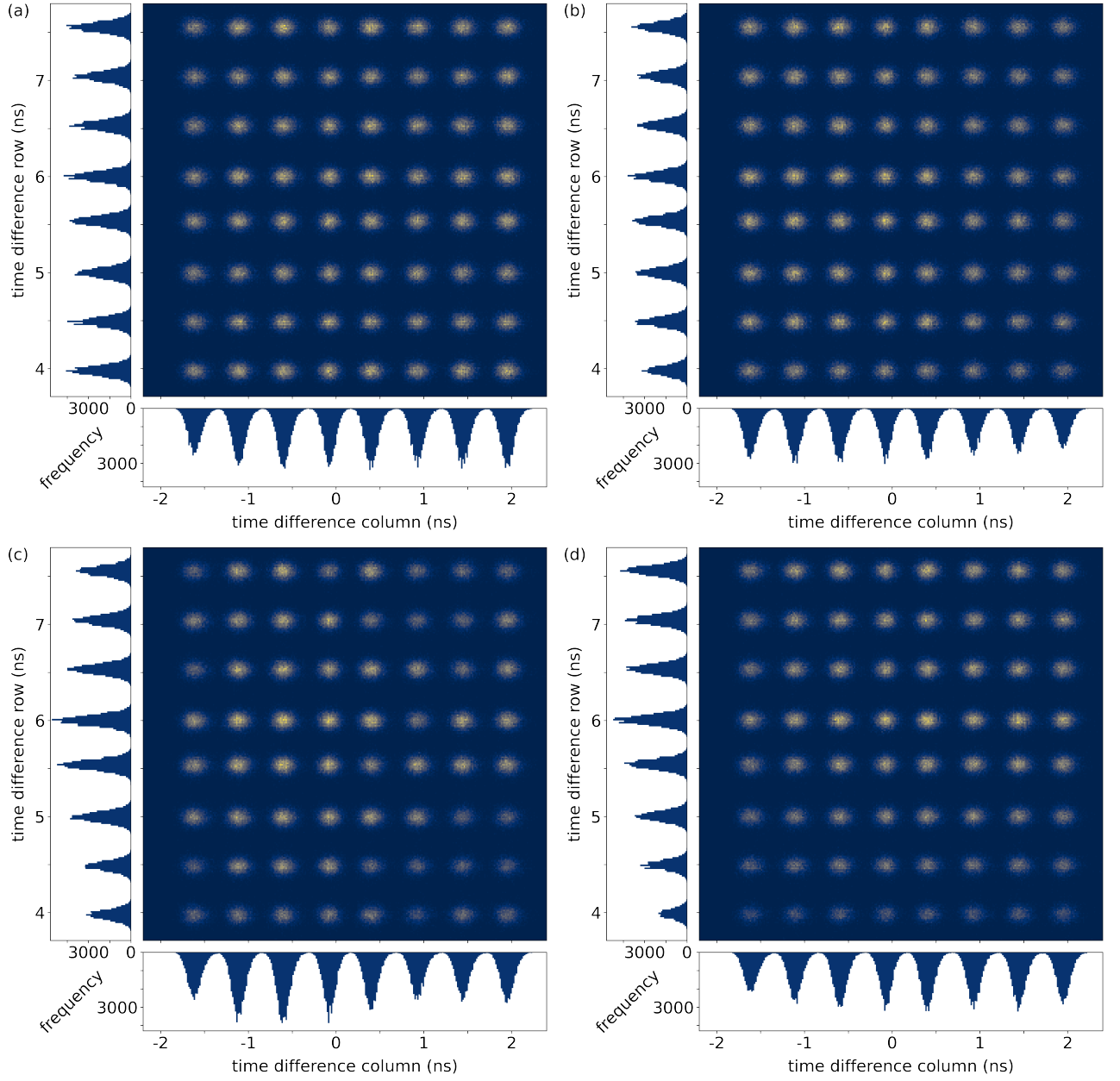


FIG. 3: Histograms of a 1s time tagger measurement at the row and column transmission line for the thermal light source with narrow bandwidth filters at (a) $3.4\text{ }\mu\text{m}$, (b) $5.3\text{ }\mu\text{m}$, (c) $7.4\text{ }\mu\text{m}$, and (d) $10\text{ }\mu\text{m}$.

Each detection event will therefore generate four time stamps that can be correlated in post processing. The measurement time for each dataset for the four different narrow bandpass filters was 1 s.

The dataset was used in post processing to calculate time differences between two correlated time stamps of the row and two of the column. This time difference is a measure for the position on the transmission line where the positive and negative voltage pulses were generated and therefore for the row or column that is responsible for the detection event. The calculated time differences for the rows and the columns are visualized

as histograms in Fig. 3. The mean full width at half maximum (FWHM) of all histogram peaks is 122 ps for the rows and 167 ps for the columns while all peaks are well separated by more than 0.5 ns. The two histograms for the rows and the columns are then combined to form a color-coded 2D-histogram that shows all 64 pixels. The array performs well for all tested mid-infrared wavelengths, but it can be seen that the distribution of detection events becomes inhomogeneous for longer wavelengths. This effect can be explained with the results presented in Fig. 2 where the SNSPDs did not reach saturated internal detection efficiency for

longer wavelength photons, which made their detection efficiency prone to small nonuniformities in bias current across the array. Small resistance differences in the bias network of the SNSPD array could therefore affect the detection efficiency of whole rows and columns. This effect can significantly be reduced by optimizing the SNSPDs to achieve saturated internal detection efficiency at the target wavelength. The maximum count rate of the array will be limited by the maximum count rate of the transmission lines. The count rate of the transmission line remained linear as a function of the input photon flux up to 2.5 Mcps, with a 3 dB compression point of 6.5 Mcps.

In summary, we have presented the design, fabrication, and measurement of a 64-pixel mid-infrared-optimized SNSPD array in this work. The performance of the array was tested at wavelengths up to 10 μm and shows very promising results towards a large-scale mid-infrared camera. The fill factor of 5 % could be further improved through reduction in size of the vias (interconnects between top and bottom layers) and wiring in the array active area. The membrane release could also be performed using a back side etch, eliminating the need for opening windows in the active area. Future work will also focus on optimization of system detection efficiency of single pixels, which will then need to be integrated into the array fabrication process. Simulations of both dielectric optical stacks and bow-tie antennas have shown that it should be possible to obtain greater than 50 % detection efficiency over a 1 μm bandwidth for a single pixel. A cryostat is also currently being constructed for calibrated efficiency measurements at mid-infrared wavelengths for both single pixels and arrays.

See the Supplementary Material for further details on the fabrication process of the SNSPD array.

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AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

Benedikt Hampel: Conceptualization (equal); Data curation (lead); Formal analysis (lead); Investigation (lead); Methodology (equal); Software (lead); Validation (equal); Visualization (lead); Writing - original draft (lead); Writing - review & editing (equal). **Richard P.**

Mirin: Funding acquisition (equal); Project administration (equal); Resources (supporting); Supervision (supporting); Writing - review & editing (equal). **Sae Woo Nam:** Funding acquisition (equal); Project administration (equal); Resources (supporting); Supervision (supporting); Writing - review & editing (equal). **Varun B. Verma:** Conceptualization (equal); Formal analysis (supporting); Funding acquisition (equal); Investigation (supporting); Methodology (equal); Project administration (equal); Resources (lead); Software (supporting); Supervision (lead); Validation (equal); Writing - original draft (supporting); Writing - review & editing (equal).

DATA AVAILABILITY

The data that support the findings of this study are openly available in the NIST Science Data Portal at <https://doi.org/XXXXXXXXXXXX>.

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SUPPLEMENTARY MATERIAL

I. FABRICATION

Fabrication was performed on a 3-inch oxidized silicon (Si) wafer with a silicon dioxide (SiO_2) thickness of 150 nm. A 5 nm-thick layer of tungsten silicide ($\text{W}_{0.5}\text{Si}_{0.5}$) was deposited by dc magnetron co-sputtering from separate tungsten (W) and Si targets at room temperature, followed by a 2 nm-thick layer of amorphous silicon. The amorphous silicon layer protects and prevents oxidation of the WSi layer during subsequent processing steps. Electron beam lithography was then performed using polymethyl methacrylate (PMMA) resist to define the co-wound SNSPD pixels and the heater and transmission line bus constrictions. The patterns were transferred into the underlying WSi superconductor layer using reactive ion etching (RIE) in an sulfur hexafluoride (SF_6) plasma. The SNSPDs consist of nanowires which are 180 nm wide. The co-wound row nanowire and column nanowire in each pixel are separated by a 100 nm-gap. Each pixel covers an area of $10\text{ }\mu\text{m} \times 5\text{ }\mu\text{m}$. Adjacent pixels are $30\text{ }\mu\text{m}$ apart, yielding a fill factor of 5 %. The superconducting constriction corresponding to R_{heater} is 60 nm-wide. This constriction must be significantly smaller than the nanowire width used to form the SNSPD to ensure that it switches to the normal state when the SNSPD bias current is diverted to it. R_{heater} is separated from the superconducting constriction corresponding to R_{bus} by a 100 nm-wide gap. The width of the constriction on the bus is 180 nm.

Larger areas of WSi such as the transmission line bus, inductors, and wiring between components were patterned using optical lithography and also etched by RIE in an SF_6 plasma. The inductor L_{bias} consists of a 2 μm -wide meandering wire with a total length of 36 mm. With a kinetic inductance for WSi of $550\text{ pH}/\square$, this yields an inductance of $L_{\text{bias}} = 10\text{ }\mu\text{H}$. Resistors were patterned by optical lithography and liftoff of a metal stack consisting of 2 nm titanium (Ti) and 25 nm palladium gold (PdAu). A 30 second argon (Ar) clean was performed before deposition of the resistor metals to remove residual photoresist and remove the amorphous silicon cap layer covering the WSi in the regions where the WSi makes contact to the resistors. The resistor geometries were chosen to provide for the following resistance values: $R_{\text{bias}} = 130\text{ }\Omega$, $R_{\text{shunt}} = 25\text{ }\Omega$, and $R_{\text{tc}} = 25\text{ }\Omega$. After deposition and liftoff of the resistor layer, another photolithography and metal liftoff step was performed to define metal pads serving as interconnects between the

WSi in the bottom layer, and the top wiring layers and ground plane which will later be deposited on top of a layer of 50 nm of SiO_2 which covers the WSi layer. These pads also serve as an etch stop for the vias when etching through the top SiO_2 layer. The metal stack for the pads consists of 2 nm Ti, 50 nm niobium (Nb), 10 nm PdAu, and 2 nm Ti. The wafer is then cleaned in an oxygen (O_2) plasma for 1 minute at 60 W. Next, a 50 nm-thick layer of SiO_2 is deposited by radio frequency (rf) sputtering. The thickness of this layer is important as it sets the capacitance between the superconducting microstrip transmission line and the ground plane, which along with the kinetic inductance of the WSi strip forming the transmission line determines the propagation velocity of the electrical pulses along the transmission line. With a kinetic inductance for WSi of $550\text{ pH}/\square$, the propagation velocity is only 0.5 % the speed of light in free space. This slow propagation velocity combined with the small jitter of SNSPDs allows pulses from adjacent rows and columns to be easily discriminated.

After deposition of the SiO_2 , vias were defined by photolithography and the SiO_2 was etched down to the underlying metal pads using a reactive ion etch in trifluoromethane (CHF_3) and O_2 . This was followed by another photolithography and liftoff step to define the bond pads, ground plane, and interconnects for the column bias current in the SNSPD array as shown in Fig. 1(d). This layer consists of 100 nm of Nb. The last step of the process is the suspension of the SiO_2 membranes underneath the SNSPD pixels and the coupling region between the heater constriction and bus constriction. Two rectangular windows are patterned using optical lithography above and below each SNSPD pixel in the array shown in Fig. 1(d), as well as above and below the coupling regions between heater and bus constrictions shown in Fig. 1(a). An inductively coupled plasma reactive ion etch using tetrafluoromethane (CF_4), Ar, and O_2 is used to etch through the 200 nm of SiO_2 down to the silicon substrate. The photoresist is not removed after this etch because the same pattern will be used in the undercut of the silicon substrate. The substrate is then diced into 1 cm chips, and a second reactive ion etch is performed for 4 minutes in an SF_6 plasma at a high pressure of 200 mTorr and low rf power to provide for an isotropic etch of the silicon substrate underneath the SiO_2 . The photoresist is removed in an O_2 plasma at 100 W for 8 minutes.