

# Message-Observing Sessions\*

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We present *MOST*, a process language with message-observing session types. Message-observing session types extend binary session types with type-level computation to specify communication protocols that vary based on messages observed on other channels. Hence, *MOST* allows us to express global invariants about processes, rather than just local invariants, in a bottom-up, compositional way. We give *MOST* a semantic foundation using *traces with binding*, a semantic approach for compositionally reasoning about traces in the presence of name generation. We use this semantics to prove type soundness and compositionality for *MOST* processes. We see this as a significant step towards capturing message-dependencies and providing more precise guarantees about processes.

CCS Concepts: • **Theory of computation** → **Program specifications**; *Type structures*; Denotational semantics; • **Computing methodologies** → *Concurrent programming languages*.

Additional Key Words and Phrases: session types, program specification, trace semantics, dependent types

## 1 INTRODUCTION

Session types [Honda 1993; Honda, Vasconcelos, et al. 1998; Takeuchi et al. 1994] allow us to specify and statically verify that processes communicate according to prescribed protocols. Hence, they rule out a wide class of communication-related bugs before executing a given protocol.

A *binary* session type specifies the communication protocol as seen from the point of view of one of the two participants. From a Curry-Howard perspective, it corresponds to the standard sequent calculus proof system for dual intuitionistic linear logic (see [Caires and Pfenning 2010; Caires, Pfenning, and Toninho 2016]), thereby building a logical foundation for specifying and reasoning about concurrent communications. Over the past decade, there have been different approaches to extend it and capture ever richer protocols: value-dependent session types [Toninho, Caires, et al. 2011; Toninho and Yoshida 2018] allow protocols to vary based on previously transmitted values; label-dependent session types [Thiemann and Vasconcelos 2019] describe different communication behaviour depending on labels being observed on a given channel; manifest sharing [Balzer and Pfenning 2017] allows (binary) session types to capture shared communication channels.

In this paper, we describe *MOST*, a language for protocols using *message-observing session types*. A message-observing session type specifies how communication evolves taking into account messages that can be observed on other channels in a process’s environment. This is in contrast to both value and label-dependent session types, where a channel’s type can only depend on messages that the channel previously carried, but not on messages on other channels in the process’s environment. Hence, *MOST* allows us to express global invariants about processes rather than just local invariants.

To specify session types that vary based on messages on other channels, we extend binary session types with *type-level processes*. This allows us to capture a wider range of correctness guarantees than is currently possible. To illustrate, we take a closer look at the identity process  $\text{id}$  that provides an implementation for the session type  $A := \oplus\{\text{left} : 1, \text{right} : 1\}$ . This session type specifies that

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communication is a message containing either label *left* or *right*, followed by a message signalling the end of communication. We can implement *id* between channels *a* and *b* of type *A* as follows:

$$\text{case } a \{ \text{left} \Rightarrow b.\text{left}; \text{wait } a; \text{close } b \mid \text{right} \Rightarrow b.\text{right}; \text{wait } a; \text{close } b \}.$$

Operationally, this process waits to receive a label on *a*, selects the corresponding branch, and sends the label over *b*. It then waits for the channel *a* to close before closing *b*. However, the session type for channel *a* and channel *b* does not capture this precisely. In particular, the type of *b* does not rule out the following erroneous implementation that swaps labels:

$$\text{case } a \{ \text{left} \Rightarrow b.\text{right}; \text{wait } a; \text{close } b \mid \text{right} \Rightarrow b.\text{left}; \text{wait } a; \text{close } b \}$$

The crux of the issue is that the messages we wish to allow on *b* depend on the messages that we observe on *a*. In *MOST*, we specify such a protocol for *b* using type-level processes as follows:

$$b : \text{CASE } a \{ \text{left} \Rightarrow \oplus\{\text{left} : 1\} \mid \text{right} \Rightarrow \oplus\{\text{right} : 1\} \}.$$

Intuitively, it specifies that the type of *b* is the unary choice  $\oplus\{\text{left} : 1\}$  if a label *left* is observed on *a*, and symmetrically if *right* is observed on *a*. A message-observing session type is necessarily open: it names the channel whose messages it observes. This is analogous to processes, which contain the channel names on which they communicate. The particular channel *a* observed by the type of *b* is determined by the process specification in which it appears. For example, the following concrete syntax specifies *id* by specifying the types of its channels, and it specifies that *b*'s type observes a channel *a* : *A* used by *id*:

$$\text{id} :: \{ \} [a : A] (b : \text{CASE } a \{ \text{left} \Rightarrow \oplus\{\text{left} : 1\} \mid \text{right} \Rightarrow \oplus\{\text{right} : 1\} \}) = \text{case } a \{ \dots \}.$$

We will revisit this concrete syntax in section 2.

*MOST* provides a “bottom-up” approach to specifying processes and their composition. We still specify the communication protocol from the point of view of one of the two participants, but take into account the messages observed on other channels in the environment. An alternative to specify multi-channel communication patterns are *multiparty* session types. Multiparty session types [Honda, Yoshida, et al. 2016] specify interactions between a static number participants using a *global type*, and project these specifications onto individual participants for typechecking. Despite recent work [Deniérou and Yoshida 2011; Stolze et al. 2023], multiparty session types are an inherently closed world or “top-down” approach, where the entire system must be designed before it can be implemented. In contrast, we can compose protocols in *MOST* from the bottom-up, while still being able to capture some of the richer interactions of multiparty session types.

Concretely, we make the following contributions:

- (1) We introduce a **message-observing binary session type system**, called *MOST*. A message-observing session type specifies how communication evolves taking into account communications that can be observed on other channels in our environment. This is achieved using **type-level processes** in session types. These type-level processes further restrict the eligible processes and their actions. We motivate this extension through a sequence of examples.
- (2) We give processes and their specifications a **semantics using traces with binding**, a semantic foundation inspired by nominal sequences [Gabbay and Ghica 2012]. A frequent challenge when we define a trace semantics or ordered semantics for concurrency is that we must account for fresh channel name generation and propagate names involved in higher-order communications. Further, compositionality often requires infinite trace sets or infinite collections of semantic objects, which complicates implementation and obscures the computational content. Instead, we use name binding to quantify over all fresh channel

in a manner reminiscent of higher-order abstract syntax, resulting in a more compact presentation. This abstraction allows us to retain compositionality.

- (3) We give a **semantically sound typechecking algorithm**. Process denotations characterize all possible communication behaviours, while specifications specify the processes traces they allow subject to constraints on ambient communications. Soundness ensures that well-typed processes only exhibit behaviours (traces) permitted by their specifications.

We see this work as a significant step towards providing more precise guarantees about processes.

## 2 MOTIVATION

We introduce the main ideas of MOST through a sequence of examples. This will allow us to showcase both the power of message-observing session types and the design decisions.

### 2.1 A Quick Guide to Session Types and Specifying Processes

Session types  $A, B, \dots$  specify the communication protocols on named channels  $a, b, \dots$ . Processes in our system are organized according to a client-server architecture. This architecture is reflected in our concrete syntax for process specifications:

$$\text{proc} :: \{a_1 : A_1, \dots, a_n : A_n\} [b_1 : B_1, \dots, b_n : B_n] (c_0 : C_0)$$

It states that the process  $\text{proc}$  is a **server** for (or **provides**) a distinguished service  $C_0$  over a channel  $c_0$ . Dually, it is a **client** of (or **uses**) zero or more services  $B_1, \dots, B_n$  on channels named  $b_1, \dots, b_n$ , respectively. Finally, its types may refer to zero or more ambient communication channels  $a_i : A_i$ . Ambient channels are instantiated either by composing  $\text{proc}$  with processes that use or provide them, or by defining  $\text{proc}$  as a composition of processes that communicate on these channels. Ambient channels will permit compositional process specifications. To keep our subsequent examples simple, most will feature empty internal and ambient contexts. Each type can refer other channel names in the specification, allowing for mutual dependency between types.

For our first examples, we consider sending a sequence of  $n$  bits on a channel  $a$ . This is accomplished by sending a sequence of  $n$  labels drawn from set  $\text{Bit} = \{0, 1\}$ . Such a sequence is specified by the session type  $\text{Bits}_n$  defined by induction on  $n$  using the following pair of equations:<sup>1</sup>

$$\text{Bits}_0 = 1 \qquad \text{Bits}_{n+1} = \oplus \{ l : \text{Bits}_n \}_{l \in \text{Bit}}.$$

The empty sequence of bits is captured by the unit type 1: it signals the end of communication on the channel. To define a bit sequence of length  $n + 1$ , we specify the possible labels (elements) that can be transmitted followed by the sequence of length  $n$ . To accomplish this, we use the internal choice type  $\oplus \{ l : \text{Bits}_n \}_{l \in \text{Bit}}$ .<sup>2</sup> Concretely, we can use the definition to generate the type for bit sequences of length 2 as follows:

$$\text{Bits}_2 = \oplus \{ \emptyset : \oplus \{ \emptyset : 1, 1 : 1 \}, 1 : \oplus \{ \emptyset : 1, 1 : 1 \} \}$$

It specifies a sequence of two bits, i.e., a sequence of two labels, 0 and 1 followed by termination.

We can then specify a family of identity processes,  $\text{id}_n$ , that use a channel  $a : \text{Bits}_n$  and provide an output channel  $b$  of the same type by the following pair of equations:

$$\begin{aligned} \text{id}_0 &:: \{ \} [a : \text{Bits}_0] (b : \text{Bits}_0) = \text{wait } a; \text{close } b \\ \text{id}_{n+1} &:: \{ \} [a : \text{Bits}_{n+1}] (b : \text{Bits}_{n+1}) = \text{case } a \{ l \Rightarrow b.l; \text{id}_n \}_{l \in \text{Bit}} \end{aligned}$$

<sup>1</sup>MOST omits language-level recursion and relies on meta-level induction to define inductive types. This simplifies MOST's presentation and avoids obscuring its key contributions. We nevertheless sketch how to extend MOST with recursive session types and processes in section 8.4.

<sup>2</sup>An internal choice  $\oplus \{ l : A_l \}_{l \in L}$  specifies that a label  $l \in L$  will be transmitted, and then communication will satisfy type  $A_l$ .

In the base case,  $\text{id}_0$ , we wait to receive a close message on  $a$  that signals the end of communication, and we immediately send it on  $b$  before terminating. In the recursive step,  $\text{id}_{n+1}$ , we perform a case analysis on the label received over  $a$ . If we receive a label  $l$ , then we take the corresponding branch and send the label  $l$  over  $b$ . At this point,  $a$  and  $b$  both have type  $\text{Bits}_n$ , so we continue as  $\text{id}_n$ .

## 2.2 Specifying Identity Processes For Real Using Message-Observing Session Types

The session type specification of the identity process  $\text{id}_n$  in the previous section fails to ensure that we copy the sequence of bits unchanged from  $a$  to  $b$ . In **MOST**, we can give a more precise type to the output channel by taking into account the message that we already have received on a given channel using message-observing sessions. In particular, we can refine the type of  $b$  to the following more precise family of types  $\text{IdBitSeq}_n a$  that observe messages on channel  $a$ . We give the definition of the identity process next to it, to highlight the close correspondence between the process and the message-observing session type:

Process	Message-Observing Session Type
$\text{id}_0 = \text{wait } a; \text{close } b$	$\text{IdBitSeq}_0 a = \text{CASE } a \{ \text{close} \Rightarrow 1 \}$
$\text{id}_{n+1} = \text{case } a \{ l \Rightarrow b.l; \text{id}_n \}_{l \in \text{Bit}}$	$\text{IdBitSeq}_{n+1} a = \text{CASE } a \{ l \Rightarrow \oplus \{ l : \text{IdBitSeq}_n a \} \}_{l \in \text{Bit}}$

In the base case, the type  $\text{CASE } a \{ \text{close} \Rightarrow 1 \}$  specifies that we can only signal termination on  $b$  after we have observed termination on  $a$ . In the recursive step,  $\text{IdBitSeq}_{n+1} a$  performs a case analysis on the label sent on  $a$ . If a label  $l$  is sent or received on  $a$ , then the type reduces to the unary internal choice  $\oplus \{ l : \text{IdBitSeq}_n a \}$  that only allows the label  $l$  to be sent, and continues as  $\text{IdBitSeq}_n a$ . We can then refine the specification of  $\text{id}$  to use this more precise protocol:

$$\begin{aligned} \text{id}_0 &:: \{ \} [a : \text{Bits}_0] (b : \text{IdBitSeq}_0 a) = \text{wait } a; \text{close } b \\ \text{id}_{n+1} &:: \{ \} [a : \text{Bits}_{n+1}] (b : \text{IdBitSeq}_{n+1} a) = \text{case } a \{ l \Rightarrow b.l; \text{id}_n \}_{l \in \text{Bit}} \end{aligned}$$

Note that the process and the type of the used channel  $a$  remain the same as in the previous section: the only change is to the type of the channel  $b$ , which now observes messages on  $a$ . At the high-level, the type  $\text{IdBitSeq}_n a$  ensures that  $\text{id}_n$  can only output on  $b$  what it receives on  $a$ .

We call the type  $\text{IdBitSeq}_n a$  a **message observing** because it observes communications on  $a$  to determine the range of permitted communications. It can be thought of as a type-level process, as it mimics choices and behaviour specified in the process  $\text{id}_n$  on the type-level. This type reduces as messages are observed on the channel  $a$ . These observations and reductions exist only in the typechecking algorithm (types are not present at runtime), which tracks messages sent or received by the specified process and reduces types accordingly. For example, to type check  $\text{id}_{n+1}$  against its specification, we check each of its branches “ $b.l; \text{id}_n$ ” (to which it steps after receiving a label  $l$ ) against the specification “ $\{ \} [a : \text{Bits}_n] (b : \oplus \{ l : \text{IdBitSeq}_n a \})$ ”, which is obtained by reducing all types by the label  $l$  received on  $a$ .

This example illustrates two key points in **MOST**: (1) message-observing session types can express dependencies on messages on other channels, and (2) message-observing types behave as type-level processes and incorporate a notion of concurrent computation on the type-level.

On a practical level, this allows us to express more precise safety guarantees that cannot be expressed by prior work. On a more theoretical level, we see message-observing session types as an essential step towards a type theory of session types.

## 2.3 Expressing Mutual Observation in Message-Observing Session Types

Session-typed languages often feature a higher-order session type  $A \otimes B$ , which specifies a server that sends a channel of type  $A$  (call it  $a$ ) and then communicates according to  $B$ .

It is useful in applications for  $B$  to observe messages on  $a$ . To do so, we introduce the syntax  $(a : A) \otimes B$ , which binds a name  $a$  for the transmitted channel of type  $A$  in  $B$ . This bound name is eventually instantiated by the actual name of the transmitted channel. This syntax then lets us specify, e.g., sending a pair of identical bit streams:  $(a : \text{Bits}_n) \otimes (\text{IdBitSeq}_n a)$ . A server of this type sends a channel  $a$  carrying  $n$  bits and then communicating according to the protocol  $\text{IdBitSeq}_n a$ , which specifies transmitting a bit stream equal to the one carried by  $a$ .

Communication on both channels occurs independently, so it is natural to ask whether  $A$  can also observe messages on the channel of type  $B$  (call it  $b$ ). From the dependent-types perspective in the sequential setting, such mutual dependency may seem unusual, but it captures the concurrent behaviour where the processes providing  $A$  and  $B$  run concurrently. To allow for mutual observation, we use the syntax  $(a : A) \otimes (b : B)$  for higher-order sessions, where name  $a$  is bound in  $B$  and name  $b$  is bound in  $A$ . It allows the type  $B$  to observe messages on the transmitted channel  $a : A$ , while also allowing the type  $A$  to observe messages on the channel  $b : B$ . In practice,  $b$  will always be instantiated with the name of the channel of type  $(a : A) \otimes (b : B)$ ; we treat it as bound in the type only to ensure that the type makes sense independently of the channel that it types. As a result, we can specify mutually observing higher-order protocols.

To illustrate mutual observation, we consider a load-balanced list service. This service provides a pair of lists, and then forces the client to alternate between lists when receiving elements. It is given by  $(a : \text{LList}_n b) \otimes (b : \text{LList}_n a)$ , where  $\text{LList}_n c$  is inductively defined by:

$$\text{LList}_0 c = 1 \quad \text{LList}_{n+1} c = \oplus \{ x : \text{CASE } c \{ y \Rightarrow \text{LList}_n c \}_{y \in \text{Bit}} \}_{x \in \text{Bit}}.$$

Intuitively, a channel  $a : \text{LList}_{n+1} c$  provides a list element  $x \in \text{Bit}$ , and then requires the client to observe some element  $y$  on  $c$  before it can observe another list element from  $a$ .

To see how the protocol  $(a : \text{LList}_{n+1} b) \otimes (b : \text{LList}_{n+1} a)$  enforces alternation, consider one of its clients. After receiving the channel provided by this service, the client has two channels: channels  $a : \text{LList}_{n+1} b$  and  $b : \text{LList}_{n+1} a$ . Assume, without loss of generality, that it first receives a label on  $a$ . The internal choice type specifies that communication on  $a$  then satisfies  $\text{CASE } b \{ y \Rightarrow \text{LList}_n b \}_{y \in \text{Bit}}$ . In particular, the client cannot communicate on  $a$  until it reduces the CASE analysis. To do so, the client must receive a label on  $b$ . It can do so, for the type of  $b$  is an internal choice  $\oplus \{ x : \text{LList}_n a \}_{x \in \text{Bit}}$  (it was reduced by the label received on  $a$ ). After receiving a label on  $b$ , the type of  $a$  reduces to  $\text{LList}_n b$ , while the type of  $b$  becomes  $\text{LList}_n a$ .

For convenience, we write  $A \otimes (b : B)$  for  $(a : A) \otimes (b : B)$  when  $a$  does not appear in  $B$ . The abbreviations  $(a : A) \otimes B$  and  $A \otimes B$  are analogous.

## 2.4 Depending on Higher-Order Sessions

Protocols can depend on higher-order sessions using an elimination form reminiscent of positive product elimination in functional languages. We illustrate this by specifying an AND logic gate, given by the process AND:

$$\begin{aligned} \text{AND} :: \{ \} [b : \text{Bits}_1 \otimes \text{Bits}_1] (c : \text{Bits}_1) = \\ a \leftarrow \text{receive } b; \text{ case } a \{ 1 \Rightarrow \text{case } b \{ 1 \Rightarrow c.1; \text{end} \mid 0 \Rightarrow c.0; \text{end} \} \\ \mid 0 \Rightarrow \text{case } b \{ x \Rightarrow c.0; \text{end} \}_{x \in \text{Bit}} \} \end{aligned}$$

where  $\text{end} :: \{ \} [a : 1, b : 1] (g : 1) = (\text{wait } a; \text{wait } b; \text{close } g)$ . From its specification, we see that AND receives a pair of bits on  $b$  and sends a bit on  $c$ . Its implementation starts by receiving a channel of type  $\text{Bits}_1$  over  $b$  and binding it to the name  $a$  (syntax: “ $a \leftarrow \text{receive } b; \dots$ ”). At this point, the process is a client of  $a : \text{Bits}_1$  and  $b : \text{Bits}_1$  and provides  $c : \text{Bits}_1$ . The process then observes the bits on  $a$  and  $b$ , sends their AND over  $c$ , and terminates.

Observe that the above specification does not specify that AND correctly implements an AND gate. However, we can use message observation to specify this functionality at the type level. To do so, we refine the above specification to

$$\text{AND} :: \{ \} [b : \text{Bits}_1 \otimes \text{Bits}_1] (c : \text{andOf } b) = \dots$$

where the following type specifies an AND gate:

$$\begin{aligned} \text{andOf } b = \text{CASE } b \{ \langle a \rangle \Rightarrow \text{CASE } a \{ 1 \Rightarrow \text{CASE } b \{ 1 \Rightarrow \oplus \{ 1 : 1 \} \mid 0 \Rightarrow \oplus \{ 0 : 1 \} \} \\ \mid 0 \Rightarrow \oplus \{ 0 : 1 \} \} \}. \end{aligned}$$

This type-level process uses the new construct  $\text{CASE } b \{ \langle a \rangle \Rightarrow C \}$ , which binds the name of a channel transmitted over  $b$  to  $a$  in  $C$ . This construct reduces to  $[\alpha/a]C$  when a channel  $\alpha$  is transmitted on  $b$ . This reduction closely mimics the behaviour of the channel-receiving process construct,  $a \leftarrow \text{receive } b; P$ , which becomes  $[\alpha/a]P$  after receiving  $\alpha$  on  $b$ . In the case of “andOf  $b$ ”, it reduces to the internal choice  $\oplus \{ 1 : 1 \}$  if the transmitted channel and its carrier both carry a 1 bit, and otherwise it reduces to  $\oplus \{ 0 : 1 \}$ .

## 2.5 A Fair Auction

In our examples so far, there has been a close correspondence between processes and the message-observing types in their specifications. In fact, this correspondence is partially by design as we take into account the process communication in defining a message-observing type. The next example shows that this correspondence between processes and message-observing types is not always trivial, and that processes can be more complex than the types specifying their communications.

Assume two bidders participating in an auction, where each may bid 0 (described by the label 0) or 1 (described by the label 1). We want to ensure that the bidder with the highest bid wins. With traditional session types, we can specify the protocols for a bidder service as follows, where the external choice  $\&\{\dots\}$  means that the bidder *receives* the label:

$$\begin{aligned} \text{bidder} &= \oplus \{ 0 : \text{result}, 1 : \text{result} \} \\ \text{result} &= \&\{ \text{lost} : 1, \text{tie} : 1, \text{win} : 1 \} \end{aligned}$$

A bidder is a process that provides a channel  $b$  of type bidder; the name  $b$  can freely be renamed, provided that it is kept distinct from other names in the process and its specification:

$$\text{Bidder} :: \{ \} [] (b : \text{bidder}) = \dots$$

An Auctioneer is a client of two bidders and signals the end of the auction on its provided channel  $c$ . We can implement it as follows, where  $\text{end} :: \{ \} [b_1 : 1, b_2 : 1] (c : 1) = (\text{wait } b_1; \text{wait } b_2; \text{close } c)$ :

$$\begin{aligned} \text{Auctioneer} :: \{ \} [b_1 : \text{bidder}, b_2 : \text{bidder}] (c : 1) = \\ \text{case } b_1 \{ 0 \Rightarrow \text{case } b_2 \{ 0 \Rightarrow b_1.\text{tie}; b_2.\text{tie}; \text{end} \\ \mid 1 \Rightarrow b_1.\text{lost}; b_2.\text{win}; \text{end} \} \\ \mid 1 \Rightarrow \text{case } b_2 \{ 0 \Rightarrow b_1.\text{win}; b_2.\text{lost}; \text{end} \\ \mid 1 \Rightarrow b_1.\text{tie}; b_2.\text{tie}; \text{end} \} \end{aligned}$$

Observe that the specification does not rule out an unfair auctioneer that privileges bidder  $b_1$  and always give them the win, i.e., an auctioneer where each branch is  $b_1.\text{win}; b_2.\text{lost}; \text{end}$ .

To prohibit unfair auctioneers, we use a message-observing protocol that specifies the interactions for one bidder in terms of the actions of its opponent  $o$ :

$$\begin{aligned} \text{fairAuct } o = & \oplus \{ \emptyset : \text{CASE } o \{ \emptyset \Rightarrow \&\{ \text{tie} : 1 \} \mid 1 \Rightarrow \&\{ \text{lost} : 1 \} \}, \\ & 1 : \text{CASE } o \{ \emptyset \Rightarrow \&\{ \text{win} : 1 \} \mid 1 \Rightarrow \&\{ \text{tie} : 1 \} \} \} \end{aligned}$$

The following specification then ensures that Auctioneer is fair:

$$\text{Auctioneer} :: \{ \} [b_1 : \text{fairAuct } b_2, b_2 : \text{fairAuct } b_1] (c : 1) = \dots$$

Indeed, the fair auction protocol  $\text{fairAuct } b_2$  specifying communications on  $b_1$  ensures that if  $b_1$  sends a label  $\emptyset$ , then the auctioneer can only send  $\text{tie}$  or  $\text{lost}$  to  $b_1$ , and then only after a label  $\emptyset$  or  $1$  has been observed on  $b_2$ , respectively. The case when  $b_1$  bids  $1$  is analogous.

This example illustrates that specifications do not simply lift process implementations to the type level. In particular, processes can specify more complex interactions than their types. This illustration is even more striking when we consider the following implementation of a bidder. It bids  $0$ , and then terminates after having received the result of the auction:

$$\begin{aligned} \text{BidsZero} :: \{ b_2 : \text{fairAuct } b_1 \} [] (b_1 : \text{fairAuct } b_2) = \\ b_1.0; \text{ case } b_1 \{ \text{result} \Rightarrow \text{close } a \}_{\text{result} \in \{ \text{lost}, \text{tie}, \text{win} \}}. \end{aligned}$$

This example illustrates how we track the ambient channel  $b_2 : \text{fairAuct } b_1$  in the specification to ensure that the type of  $b_1$  remains well-scoped. When typechecking  $\text{BidsZero}$  against its specification, our typechecking algorithm generates constraints on messages that can appear on  $b_2$  that are then checked when  $\text{BidsZero}$  is composed with a process implementing  $b_2$ .

The ambient context helps ensure that we know the types of all channels observed by types in process interfaces. As a result, we can ensure that types are well-formed relative to each other. It also means that we can modularly specify processes: we specify the types of local channels, but these can observe channels used by other processes in our execution environment.

## 2.6 Process composition

To illustrate how  $\text{MOST}$  typechecks compositions, we consider the composition of two bit-flipping processes. A bit-flipping process is a client of a bit that provides its negation:

$$\text{neg} :: \{ \} [i : \text{Bits}_1] (o : \text{Bits}_1) = \text{case } i \{ \emptyset \Rightarrow o.1; \text{end} \mid 1 \Rightarrow o.0; \text{end} \}$$

where  $\text{end} :: \{ \} [i : 1] (o : 1) = (\text{wait } i; \text{close } o)$ . We can give  $\text{neg}$  more precise specifications. For example, we can specify that

$$\begin{aligned} \text{neg} :: \{ \} [i : \text{Bits}_1] (o : \text{negBit } i) = \dots \\ \text{or } \text{neg} :: \{ c : \text{Bits}_1 \} [i : \text{negBit } c] (o : \text{ldBitSeq}_1 c) = \dots, \\ \text{where } \text{negBit } a = \text{CASE } a \{ \emptyset \Rightarrow \oplus \{ 1 : 1 \} \mid 1 \Rightarrow \oplus \{ \emptyset : 1 \} \}. \end{aligned}$$

The first specification ensures that  $\text{neg}$  sends the negation of the bit it receives on  $i$  over  $o$ . The second is more interesting: it specifies that if  $\text{neg}$  receives on  $i$  the negation of a bit sent on some ambient channel  $c$ , then it outputs that original bit on  $o$ .

Using these two specifications, we can ensure that flipping a bit twice behaves as the identity. To do so, we compose an implementation of  $\text{neg}$  with itself:

$$\text{doubleNeg} :: \{ c : \text{negBit } i \} [i : \text{Bits}_1] (o : \text{ldBitSeq}_1 i) = (i \leftrightarrow \text{neg} \leftrightarrow c) \parallel_c (c \leftrightarrow \text{neg} \leftrightarrow o).$$

The syntax  $(i \leftrightarrow \text{neg} \leftrightarrow c)$  instantiates the implementation of  $\text{neg}$  with the channel names  $i$  and  $c$ , while the syntax  $P \parallel_c Q$  composes a server  $P$  with its client  $Q$  on  $c$ . The type of the composition

channel  $c$  is determined by the ambient context  $\{c : \text{negBit } i\}$ . The type checking algorithm checks each composed process against the types specified by the outer specification, i.e., that

$$\begin{aligned} \text{neg} &:: \{ \quad \} [i : \text{Bits}_1] (c : \text{negBit } i) = \dots \\ \text{neg} &:: \{i : \text{Bits}_1\} [c : \text{negBit } i] (o : \text{ldBitSeq}_1 i) = \dots \end{aligned}$$

We treat the channel  $i$  as ambient in the second specification (it is not accessible to its process). Typechecking the first process poses no difficulties; checking second process is interesting because it is a client of the channel  $c$  whose type immediately observes the ambient channel  $i$ . To check the second process, we do a case analysis on the messages that could appear on  $i$ , reduce the types of  $c$  and  $o$  accordingly in each case, and check that  $\text{neg}$  is well typed in the reduced specification. For instance, in the case where  $\emptyset$  appears on  $i$ , we check that:

$$\text{neg} :: \{i : 1\} [c : \oplus\{1 : 1\}] (o : \oplus\{\emptyset : 1\}) = \dots$$

As a side effect, we generate a constraint relating the label  $\emptyset$  on  $i$  to the behaviour of  $\text{neg}$  on  $c$  and  $o$ . After checking both processes independently, our algorithm checks that they impose mutually consistent constraints, and that they satisfy each other's constraints where applicable. In this example, the second process produces a constraint that  $\text{neg}$  receives 1 on  $c$  only if  $\emptyset$  appears on  $i$ ; this constraint is clearly satisfied by the first negation process. Assuming that all constraints are satisfied, we conclude that the composition is well-typed.

The types of composition channels can be locally specified using the program syntax  $\nu(a : A) . P$ , which binds a private channel  $a : A$  in  $P$ . This privacy also ensures that  $a$  cannot externally be observed. For example, we can hide the fact that  $\text{doubleNeg}$  is implemented as a composition and ensure that no other processes can observe its composition channel  $c$  by hiding  $c : \text{negBit } i$ :

$$\text{doubleNeg}' :: \{ \} [i : \text{Bits}_1] (o : \text{ldBitSeq}_1 i) = \nu(c : \text{negBit } i) . \text{doubleNeg}.$$

More generally, the syntax  $\nu(a_1 : A_1, \dots, a_n : A_n) . P$  binds channels  $a_1, \dots, a_n$  in  $P$ . To check  $\overrightarrow{\nu a : \vec{A}} . P$  against a specification, we check  $P$  against the same specification extended with the ambient channels  $\vec{a} : \vec{A}$ . For example, checking  $\text{doubleNeg}'$  entails checking the specification  $\text{doubleNeg}$ .

Our treatment of composition departs from many simply session typed systems [Caires and Pfenning 2010; Wadler 2014] that combine parallel composition with hiding, i.e., that define the composition of  $P$  and  $Q$  along  $a$  as  $\nu(a : A) . P \parallel_a Q$ . We distinguish these operations to more flexibly specify process compositions and to ensure that process composition is associative and partially commutative. Indeed, suppose we wished to chain together three processes:

$$\begin{aligned} P_1 &:: \{a_3 : A_3(a_2)\} [a_1 : 1] (a_2 : A_2(a_3)) \\ P_2 &:: \{ \} [a_2 : A_2(a_3)] (a_3 : A_3(a_2)) \\ P_3 &:: \{a_2 : A_2(a_3)\} [a_3 : A_3(a_2)] (a_4 : 1), \end{aligned}$$

where we write  $A_i(a_j)$  to mean  $A_i$  observes  $a_j$ , to form a process  $P :: \{ \} [a_1 : 1] (a_4 : 1)$ . The specification of  $P$  is clearly well-defined. However, if we always combine parallel composition with hiding, then neither of the following composition specifications is well-defined:

$$\begin{aligned} P_{12} &:: \{ \} [a_1 : 1] (a_3 : A_2(a_3)) = \nu(a_2 : A_2(a_3)) . (P_1 \parallel_{a_2} P_2) \\ P_{23} &:: \{ \} [a_2 : A_2(a_3)] (a_4 : 1) = \nu(a_3 : A_3(a_2)) . (P_2 \parallel_{a_3} P_3) \end{aligned}$$

This is because the types  $A_2(a_3)$  and  $A_3(a_2)$  are ill-scoped after hiding the channels  $a_2$  and  $a_3$ . In contrast, if we hide  $a_2$  and  $a_3$  *after* the parallel composition, we can successfully define  $P$  as:

$$P :: \{ \} [a_1 : 1] (a_4 : 1) = \nu(a_2 : A_2(a_3), a_3 : A_3(a_2)) . (P_1 \parallel_{a_2} (P_2 \parallel_{a_3} P_3)).$$

Our examples show how MOST modularly specifies processes while guaranteeing rich invariants.

### 3 A HIGHER-ORDER PROCESS LANGUAGE

Processes  $P$  are generated by the following grammar, where  $a$  and  $b$  range over channel names,  $L$  over sets of choice labels,  $l$  and  $k$  over labels, and  $A$  over types:

Process $P, Q ::=$	$\text{close } a$	End communication on $a$ and terminate
	$\text{wait } a; P$	Wait for communication to end on $a$ ; continue as $P$
	$a.k; P$	Send label $k$ on $a$ ; continue as $P$
	$\text{case } a \{ l \Rightarrow P_l \}_{l \in L}$	Continue as $P_l$ after receiving label $l \in L$ on $a$
	$\text{send } a (b.P); Q$	Send a channel provided by $P$ on $a$ ; continue as $Q$
	$b \leftarrow \text{receive } a; P$	Receive channel $b$ on $a$ ; continue as $P$
	$P \parallel_a Q$	Compose $P$ and $Q$ along channel $a$
	$\nu(a_1 : A_1, \dots, a_n : A_n) . P$	Introduce private channels $a_i$ of type $A_i$ in $P$

The channel name  $b$  is bound in  $P$  in  $b \leftarrow \text{receive } a; P$  and in  $\text{send } a (b.P); Q$ . In  $\text{send } a (b.P); Q$ , it represents the sent channel provided by  $P$ . Intuitively, this process forks  $P$  with  $b$  instantiated by a fresh name, and sends this name over  $a$ . The names  $a_1, \dots, a_n$  are bound in  $P$  in  $\nu(a_1 : A_1, \dots, a_n : A_n) . P$ , and the type annotations exist only to simplify type checking. For the semantics to be reasonable, we require that  $a$  does not appear free in  $P$  in  $\text{wait } a; P$ , that  $P$  and  $Q$  have disjoint sets of free names in  $\text{send } a (b.P); Q$ , and that their free names intersect only in  $a$  in  $P \parallel_a Q$ . As is usual for session-typed processes, we consider *open* processes: the free names in a process name its used and provided channels.

#### 3.1 Trace Semantics

A process denotes a set of traces that describe its possible executions. Traces are sequences of elements  $(s; m)$ , where  $m$  is a message on a channel and  $s$  is a tag indicating if  $m$  was sent or received, or if it appeared on an unhidden internal channel. To model sending and receiving channels (process constructs  $\text{send } a (b.P); Q$  and  $b \leftarrow \text{receive } a; P$ ), we extend traces with a binding structure similar to the coabstraction operator for nominal sequences [Gabbay and Ghica 2012]. To help ensure that our semantics is compositional, we bind  $b$  in the tail of a trace after a channel transmission message. This avoids free name clashes when interleaving traces in the denotation of process composition. Advantageously, our approach keeps the denotations of processes finite, a key property in ensuring that typechecking terminates. Explicitly, traces  $t$ , messages  $m$ , and signs  $s$  are given by the grammar:

Observable Signs $o ::=$	$! \mid ? \mid \sigma$	Messages $m ::=$	$\text{close on } a$	Traces $t ::=$	$\varepsilon$
Signs $s ::=$	$o \mid \zeta$		$\mid \text{label } l \text{ on } a$		$\mid (s; m) :: \vec{a} . t$
			$\mid \text{chan on } a$		

Observable **signs** specify actions performed by processes. They are  $?$  (input),  $!$  (output), and  $\sigma$  (internal synchronization). The constraint sign  $\zeta$  is not used in this section, but will be used in the semantics of message-observing types. It specifies a constraint on an ambient channel, i.e., that a message must be observed in the ambient environment before the trace can continue. **Messages**  $m$  describe possible messages on a given channel  $a$ . The message “close on  $a$ ” means the end of communication, “label  $l$  on  $a$ ” describes sending a label  $l$ , and “chan on  $a$ ” captures sending a channel. We write  $\text{cc}(m)$  for the name of the channel carrying  $m$ ; in the above cases,  $\text{cc}(m) = a$ . Finally, **traces** follow a list-like structure. The empty trace is  $\varepsilon$ . The trace  $(s; m) :: \vec{a} . t$  prefixes a message  $m$  with sign  $s$  onto trace  $t$ , with zero or more names  $\vec{a}$  bound in  $t$ . We identify traces up to

$\alpha$ -equivalence. The **free channel names**  $\text{fc}(t)$  in a trace  $t$  are defined by induction on the structure of  $t$ :  $\text{fc}(\varepsilon) = \emptyset$  and  $\text{fc}(s; m) :: \vec{a}.t = \{\text{cc}(m)\} \cup (\text{fc}(t) \setminus \vec{a})$ .

Processes denote sets of traces, where each trace denotes a possible interleaving of its actions. The denotation  $\llbracket P \rrbracket$  of a process  $P$  is given by induction on its syntax. We explain the semantic clauses and the trace operators they use below:

$$\llbracket \text{close } a \rrbracket = \{ (!; \text{close on } a) :: \varepsilon \} \quad (1)$$

$$\llbracket \text{wait } a; P \rrbracket = (?; \text{close on } a) :: \llbracket P \rrbracket \quad (2)$$

$$\llbracket a.k; P \rrbracket = (!; \text{label } k \text{ on } a) :: \llbracket P \rrbracket \quad (3)$$

$$\llbracket \text{case } a \{ l \Rightarrow P_l \}_{l \in L} \rrbracket = \bigcup_{l \in L} (?; \text{label } l \text{ on } a) :: \llbracket P_l \rrbracket \quad (4)$$

$$\llbracket \text{send } a(b.P); Q \rrbracket = (!; \text{chan on } a) :: b.(\llbracket P \rrbracket \parallel \llbracket Q \rrbracket) \quad (5)$$

$$\llbracket b \leftarrow \text{receive } a; P \rrbracket = (?; \text{chan on } a) :: b. \llbracket P \rrbracket \quad (6)$$

$$\llbracket P \parallel_a Q \rrbracket = \llbracket P \rrbracket \parallel \llbracket Q \rrbracket \quad (7)$$

$$\llbracket \nu(a_1 : A_1, \dots, a_n : A_n). P \rrbracket = \llbracket P \rrbracket \setminus \{a_1, \dots, a_n\} \quad (8)$$

$$\text{where } (s; m) :: \vec{a}.T = \{ (s; m) :: \vec{a}.t \mid t \in T \}$$

$$T_1 \parallel T_2 = \{ t \mid t_1 \in T_1, t_2 \in T_2, t \in (t_1 \parallel t_2) \}$$

$$T \setminus \{a_1, \dots, a_n\} = \{ t \setminus \{a_1, \dots, a_n\} \mid t \in T \}$$

Equation (1) specifies that the only action performed by the process ( $\text{close } a$ ) is sending a close message on  $a$ . Dually, eq. (2) specifies that every execution of the process ( $\text{wait } a; P$ ) receives a close message on  $a$  before continuing as  $P$ .

Equation (3) similarly captures sending a label  $k$  on  $a$ . Equation (4) specifies that after receiving a label  $l$ , the process  $P = (\text{case } a \{ l \Rightarrow P_l \}_{l \in L})$  continues executing as  $P_l$ . The meaning of the process  $P$  is then the union of all the traces for  $P_l$  prefixed by the received label  $l$ .

Equation (5) specifies sending a channel  $b$  provided by  $P$  over  $a$  and then continuing as  $Q$ . Processes  $P$  and  $Q$  execute independently, so the traces that follows sending  $b$  are interleavings of the executions of  $P$  and  $Q$ , i.e., elements of  $\llbracket P \rrbracket \parallel \llbracket Q \rrbracket$ . The interleaving operator  $\parallel$  is defined in section 5.4. We assume without loss of generality that the bound name  $b$  is chosen distinct from any free name in  $Q$ . Because  $P$  and  $Q$  are assumed to have disjoint sets of free channel names, so will their traces. Therefore,  $\parallel$  will effectively compute arbitrary interleavings of these two traces. Equation (6) specifies receiving a channel over  $a$  and binding it to the name  $b$  in  $P$ .

Equation (7) states that the parallel composition of processes is given by the synchronized interleavings of their traces. The synchronized interleavings of a pair of traces are defined in such a way that each receive action synchronizes with a send action when available. In particular, it captures a synchronous communication semantics. The process syntax  $P \parallel_a Q$  specifies the channel name  $a$  along which  $P$  and  $Q$  are composed to specify the only name shared between  $P$  and  $Q$  and to guide typechecking. In contrast, the interleaving operator does not mention  $a$ . Our operator is sufficiently general to synchronize traces on multiple channels. This is required, e.g., to handle synchronization for channel transmission, where we must synchronize processes traces on two channels: the transmitted channel and the channel carrying it.

Finally, eq. (8) hides the bound channels  $a_i$  by deleting all messages appearing on  $a_i$  from the traces in  $\llbracket P \rrbracket$ . It uses the deletion operation  $t \setminus \{a_1, \dots, a_n\}$  which is given in section 5.1. This deletion operator is defined in such a manner that it deletes not only actions on the  $a_i$ , but also actions appearing on the channels they carried, and so on and so forth.

To avoid proliferating definitions for trace operators, we postpone their formal definitions until we have discussed the slightly more general case of traces with constraints; readers wishing to skip ahead are invited to see section 5. Meanwhile, we consider a few intuition-building examples:

*Example 3.1.* We compute the denotation of the double negation process  $\text{doubleNeg}'$  of section 2.6. We start by computing the denotation of the bit flipping process  $\text{neg}$  composing it:

$$\begin{aligned} \llbracket i \leftrightarrow \text{neg} \leftrightarrow c \rrbracket &= \{ (?; \text{label } 0 \text{ on } i) :: (!; \text{label } 1 \text{ on } c) :: (?; \text{close on } i) :: (!; \text{close on } c) :: \varepsilon, \\ &\quad (?; \text{label } 1 \text{ on } i) :: (!; \text{label } 0 \text{ on } c) :: (?; \text{close on } i) :: (!; \text{close on } c) :: \varepsilon \}, \\ \llbracket c \leftrightarrow \text{neg} \leftrightarrow o \rrbracket &= \{ \text{analogous} \}. \end{aligned}$$

The denotation of  $\text{doubleNeg}$  uses the synchronized interleaving operator  $\parallel$  to interleave traces from each denotation. Intuitively, it matches  $(!; m)$  and  $(?; m)$  elements and replaces them by synchronization elements  $(\sigma; m)$ ; the remaining elements are freely interleaved. For example, it interleaves the following traces, respectively from  $\llbracket i \leftrightarrow \text{neg} \leftrightarrow c \rrbracket$  and  $\llbracket c \leftrightarrow \text{neg} \leftrightarrow o \rrbracket$ ,

$$\begin{aligned} & (?; \text{label } 0 \text{ on } i) :: (!; \text{label } 1 \text{ on } c) :: (?; \text{close on } i) :: (!; \text{close on } c) :: \varepsilon, \\ & (?; \text{label } 1 \text{ on } c) :: (!; \text{label } 0 \text{ on } o) :: (?; \text{close on } c) :: (!; \text{close on } o) :: \varepsilon, \end{aligned}$$

to produce, among others, the following traces in  $\llbracket (i \leftrightarrow \text{neg} \leftrightarrow c) \parallel_c (c \leftrightarrow \text{neg} \leftrightarrow o) \rrbracket$ :

$$\begin{aligned} & (?; \text{label } 0 \text{ on } i) :: (\sigma; \text{label } 1 \text{ on } c) :: (!; \text{label } 0 \text{ on } o) :: (?; \text{close on } i) :: (\sigma; \text{close on } c) :: \dots, \\ & (?; \text{label } 0 \text{ on } i) :: (\sigma; \text{label } 1 \text{ on } c) :: (?; \text{close on } i) :: (!; \text{label } 0 \text{ on } o) :: (\sigma; \text{close on } c) :: \dots. \end{aligned}$$

Contrasting the two given traces illustrates the concurrent nature of message-passing processes. In the first execution, the left  $\text{neg}$  process sends 0 before the right receives its close message on  $i$ ; this order is reversed in the second trace. Finally, the denotation of  $\text{doubleNeg}'$  hides all messages on  $c$ :

$$\begin{aligned} \llbracket \nu(c : \text{negBit } i) . \text{doubleNeg} \rrbracket &= \{ (?; \text{label } 0 \text{ on } i) :: (!; \text{label } 0 \text{ on } o) :: (?; \text{close on } i) :: \dots, \\ &\quad (?; \text{label } 0 \text{ on } i) :: (?; \text{close on } i) :: (!; \text{label } 0 \text{ on } o) :: \dots, \dots \}. \end{aligned}$$

*Example 3.2.* To illustrate how traces with binding help model channel transmission, consider the parallel composition  $\text{comp} :: \{ \} [] (c : 1) = P \parallel_a Q$ , where

$$\begin{aligned} P &:: \{ \} [] (a : 1 \otimes 1) = \text{send } a (c.\text{close } c); \text{close } a \\ Q &:: \{ \} [a : 1 \otimes 1] (c : 1) = b \leftarrow \text{receive } a; \text{wait } a; \text{wait } b; \text{close } c \end{aligned}$$

Process  $P$  denotes two traces, capturing the independent executions of processes  $\text{close } a$  and  $\text{close } c$ :

$$\begin{aligned} & \{ (!; \text{chan on } a) :: c . (!; \text{close on } c) :: (!; \text{close on } a) :: \varepsilon, \\ & \quad (!; \text{chan on } a) :: c . (!; \text{close on } a) :: (!; \text{close on } c) :: \varepsilon \} \end{aligned}$$

The process  $Q$  exhibits no non-determinism and denotes a single trace:

$$\llbracket Q \rrbracket = \{ (?; \text{chan on } a) :: b . (?; \text{close on } a) :: (?; \text{close on } b) :: (!; \text{close on } c) :: \varepsilon \}$$

Their parallel composition denotes a synchronized interleaving of these two trace sets. It is here that binding pays off: we can  $\alpha$ -vary the bound name  $c$  identifying the transmitted channel in the traces of  $P$  to a fresh name  $b$  to avoid clashing with the free name  $c$  in the trace of  $Q$ . The interleaved trace set captures synchronization on both  $a$  and the transmitted channel:

$$\llbracket \text{comp} \rrbracket = \{ (\sigma; \text{chan on } a) :: b . (\sigma; \text{close on } a) :: (\sigma; \text{close on } b) :: (!; \text{close on } c) :: \varepsilon \}$$

It contains a single trace, because only the first of  $P$ 's executions successfully synchronizes with  $Q$ .

There are no non-terminating executions, i.e., every process performs a finite number of actions:

**PROPOSITION 3.3 (TERMINATION).** *For all processes  $P$  and traces  $t \in \llbracket P \rrbracket$ ,  $t$  is finite.*

#### 4 MESSAGE-OBSERVING SESSION TYPES AND PROCESS SPECIFICATIONS

Our type soundness result will show that if a process typechecks against a specification, then its range of behaviours (the traces it denotes) is among those allowed by its specification. We specify which behaviours a process specification allows by giving specifications a trace semantics in section 4.1. First, we define message-observing session types and process specifications.

Recall that each process is a client of some services and the server of a distinguished service. From the perspective of a server of type  $A$ , session types are generated by the grammar:

Session Types $A, B ::= 1$	End communication and terminate
$\mid \oplus_{l \in L} A_l$	Internal choice
$\mid \&_{l \in L} A_l$	External choice
$\mid (a : A) \otimes (b : B)$	Channel transmission
$\mid (a : A) \multimap (b : B)$	Channel reception
$\mid \text{CASE } a \{ \text{close} \Rightarrow A \}$	Termination observation
$\mid \text{CASE } a \{ l \Rightarrow A_l \}_{l \in L}$	Label observation
$\mid \text{CASE } a \{ \langle b \rangle \Rightarrow A \}$	Higher-order observation

Session Type Context  $\Pi, \Delta, I ::= a_1 : A_1, \dots, a_n : A_n$

We explain session types from the perspective of a server. A server of  $1$  sends a close message signalling the end of communication on a channel. The internal choice type  $\oplus_{l \in L} A_l$  specifies sending a label  $l \in L$ , and then communicating according to  $A_l$ . Dually, the external choice type  $\&_{l \in L} A_l$  specifies receiving a label  $l \in L$ , and then communicating according to  $A_l$ . Many session-typed languages provide a session type  $A \otimes B$ . A server for a channel  $c$  of type  $A \otimes B$  sends a channel  $a$  of type  $A$  on  $c$  and then communicates on  $c$  according to  $B$ . Our syntax  $(a : A) \otimes (b : B)$  extends the usual syntax to support mutual observation between the types  $A$  and  $B$ . It binds the name  $a$  in  $B$  to allow  $B$  to observe communications on the transmitted channel, and it binds  $b$  in  $A$  to allow  $A$  to observe subsequent communications on the carrier channel. A server of a channel  $c$  of type  $(a : A) \otimes (b : B)$  sends a channel  $a$  of type  $[c/b]A$ , and then communicates on  $c$  according to  $B$ . Symmetrically, a server of  $c : (a : A) \multimap (b : B)$  specifies receiving a channel  $a$  of type  $[c/b]A$  and then communicating on  $c$  according to  $B$ . This type has an identical binding structure. We call these types **weak head normal** because they do not involve computation at the outermost level.

**Observing types** restrict communication based on communication on ambient channels. The type  $\text{CASE } a \{ \text{close} \Rightarrow A \}$  observes an ambient channel  $a$  and reduces to  $A$  when a close message is sent on  $a$ . The type  $\text{CASE } a \{ l \Rightarrow A_l \}_{l \in L}$  reduces to the type  $A_l$  when a label  $l$  is sent on  $a$ . Finally,  $\text{CASE } a \{ \langle b \rangle \Rightarrow A \}$  reduces to  $A$  if a channel named  $b$  is transmitted over  $a$ . Because a priori, we do not know the name of the transmitted channel,  $\text{CASE } a \{ \langle b \rangle \Rightarrow A \}$  binds the name  $b$  in  $A$ .

The free channels in a type are inductively defined on the structure in the obvious manner.

Our motivating examples used a concrete syntax “name  $:: \{ \Sigma \} [\Delta] (c : C) = P$ ” to specify a process name, implemented by  $P$ , that used channels  $\Delta$  to provide a service  $c : C$ , where these channels were free to observe ambient channels  $\Sigma$ . These ambient channels were either (unhidden) internal channels, or channels in other processes whose meaning would be given by process composition. By inspecting the syntax of  $P$ , we can partition  $\Sigma$  into a context  $I$  of internal channels and a context  $\Pi$  containing the remaining ambient channels. We can then translate the concrete specification syntax  $\{ \Pi, I \} [\Delta] (c : C)$  into the abstract syntax  $\Pi \# (\Delta \mid I \vdash c : C)$ . Our abstract syntax distinguishes  $\Pi$  and  $I$  because they play different roles in typechecking: channels in  $I$  can be hidden by  $P$ , while those in  $\Pi$  cannot appear in  $P$ . Most’s typing judgment  $P \Vdash \Pi \# (\Delta \mid I \vdash c : C) // \mathcal{T}$

checks  $P$  against its specification, generating constraints  $\mathfrak{T}$ . Our concrete syntax names processes only to simplify examples; process names do not appear in our formal system.

To specify channel transmission, we need to specify multiple independent processes that execute concurrently. Indeed,  $\text{send } a(b.P)$ ;  $Q$  spawns process  $P$  providing a fresh  $b$ , sends  $b$  over  $a$ , and runs  $P$  concurrently with  $Q$  providing  $a$ . Checking  $\text{send } a(b.P)$ ;  $Q$  against its specification requires decomposing its specification into specifications for  $P$  and  $Q$ , and checking  $P$  and  $Q$ . Accordingly, we extend our abstract syntax for specifications to simultaneously specify multiple independent processes. We write  $\Pi \# ((\Delta_1 \vdash I_1 \vdash c_1 : C_1) \mid \cdots \mid (\Delta_n \vdash I_n \vdash c_n : C_n))$  to simultaneously specify  $n$  processes, where channels  $\Pi$  and  $\Delta_i, I_i, c_i : C_i$  are ambient in  $\Delta_j, I_j, c_j : C_j$  for  $i \neq j$ . We call  $(\Delta \vdash I \vdash c : C)$  a process **interface**: it specifies the communication interface for a process. We range over interface sequences  $((\Delta_1 \vdash I_1 \vdash c_1 : C_1) \mid \cdots \mid (\Delta_n \vdash I_n \vdash c_n : C_n))$  using the meta-variable  $\mathcal{G}$ .

A specification  $\Pi \# \mathcal{G}$  is well-defined only if for each  $a : A$  in  $\Pi$  or  $\mathcal{G}$ , the free channels in  $A$  are contained in the domain of  $\Pi \# \mathcal{G}$ , i.e., are assigned types by the specification. This precludes, e.g.,  $\cdot \# (a : A \vdash \cdot \vdash c : \text{CASE } b \{ \text{close} \Rightarrow C \})$  because the channel  $b$  is not typed by the specification.

#### 4.1 Type reductions and the semantics of specifications

In our motivating examples, we informally reduced types in specifications after observing messages on channel. To make this explicit, we first define reduction on types, and then extend it to entire specifications. Type reduction is given by a syntactic operation  $A/\pi$  that reduces a type  $A$  by an observed communication  $\pi$ . Observed communication,  $\text{close on } a$ ,  $\text{label } l \text{ on } a$ , and  $\text{chan } b \text{ on } a$ , closely resembles messages from the trace semantics. The only difference is in the case of channel transmissions, where a free channel name  $b$  identifies the transmitted channel. This free name will be used by the process typing judgment to update types with the name of the received channel. We write  $\text{fc}(\pi)$  for the free channels in  $\pi$ , and  $\text{cc}(\pi) = a$  for the **carrier channel**.

**Type reduction** is partial, capture-avoiding, and inductively defined by:

$$\begin{aligned}
 & 1/\pi = 1 \\
 & (\odot_{l \in L} A_l) / \pi = \odot_{l \in L} (A_l / \pi) \quad (\odot \in \{ \oplus, \& \}) \\
 & ((a : A) \odot (b : B)) / \pi = (a : A/\pi) \odot (b : B/\pi) \quad (\odot \in \{ \otimes, \multimap \}) \\
 & (\text{CASE } c \{ \text{close} \Rightarrow A \}) / \pi = \begin{cases} A & \text{if } \pi = \text{close on } c \\ \text{CASE } c \{ \text{close} \Rightarrow A/\pi \} & \text{if } \text{cc}(\pi) \neq c \end{cases} \\
 & (\text{CASE } c \{ l \Rightarrow A_l \}_{l \in L}) / \pi = \begin{cases} A_l & \text{if } \pi = \text{label } l \text{ on } c \text{ and } l \in L \\ \text{CASE } c \{ l \Rightarrow A_l/\pi \}_{l \in L} & \text{if } \text{cc}(\pi) \neq c \end{cases} \\
 & (\text{CASE } c \{ \langle b \rangle \Rightarrow A \}) / \pi = \begin{cases} A & \text{if } \pi = \text{chan } b \text{ on } c \\ \text{CASE } c \{ \langle b \rangle \Rightarrow A/\pi \} & \text{if } \text{cc}(\pi) \neq c \end{cases}
 \end{aligned}$$

All other cases are undefined.

When a message is observed on a channel in a process specification, we simultaneously reduce all types in the specification. This eliminates the need to track past observations and it simplifies the presentation. The syntax  $(a_1 : A_1, \dots, a_n : A_n) / \pi = a_1 : (A_1/\pi), \dots, a_n : (A_n/\pi)$  captures the simultaneous reduction of types in a context; it is defined whenever all  $A_i/\pi$  are defined. It lifts to process specifications in context-by-context.

A specification  $\Pi \# \mathcal{G}$  specifies a collection of permitted behaviours, i.e., a trace set  $\llbracket \Pi \# \mathcal{G} \rrbracket$ . To make this discussion concrete, consider a specification  $\Pi \# (\Delta \vdash I \vdash c : C)$ . The traces in  $\llbracket \Pi \# (\Delta \vdash I \vdash c : C) \rrbracket$  are process traces that involve sending or receiving messages on  $\Delta, c : C$ , with synchronizations on  $I$ . However, these traces may also contain **constraint elements**  $(\zeta; m)$ . An

element  $(\zeta; \text{close on } a)$  means that the execution described by the remaining tail of the trace is permitted only if a close message appears on the ambient channel  $a \in \text{dom}(\Pi)$ . Constraint elements  $(\zeta; \text{label } l \text{ on } a)$  and  $(\zeta; \text{chan on } a)$  are analogous. We use constraint elements to semantically specify that a process is well-typed, provided that some other processes perform some action.

A trace set  $\llbracket \Pi \# \mathcal{G} \rrbracket$  is the least set satisfying the following collection of inequalities. It is well-defined because type reduction reduces the number of type operators in a specification, so the right-hand specification always contains fewer type operators than the left-hand specification. The right-hand side may be undefined; in this case, we treat it as an empty set.

We first specify label transmission. Where  $\pi = \text{label } k \text{ on } a$ ,  $m = \text{label } k \text{ on } a$ , and  $k \in L$ :

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : \oplus_{l \in L} A_l) \rrbracket \supseteq (!; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash a : A_k) \rrbracket \quad (9)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta, a : \oplus_{l \in L} A_l \mid I \vdash c : C) \rrbracket \supseteq (?; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi, a : A_k \mid I/\pi \vdash c : C/\pi) \rrbracket \quad (10)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, a : \oplus_{l \in L} A_l \vdash c : C) \rrbracket \supseteq (\sigma; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi, a : A_k \vdash c : C/\pi) \rrbracket \quad (11)$$

The first clause specifies that a provider of type  $\oplus_{l \in L} A_l$  can send any label  $l$  in  $L$ . The provider must obey its original specification, reduced by the just-observed message label  $l$  on  $a$ . Receiving a label is dually specified. The last clause specifies that a process formed of a composition along  $a : \oplus_{l \in L} A_l$  can perform a synchronization on that channel. We define clauses with inequalities to avoid imposing an order on communication on different channels. For example, given a specification  $\cdot \# (a : \oplus(\emptyset : 1) \mid \cdot \vdash b : \oplus(\emptyset : 1))$ , the above clauses ensure that  $a$  and  $b$  can be used in either order because its denotation includes traces starting with either channel.

A provider of type **1** can terminate only if it has no other channels. This ensures that channels do not accidentally get discarded and it is captured by the first clause below. Receiving and synchronizing on channels of type **1** ends communication on that channel. The last clause captures termination when there are no processes left. Where  $\pi = \text{close on } a$ :

$$\llbracket \Pi \# \mathcal{G} \mid (\cdot \mid \cdot \vdash a : 1) \rrbracket \supseteq (!; \text{close on } a) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \rrbracket \quad (12)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta, a : 1 \mid I \vdash c : C) \rrbracket \supseteq (?; \text{close on } a) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash c : C/\pi) \rrbracket \quad (13)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, a : 1 \vdash c : C) \rrbracket \supseteq (\sigma; \text{close on } a) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash c : C/\pi) \rrbracket \quad (14)$$

$$\llbracket \Pi \# \cdot \rrbracket \supseteq \{\varepsilon\} \quad (15)$$

Channel transmission changes the shape of specifications to capture spawning processes. Where  $\pi = (\text{chan } b \text{ on } a)$  with  $b$  fresh:

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta_1, \Delta_2 \mid I_1, I_2 \vdash a : (b : B) \otimes (a : A)) \rrbracket \\ & \supseteq (!; \text{chan on } a) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta_1/\pi \mid I_1/\pi \vdash b : B) \mid (\Delta_2/\pi, a : A \mid I_2/\pi \vdash c : C/\pi) \rrbracket \end{aligned} \quad (16)$$

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta, a : (b : B) \otimes (a : A) \mid I \vdash c : C) \rrbracket \\ & \supseteq (?; \text{chan on } a) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi, b : B, a : A \mid I/\pi \vdash c : C/\pi) \rrbracket \end{aligned} \quad (17)$$

The first clause specifies the permitted behaviours of a provider send  $a(b.P)$ ;  $Q$ . Operationally, this process forks  $P$  and  $Q$ , respectively providing  $b$  and  $a$ , and sends  $b$  over  $a$ . The used and internal channels of send  $a(b.P)$ ;  $Q$  are then divided between  $P$  and  $Q$ . Accordingly, the first clause splits its interface  $\Delta_1, \Delta_2 \mid I_1, I_2 \vdash a : (b : B) \otimes (a : A)$  in two. The channels assigned to  $P$  and  $Q$  depend on the particular implementation, so the clause allows for arbitrary partitions  $\Delta_1, \Delta_2$  and  $I_1, I_2$ . This clause also illustrates why the observed message  $\text{chan } b \text{ on } a$  (used in type reductions) specifies a free name  $b$ , in contrast to the message element  $\text{chan on } a$  used in traces. When we reduce the remaining types in the specification to account for the channel transmission, we must specify the free name  $b$  that carries the communications of  $P$ . However, this name  $b$  is not externally meaningful, and we bind it in the trace to avoid name clashes in process compositions. The second

clause captures receiving a channel over  $b$ ; it updates the context to contain the received channel. The elided clause for synchronization is analogous to the second clause.

Finally, we specify the constraints generated by ambient channels in  $\Pi$ :

$$\begin{aligned} \llbracket \Pi, a : 1 \# \mathcal{G} \rrbracket \supseteq (\zeta; \text{close on } a) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \rrbracket, \\ \text{where } \pi = \text{close on } a; \end{aligned} \quad (18)$$

$$\begin{aligned} \llbracket \Pi, a : \odot_{l \in L} A_l \# \mathcal{G} \rrbracket \supseteq (\zeta; \text{label } l \text{ on } a) :: \llbracket \Pi/\pi, a : A_l \# \mathcal{G}/\pi \rrbracket, \\ \text{where } \pi = \text{label } l \text{ on } a, l \in L, \text{ and } \odot \in \{ \oplus, \& \}; \end{aligned} \quad (19)$$

$$\begin{aligned} \llbracket \Pi, a : (b : B) \odot (a : A) \# \mathcal{G} \rrbracket \supseteq (\zeta; \text{chan on } a) :: b. \llbracket \Pi/\pi, a : A, b : B \# \mathcal{G}/\pi \rrbracket, \\ \text{where } \pi = \text{chan } b \text{ on } a, \text{ and } \odot \in \{ \emptyset, \multimap \}. \end{aligned} \quad (20)$$

The first clause specifies that if we reduce a specification by observing a close message on an ambient channel  $a$ , then the traces permitted by the reduced specification are valid provided that some process in the environment closes  $a$ . The second clause is analogous. The third clause captures the fact that sending a channel results in a pair of channels in a context: the transmitted channel and its carrier. We assume without loss of generality that  $b$  is fresh in the third clause.

The omitted clauses for  $\multimap$  and  $\&$  are analogous. There are no clauses for observing types: they allow communication only after reducing to a weak-head normal type. In particular, type reductions are silent and they occur as a result of communication on channels with weak-head normal types.

*Example 4.1.* The concrete specification  $\text{neg} :: \{ \} [i : \text{Bits}_1] (c : \text{negBit } i)$  of section 2.6 corresponds to the specification  $\cdot \# (i : \text{Bits}_1 \vdash c : \text{negBit } i)$ . It only allows the executions

$$\begin{aligned} (?; \text{label } 0 \text{ on } i) :: (!; \text{label } 1 \text{ on } c) :: (?; \text{close on } i) :: (!; \text{close on } c) :: \varepsilon, \\ (?; \text{label } 0 \text{ on } i) :: (?; \text{close on } i) :: (!; \text{label } 1 \text{ on } c) :: (!; \text{close on } c) :: \varepsilon, \end{aligned}$$

plus the two traces obtained by exchanging 0 and 1. The first two traces illustrate the concurrent nature of communication on different channels. Indeed,  $\text{negBit } i$  only allows communication on  $c$  after communication on  $i$ , but no other ordering is imposed between messages on  $i$  and  $c$ .

*Example 4.2.* The concrete specification  $\text{neg} :: \{ i : \text{Bits}_1 \} [c : \text{negBit } i] (o : \text{ldBitSeq}_1 i)$  of section 2.6 corresponds to the specification  $i : \text{Bits}_1 \# (c : \text{negBit } i \vdash o : \text{ldBitSeq}_1 i)$ . It allows:

$$\begin{aligned} (\zeta; \text{label } 0 \text{ on } i) :: (?; \text{label } 1 \text{ on } c) :: (!; \text{label } 0 \text{ on } o) :: \\ (\zeta; \text{close on } i) :: (?; \text{close on } c) :: (!; \text{close on } o) :: \varepsilon, \\ (\zeta; \text{label } 0 \text{ on } i) :: (\zeta; \text{close on } i) :: (?; \text{label } 1 \text{ on } c) :: (!; \text{label } 0 \text{ on } o) :: \\ (?; \text{close on } i) :: (!; \text{close on } o) :: \varepsilon, \text{ etc.} \end{aligned}$$

Interpreting each trace as an execution or sequence of actions permitted to  $\text{neg}$ , the first trace specifies that if a label 0 is sent on an ambient channel  $i$ , then  $\text{neg}$  can receive a bit 1 on  $c$ , send 0 on  $o$ , and so on and so forth. The second trace is analogous, but illustrates that we impose no extraneous orderings on messages on different channels.

*Example 4.3.* The specification  $a : 1, b : 1 \# (\cdot \vdash c : \text{CASE } a \{ \text{close} \Rightarrow \text{CASE } b \{ \text{close} \Rightarrow 1 \} \})$  permits two traces:

$$\begin{aligned} (\zeta; \text{close on } a) :: (\zeta; \text{close on } b) :: (!; \text{close on } c) :: \varepsilon, \\ (\zeta; \text{close on } b) :: (\zeta; \text{close on } a) :: (!; \text{close on } c) :: \varepsilon. \end{aligned}$$

These two traces illustrate that the type of  $c$  does not impose an order on ambient communications: because we can reduce under CASE, communication on  $a$  and  $b$  can occur in any order. Indeed, the first trace corresponds to reducing the specification to  $b : 1 \# (\cdot \vdash c : \text{CASE } b \{ \text{close} \Rightarrow 1 \})$

(after observing close on  $a$ ) and then to  $\cdot \# (\cdot \vdash c : 1)$  (after observing close on  $b$ ); the second captures first reducing to  $a : 1 \# (\cdot \vdash c : \text{CASE } a \{ \text{close} \Rightarrow 1 \})$ , then to  $\cdot \# (\cdot \vdash c : 1)$ .

*Example 4.4.* The specification  $\cdot \# (\cdot \vdash a : (b : 1) \otimes \text{CASE } b \{ \text{close} \Rightarrow 1 \})$  specifies sending a channel of type 1, and with the carrier closing after the sent channel. Its trace set is:

$$\begin{aligned} & (!; \text{chan on } a) :: b. \llbracket \cdot \# (\cdot \vdash b : 1) \mid (\cdot \vdash a : \text{CASE } b \{ \text{close} \Rightarrow 1 \}) \rrbracket \\ &= (!; \text{chan on } a) :: b. (!; \text{close on } b) :: \llbracket \cdot \# (\cdot \vdash a : 1) \rrbracket \\ &= \{ (!; \text{chan on } a) :: b. (!; \text{close on } b) :: (!; \text{close on } a) :: \varepsilon \}. \end{aligned}$$

This example shows how multiple interfaces in a specification are used to specify relationships between messages from different processes that are spawned in the course of channel transmission.

## 5 OPERATIONS ON TRACES

Our trace semantics for processes uses two operations on traces: deletion and synchronized interleaving. Name deletion deletes all messages in a trace whose names appear in a given set, while synchronized interleavings interleave two traces while ensuring that input and output actions match up. We also specify how to delete constraints elements from traces. All three operations will be used by our typechecking algorithm when generating traces with constraints.

### 5.1 Deleting channel names from traces

The construct  $v(\overrightarrow{a_i : A_i}) . P$  binds the channels  $a_i$  to hide them from external view. Semantically, hiding is captured by deleting all messages on channels  $a_i$  from the traces of  $P$ . To account for channel transmission, we may need to delete messages on other channel names. In particular, if  $a_i$  carries some channel  $b$ , then we must also delete all messages on  $b$  from the trace.

Given a set  $X$  of names and a trace  $t$ , the deletion of  $X$  in  $t$  is the trace  $t \setminus X$  inductively defined by:

$$\varepsilon \setminus X = \varepsilon \quad \text{and} \quad ((s; m) :: c_1, \dots, c_n . t) \setminus X = \begin{cases} t \setminus (X \cup \{c_1, \dots, c_n\}) & \text{if } \text{cc}(m) \in X \\ (s; m) :: c_1, \dots, c_n . (t \setminus X) & \text{otherwise} \end{cases}$$

We assume without loss of generality that the names  $c_i$  are chosen distinct from those already in  $X$ .

*Example 5.1.* If  $t = (?; \text{chan on } a) :: b. (?; \text{close on } a) :: (?; \text{close on } b) :: (!; \text{close on } c) :: \varepsilon$ , then  $t \setminus \{a\} = (!; \text{close on } c) :: \varepsilon$ .

### 5.2 Deleting constraints from traces

The operation  $t/\dot{\iota}$  deletes all constraints from  $t$ . We will use it when checking that processes have consistent constraint sets when typing process compositions. It is inductively defined by

$$\varepsilon/\dot{\iota} = \varepsilon \quad \text{and} \quad (s; m) :: \vec{x}. t/\dot{\iota} = \begin{cases} t/\dot{\iota} & \text{if } s = \dot{\iota} \\ (s; m) :: \vec{x}. t/\dot{\iota} & \text{otherwise} \end{cases}$$

### 5.3 Trace reduction

When interleaving two traces, we need to delete constraints in one trace satisfied by messages in the other trace. We do so using a partial operation  $t/\pi$  that deletes a constraint  $(\dot{\iota}; m)$  in  $t$  if it is satisfied by the observed communication  $\pi$ . It is inductively defined on the structure of the trace:

$$\begin{aligned} \varepsilon/\pi &= \varepsilon & ((\dot{\iota}; \text{close on } a) :: t)/(\text{close on } a) &= t \\ ((\dot{\iota}; \text{label } l \text{ on } a) :: t)/(\text{label } l \text{ on } a) &= t & ((\dot{\iota}; \text{chan on } a) :: c. t)/(\text{chan } b \text{ on } a) &= [b/c]t \\ ((s; m) :: \vec{x}. t)/\pi &= (s; m) :: \vec{x}. (t/\pi) & \text{if } \text{cc}(m) \neq \text{cc}(\pi), \end{aligned}$$

and it is undefined in all other cases. The fourth clause instantiates a bound name  $c$  for a transmitted channel with the free name  $b$  actually observed in the course of channel transmission. The last clause handles an observation on a channel different from the head of the trace. It is defined only if  $t/\pi$  is defined. We always  $\alpha$ -vary  $\vec{x}$  to be distinct from the names in  $\pi$ .

*Example 5.2.* The reduction  $((\zeta; \text{label } k \text{ on } a) :: \varepsilon) / (\text{label } l \text{ on } a)$  is undefined because the label  $l$  observed on  $a$  does not match the label  $k$  expected by the constraint. Similarly, the reduction  $((!; \text{close on } a) :: \varepsilon) / (\text{label } l \text{ on } a)$  is undefined because the trace we are reducing sends on  $a$ , while the observation “label  $l$  on  $a$ ” is meant to be an observation on an ambient channel  $a$ .

#### 5.4 Synchronized trace interleavings

Synchronized interleavings interleave traces according to a synchronous communication semantics. It marks matching input-output elements as synchronized, and ensures that constraints imposed by one trace are not violated by the other trace. This commutative operator is defined by lexicographic induction on the length of the traces. We group its clauses into four categories:

- (1) the base cases: interleaving a trace with an empty trace;
- (2) synchronizing cases: the heads of each trace perform actions on the same channel;
- (3) commuting cases: the heads of each trace perform actions on different channels;
- (4) ill-defined cases: unsynchronizable sends and receives, etc.

The base cases specify that the empty trace is the unit for interleaving:

$$\varepsilon \parallel t = \{t\}, \quad t \parallel \varepsilon = \{t\}.$$

The first synchronizing case holds whenever two traces send and receive the same message:

$$((!; m) :: \vec{x}. t_1) \parallel ((?; m) :: \vec{x}. t_2) = (\sigma; m) :: \vec{x}. (t_1 \parallel t_2).$$

It specifies that these two traces synchronize (captured by the  $\sigma$  sign), and that the tail is given by interleaving their tails. To ensure that the resulting trace is well-formed, we require that both traces bind the same names. It is this implicit  $\alpha$ -variation that matches sent and received channel names  $b$  when interleaving traces of the form  $(!; \text{chan on } a) :: b.t_1$  and  $(?; \text{chan on } a) :: b.t_2$ .

The other principal case manages constraints. It states that a constraint in one trace can be dropped whenever it is satisfied by a corresponding action in the other trace, or whenever both traces have the same constraint. If a constraint is matched against a different message on that channel, then the constraint cannot be satisfied and there are no interleavings.

$$((s; m) :: \vec{x}. t_1) \parallel ((\zeta; m) :: \vec{x}. t_2) = (s; m) :: \vec{x}. (t_1 \parallel t_2)$$

The commuting cases handle heads that act on different channels. Let  $(m \propto \vec{x})$  be the observed communication induced by a message  $m$  and channel names  $\vec{x}$ :  $(\text{close on } a \propto \emptyset) = \text{close on } a$ ;  $(\text{label } l \text{ on } a \propto \emptyset) = \text{label } l \text{ on } a$ ;  $(\text{chan on } a \propto b) = \text{chan } b \text{ on } a$ ; and  $(m \propto \vec{x})$  is undefined otherwise. Given a partial map  $f$ , let  $f(x) \simeq y$  define  $y$  whenever  $f(x)$  is defined. Where  $T_i = (s_i; m_i) :: \vec{x}_i.t_i$ ,

$$\left. \begin{aligned} & (s_1; m_1) :: \vec{x}_1.t_1 \parallel (s_2; m_2) :: \vec{x}_2.t_2 \\ & = \{ (s_1; m_1) :: \vec{x}_1.t \mid T_2 / (m_1 \propto \vec{x}_1) \simeq T'_2 \wedge t \in t_1 \parallel T'_2 \} \cup \\ & \cup \{ (s_2; m_2) :: \vec{x}_2.t \mid T_1 / (m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in T'_1 \parallel t_2 \} \end{aligned} \right\} \quad \text{if } \text{cc}(m_1) \neq \text{cc}(m_2).$$

This case is well-defined because the lengths of  $T'_1$  and  $T'_2$  are respectively at most the lengths of  $T_1$  and  $T_2$ . Our trace reduction operator hides considerable complexity, so we unpack the definition.

First, observe that the commuting case ensures that if a synchronization between  $(s_1; m_1)$  and an element of  $T_2$  is eventually possible, then it occurs. Indeed, if  $\text{cc}(m_1)$  appears free in  $T_2$  with an observable sign, then  $T_2 / (m_1 \propto \vec{x}_1)$  is undefined (see, e.g., example 5.2). As a result, the first set

(prefixing  $(s_1; m_1)$ ) is empty, and we interleave  $T_1$  with the tail of  $T_2$  in the second set to eventually synchronize  $(s_1; m_1)$  with the corresponding element of  $T_2$ .

*Example 5.3.* The interleaving  $(!; \text{close on } a) :: \varepsilon \parallel (?: \text{close on } b) :: (?: \text{close on } a) :: \varepsilon$  is determined by the commuting case. The reduction in the first set of the clause is undefined, so the first set is empty. The second,  $\{ (?: \text{close on } b) :: t \mid t \in ((!; \text{close on } a) :: \varepsilon) \parallel ((?: \text{close on } a) :: \varepsilon) \}$ , is defined in terms of a principal case. This principal case captures the synchronization on  $a$ , and the interleavings are given by  $\{ (?: \text{close on } b) :: (\sigma; \text{close on } a) :: \varepsilon \}$ .

Second, the commuting case detects deadlock. Indeed, if two traces attempt to synchronize on different channels in opposite orders, then the carrier of the head of one trace is free in the other trace. This implies that the reduction in each set is undefined, resulting in no interleavings:

$$((?: \text{close on } a) :: (!; \text{close on } b) :: \varepsilon) \parallel ((?: \text{close on } b) :: (!; \text{close on } a) :: \varepsilon) = \emptyset.$$

Third, the commuting case uses reduction to handle constraint satisfaction. Indeed, if  $(s_1; m_1)$  satisfies a constraint in  $T_2$ , then the reduction  $T_2 / (m_1 \propto \vec{x}_1)$  deletes that constraint before interleaving the result with the tail  $t_1$ .

*Example 5.4.* Interleaving  $((!; \text{close on } a) :: \varepsilon) \parallel ((?: \text{close on } b) :: (!; \text{close on } a) :: \varepsilon)$  produces the interleavings  $\{ (!; \text{close on } a) :: (?: \text{close on } b) :: \varepsilon, (?: \text{close on } b) :: (!; \text{close on } a) :: \varepsilon \}$ , where the send on  $a$  in the first trace satisfies the constraint in the second trace.

Finally, the commuting case ensures that constraint satisfaction obeys synchronization boundaries. For example, consider interleaving the traces  $(!; \text{close on } a) :: (?: \text{close on } b) :: \varepsilon$  and  $(!; \text{close on } b) :: (!; \text{close on } a) :: \varepsilon$ . Intuitively, they synchronize on  $b$ . However, the constraint on  $a$  cannot be satisfied by the send on  $a$ , because the constraint is imposed before the synchronization, while the send occurs after the synchronization on  $b$ . This unsatisfiability implies that the traces should have no synchronizing interleavings. This is captured by our interleaving operator: the only applicable clause is the commuting case, and both sets defining it are empty.

All other cases are taken to be ill-defined, so we define their set of interleavings to be empty. These include, e.g., cases of the form  $((!; \text{close on } a) :: t) \parallel ((!; \text{label } l \text{ on } a) :: t')$  (sending on the same channel) and mismatched sends and receives:  $((!; \text{close on } a) :: t) \parallel ((?: \text{label } l \text{ on } a) :: t')$ .

A trace is **safely constrained** if any carrier channel associated with a constraint sign is not also associated with input, output, or synchronization. By proposition 5.5, constraints can only limit the interleavings of observable actions. In other words, checking constraints when interleaving traces does not introduce new observable behaviours. The opposite inclusion will play a key role when typechecking process composition:

PROPOSITION 5.5. *Given safely constrained traces  $t_1$  and  $t_2$ ,  $(t_1 \parallel t_2) / \zeta \subseteq (t_1 / \zeta) \parallel (t_2 / \zeta)$ .*

## 5.5 Type-theoretic properties

Section 4.1 gave a trace semantics for process specifications. We relate the behaviour of trace operators to this trace assignment. Recall that a process specification  $\Pi \# \mathcal{G}$  is well-formed only if every channel appearing free in a type in  $\Pi$ ,  $\mathcal{G}$  is typed by some other type assignment in the specification.

Proposition 5.6 semantically characterizes hiding internal channels in a specification. The well-formedness hypothesis is required to ensure that no types become ill-scoped. For example, it precludes deleting  $a : \mathbf{1}$  from  $\Pi \# (\Delta \mid \mathbf{I}, a : \mathbf{1} \vdash c : \text{CASE } a \{ \text{close} \Rightarrow C \})$ .

PROPOSITION 5.6. *If  $t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid \mathbf{I}, \Gamma \vdash a : A) \rrbracket$  and  $\Pi \# \mathcal{G} \mid (\Delta \mid \mathbf{I} \vdash a : A)$  is well-formed, then  $(t \setminus \text{dom}(\Gamma)) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid \mathbf{I} \vdash a : A) \rrbracket$ .*

The following proposition characterizes independent parallel executions. To concretize it, consider specifications  $\Pi, \Delta_2, I_2, a_2 : A_2 \# (\Delta_1 \mid I_1 \vdash a_1 : A_1)$  and  $\Pi, \Delta_1, I_1, a_1 : A_1 \# (\Delta_2 \mid I_2 \vdash a_2 : A_2)$ . They specify two processes whose types can observe each other's channels. We can compose these two specifications in a single specification:  $\Pi \# (\Delta_1 \mid I_1 \vdash a_1 : A_1) \mid (\Delta_2 \mid I_2 \vdash a_2 : A_2)$ . The proposition states that interleaving the traces of each individual specification results in traces allowed by the composed specification. This proposition is useful when characterizing traces involving channel transmissions. More generally, where  $\overline{\mathcal{G}}$  denotes the typed channels in  $\mathcal{G}$ :

**PROPOSITION 5.7.** *If  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$  and  $t_2 \in \llbracket \Pi, \overline{\mathcal{G}_1} \# \mathcal{G}_2 \rrbracket$ , then  $t_1 \parallel t_2 \subseteq \llbracket \Pi \# \mathcal{G}_1 \mid \mathcal{G}_2 \rrbracket$ .*

An alternate form of specification composition results in synchronization. Indeed, consider processes specifications  $\Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A)$  and  $\Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C)$ . The parallel composition operation composes them along the channel  $a$  so that they synchronize on this channel. Subject to side conditions, their composition satisfies  $\Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C)$  where  $a$  becomes an inner synchronization channel. This synchronizing parallel composition is also characterized by synchronized interleaving:

**PROPOSITION 5.8.** *If  $t_1 \in \llbracket \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \rrbracket$  and  $t_2 \in \llbracket \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \rrbracket$ , then  $t_1 \parallel t_2 \subseteq \llbracket \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \rrbracket$ .*

## 6 TYPECHECKING PROCESSES

We give a typechecking algorithm to statically check that a process satisfies its specification, i.e., that each execution of the process is permitted by its specification. It is driven by a collection of inference rules and operates in two alternating phases.

The first phase decomposes process compositions into individual processes. It then determines the next channel  $a$  that a given process will use to communicate, and kicks off the second phase. The second phase focusses on the type of  $a$ . It first tries to reduce the focussed type to weak head normal form, and then proceeds to typecheck the given process with respect to that weak head normal type. Because our system is compositional, types may depend on ambient channels whose meaning will eventually be given by composition, and type reduction may cause constraints to be imposed on ambient communications. To capture these, our algorithm generates a collection constraints that must be satisfied by the environment in order for the process to be well-typed.

### 6.1 Phase one: uniform process typing

The first phase, which we call uniform typing, is driven by a judgment  $P \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}$  inductively defined by the rules of fig. 1. We use colours to indicate the modes of each parameter in the judgment, where **red** indicates inputs to the judgment and **blue** indicates outputs. Here, we attempt to check process  $P$  against the well-formed specification  $\Pi \# (\Delta \mid I \vdash a : A)$ . The judgment outputs a set  $\mathfrak{T}$  of traces that consist of the communications of  $P$  interleaved with constraints on channels in  $\Pi$ . By our soundness result (theorem 7.1), restricting each trace in  $\mathfrak{T}$  to its observable actions (i.e., those with sign  $?$ ,  $!$ , or  $\sigma$ ) will result in the set  $\llbracket P \rrbracket$  of traces of  $P$ . We implicitly assume that process specifications are well-formed, i.e., that all names are in scope.

The rule (NEW) introduces internal channels that can be used to type process  $P$ . The constraint set for its conclusion is obtained by deleting the new channels from the constraint set for  $P$ . This is analogous to the trace semantics of hiding in section 3.1. Because we assume that all specifications are well-formed, we know that no types in  $\Pi \# (\Delta \mid I \vdash c : C)$  attempt to observe  $a_1, \dots, a_n$ .

The rule (PAR) ensures that two processes can be composed only if they impose compatible constraints. This is ensured by premise  $\mathfrak{T}_1 \therefore \mathfrak{T}_2$  that holds if and only if:

$$\forall t_1 \in \mathfrak{T}_1 . \forall t_2 \in \mathfrak{T}_2 . (t_1 / \dot{\iota} \parallel t_2 / \dot{\iota}) = (t_1 \parallel t_2) / \dot{\iota}. \quad (21)$$

$$\boxed{P \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}} \quad \text{Process } P \text{ satisfies specification } \Pi \# (\Delta \mid I \vdash a : A) \text{ subject to constraints } \mathfrak{T}$$

$$\frac{P \Vdash \Pi \# (\Delta \mid I, a_1 : A_1, \dots, a_n : A_n \vdash c : C) \parallel \mathfrak{T}}{v(a_1 : A_1, \dots, a_n : A_n). P \Vdash \Pi \# (\Delta \mid I \vdash c : C) \parallel \mathfrak{T} \setminus \{a_1, \dots, a_n\}} \text{ (NEW)}$$

$$\frac{P \Vdash \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1 \quad Q \Vdash \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2 \quad \mathfrak{T}_1 \dot{\vdash} \mathfrak{T}_2}{P \parallel_a Q \Vdash \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2} \text{ (PAR)}$$

$$\frac{P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{T} \quad \text{princ}(P) = a}{P \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}} \text{ (R)}$$

$$\frac{P \Rightarrow \Pi \# (\Delta, [a : A] \mid I \vdash c : C) \parallel \mathfrak{T} \quad \text{princ}(P) = a}{P \Vdash \Pi \# (\Delta, a : A \mid I \vdash c : C) \parallel \mathfrak{T}} \text{ (L)}$$

Fig. 1. Uniform Process Typing Judgment

Intuitively, it specifies that if traces  $t_1$  and  $t_2$  corresponding to  $P$  and  $Q$  can be composed as process executions, then each synchronized interleaving of  $t_1$  and  $t_2$  qua process traces can be obtained by synchronizing  $t_1$  and  $t_2$  as constraint traces. In other words, we can interleave the constraints in  $t_1$  and  $t_2$  such that all of the constraints are consistent with the process actions in  $t_1$  and  $t_2$ , and recover all valid process interleavings. The key point of this definition is that we can recover all valid process interleavings despite the presence of constraints, i.e., that  $(t_1/\dot{\iota} \parallel t_2/\dot{\iota}) \subseteq (t_1 \parallel t_2)/\dot{\iota}$ ; we already know by proposition 5.5 that adding constraints cannot produce new process interleavings, i.e., that  $(t_1/\dot{\iota} \parallel t_2/\dot{\iota}) \supseteq (t_1 \parallel t_2)/\dot{\iota}$ .

*Example 6.1.* We illustrate how constraint satisfaction rules out incompatible compositions by attempting to compose the following two processes (respectively,  $P$  and  $Q$ ) in parallel:

$$\begin{aligned}
&\text{case } a \{ l \Rightarrow b.0; \text{ wait } a; \text{ close } b \}_{l \in \text{Bit}} \Vdash \cdot \# (a : \text{Bits}_1 \mid \cdot \vdash b : \text{Bits}_1) \parallel \mathfrak{T}_1 \\
&\text{case } a \{ l \Rightarrow b.l; \text{ wait } a; \text{ close } b \}_{l \in \text{Bit}} \Vdash a : \text{Bits}_1 \# (b : \text{Bits}_1 \mid \cdot \vdash c : \text{IdBitSeq}_1 a) \parallel \mathfrak{T}_2.
\end{aligned}$$

Their composition should not satisfy  $P \parallel_a Q \Vdash \cdot \# (a : \text{Bits}_1 \mid b : \text{Bits}_1 \vdash c : \text{IdBitSeq}_1 a) \parallel \mathfrak{T}$  because an input of 1 on  $a$  does not result in the output 1 on  $c$  specified by  $\text{IdBitSeq}_1 a$ . The following two traces violate (21) (they have no interleavings) and keep (PAR) from being applied:

$$\begin{aligned}
&(\text{?}; \text{label 1 on } a) :: (!; \text{label 0 on } b) :: (\text{?}; \text{close on } a) :: (!; \text{close on } b) :: \varepsilon \in \mathfrak{T}_1 \\
&(\text{?}; \text{label 0 on } b) :: (\dot{\iota}; \text{label 0 on } a) :: (\text{?}; \text{label 0 on } c) :: \dots \in \mathfrak{T}_2
\end{aligned}$$

The rules (R) and (L) uniformly identify the next channel  $a$  on which a process will communicate, and kick off the second phase that reduces the type of  $a$ . This channel  $a$  is called the **principal channel** of the process, and is given by  $\text{princ}(P) = a$  for

$$P := \text{close } a \mid \text{wait } a; Q \mid a.k; Q \mid \text{case } a \{ l \Rightarrow Q_l \}_{l \in L} \mid \text{send } a (b.Q_1); Q_2 \mid b \leftarrow \text{receive } a; Q.$$

We remark that there is exactly one rule in the first phase for each process forming construct.

## 6.2 Phase two: focussed type reduction

The second phase is driven by a pair of type-reduction judgments  $P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{T}$  and  $P \Rightarrow \Pi \# (\Delta, [a : A] \mid I \vdash c : C) \parallel \mathfrak{T}$  that reduce a focussed type  $[a : A]$  to weak head normal form, and checks that if the principal channel of  $P$  is  $a$ , then  $P$  communicates on  $a$  according to  $A$ . The right-focussed judgment is inductively defined by the rules of fig. 2 (weak head normal cases) and fig. 3 (reduction cases); the analogous left-focussed judgment exchanges sending and receiving.

$$\boxed{P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{I}} \quad \text{Focussed reduction of } a : A \text{ on the right}$$

$$\begin{array}{c}
\frac{}{\text{close } a \Rightarrow \Pi \# (\cdot \mid \cdot \vdash [a : 1]) \parallel \{(!; \text{close on } a) :: \varepsilon\}} (\Rightarrow 1R) \\
\\
\frac{P \Vdash \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash a : A_k) \parallel \mathfrak{I} \quad \pi = \text{label } k \text{ on } a}{a.k; P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : \oplus_{l \in L \cup \{k\}} A_l]) \parallel (!; \text{label } k \text{ on } a) :: \mathfrak{I}} (\Rightarrow \oplus R) \\
\\
\frac{P_l \Vdash \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash a : A_l) \parallel \mathfrak{I}_l \quad \pi = \text{label } l \text{ on } a \quad (\forall l \in L)}{\text{case } a \{ l \Rightarrow P_l \}_{l \in L \cup L'} \Rightarrow \Pi \# (\Delta \mid I \vdash [a : \&_{l \in L} A_l]) \parallel \bigcup_{l \in L} (?; \text{label } k \text{ on } a) :: \mathfrak{I}_l} (\Rightarrow \& R) \\
\\
\frac{P \Vdash (\Pi, \Delta_2, I_2)/\pi, a : A \# (\Delta_1/\pi \mid I_1/\pi \vdash b : B) \parallel \mathfrak{I}_1 \quad Q \Vdash (\Pi, \Delta_1, I_1)/\pi, b : B \# (\Delta_2/\pi \mid I_2/\pi \vdash a : A) \parallel \mathfrak{I}_2 \quad \pi = \text{chan } b \text{ on } a \quad \mathfrak{I}_1 \cdot \mathfrak{I}_2}{\text{send } a (b.P); Q \Rightarrow \Pi \# (\Delta_1, \Delta_2 \mid I_1, I_2 \vdash [a : (b : B) \otimes (a : A)]) \parallel (!; \text{chan on } a) :: b. (\mathfrak{I}_1 \parallel \mathfrak{I}_2)} (\Rightarrow \otimes R) \\
\\
\frac{P \Vdash \Pi/\pi \# (\Delta/\pi, b : B \mid I/\pi \vdash a : A) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } a}{b \leftarrow \text{receive } a; P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : (b : B) \multimap (a : A)]) \parallel (?; \text{chan on } a) :: b. \mathfrak{I}} (\Rightarrow \multimap R)
\end{array}$$

Fig. 2. Right-Focussed Process Typing — Focussed Type in Weak Head Normal Form

$$\begin{array}{c}
\frac{P \Rightarrow \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash [a : A]) \parallel \mathfrak{I} \quad \pi = \text{close on } c}{P \Rightarrow \Pi, c : 1 \# (\Delta \mid I \vdash [a : \text{CASE } c \{ \text{close} \Rightarrow A \}]) \parallel (!; \text{close on } c) :: \mathfrak{I}} (\Rightarrow 1[R]) \\
\\
\frac{P \Rightarrow \Pi/\pi_l, c : C_l \# (\Delta/\pi_l \mid I/\pi_l \vdash [a : A_l]) \parallel \mathfrak{I}_l \quad \pi_l = \text{label } l \text{ on } c \quad (\forall l \in L) \quad L \subseteq L' \subseteq L'' \quad \odot \in \{\oplus, \&\}}{P \Rightarrow \Pi, c : \odot_{l \in L'} C_l \# (\Delta \mid I \vdash [a : \text{CASE } c \{ l \Rightarrow A_l \}_{l \in L'}]) \parallel \bigcup_{l \in L} (!; \text{label } l \text{ on } c) :: \mathfrak{I}_l} (\Rightarrow \oplus \& [R]) \\
\\
\frac{P \Rightarrow \Pi/\pi, b : B, c : C \# (\Delta/\pi \mid I/\pi \vdash [a : A]) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } c \quad \odot \in \{\otimes, \multimap\}}{P \Rightarrow \Pi, c : (b : B) \odot (c : C) \# (\Delta \mid I \vdash [a : \text{CASE } c \{ \langle b \rangle \Rightarrow A \}]) \parallel (!; \text{chan on } c) :: b. \mathfrak{I}} (\Rightarrow \otimes \multimap [R])
\end{array}$$

Fig. 3. Right-Focussed Process Typing — Reducing the Type in Focus

For example, the rule  $(\Rightarrow 1R)$  of fig. 2 specifies that the process  $\text{close } a$  communicates according to the weak head normal type  $a : 1$ , provided it has empty client and internal contexts. The client context must be empty to ensure that we do not accidentally discard any clients. The internal context must be empty to ensure that we use all internal channels in the specification. We must do so, for the current process may have been spawned by a larger process (typed by channels in  $\Pi$ ) that observes channels in  $I$ , and we must not allow these channels to be discarded. Because no constraints are imposed on the environment, the output constraint set is exactly  $\llbracket \text{close } a \rrbracket$ .

The rule  $(\Rightarrow \oplus R)$  specifies that a process  $a.k; P$  is a server for a channel  $a : \oplus_{l \in L \cup \{k\}} A_l$  provided  $P$  is well-typed when  $a : A_k$  and the remainder of the specification has been reduced by label  $k$  on  $a$ . This reduction ensures that all types that depend on  $a$  observe the transmitted label  $k$ . The resulting constraint set is given by prefixing constraint set  $\mathfrak{I}$  for  $P$  by the output action  $(!, \text{label } k \text{ on } a)$ .

Channel transmission  $\text{send } a (b.P); Q$  spawns a process  $P$  that provides a fresh channel  $b$ , sends  $b$  over  $a$ , and continues as  $Q$ . Processes  $P$  and  $Q$  cannot share any channels. This is ensured by  $(\Rightarrow \otimes R)$ : it treats all channels used or provided by  $P$  as ambient in  $Q$ 's typing judgment and vice-versa. The rule also ensures that their specifications impose compatible constraints, analogously to  $(\text{PAR})$ .

Sometimes, the focussed channel is not yet weak head normal. In these cases, we must reduce it to a weak head normal type (see fig. 3). The rule  $(\Rightarrow 1[R])$  reduces a focussed channel of type  $\text{CASE } c \{ \text{close} \Rightarrow A \}$ . This reduction is justified so long as a close message is observed on ambient

channel  $c$ . This constraint is captured by prefixing the constraint ( $\zeta$ , close on  $c$ ) on the constraint set  $\mathfrak{T}$  generated by the premise. The rule ( $\Rightarrow \oplus \& [R]$ ) can generate constraints for a subset of labels in a choice to support composition with processes that only send a subset of the permitted labels. The remaining reduction rules are analogous. We remark that the rules of fig. 3 only reduce types that observe ambient channels. A focussed type that observes a used or provided channel cannot be reduced because we cannot reduce types based on future communications. Indeed, the observation required to reduce the type can only be provided by the process being checked, but the process will communicate on the focussed principal channel before it will communicate on the channel being observed. Types that observe local channels are instead immediately reduced by the rules of fig. 2.

*Example 6.2.* Set  $C = \text{CASE } a \{ \text{close} \Rightarrow 1 \}$  and  $P_{x,y} = (\text{wait } x; \text{close } y)$ . We illustrate the mechanics of our typechecking algorithm by attempting to typecheck  $v(b : 1) \cdot (P_{a,b} \parallel_b P_{b,c})$  against the specification  $\cdot \# (a : 1 \mid \vdash c : C)$ . Typechecking succeeds if we can build a derivation of  $v(b : 1) \cdot (P_{a,b} \parallel_b P_{b,c}) \Vdash \cdot \# (a : 1 \mid \vdash c : C) \parallel \mathfrak{T}$  for some  $\mathfrak{T}$ . First, we build a candidate derivation by considering only the inputs to the judgment (the red parts of the tree). The only possible bottom rule in our case is (NEW), and we use proof search to complete the derivation. The focussed nature of our system ensures that search amounts to inversion: in each case, at most one rule can be used to extend the derivation. Eventually each branch either gets stuck or reaches a leaf (an axiom). The following candidate derivation shows that we can successfully build the red portion of the tree:

$$\begin{array}{c}
 \frac{\frac{\frac{\text{close } b \Rightarrow c : 1 \# (\cdot \mid \vdash [b : 1]) \parallel \mathcal{L}_1}{\text{close } b \Rightarrow c : 1 \# (\cdot \mid \vdash b : 1) \parallel \mathcal{L}_1} (\Rightarrow 1R) \quad \frac{\frac{\text{close } c \Vdash \cdot \# (\cdot \mid \vdash [c : 1]) \parallel \mathcal{R}_1}{\text{close } c \Vdash a : 1 \# (\cdot \mid \vdash [c : C]) \parallel \mathcal{R}_2} (\Rightarrow 1[R])}{\text{close } c \Vdash a : 1 \# (\cdot \mid \vdash c : C) \parallel \mathcal{R}_2} (\parallel R)} (\Rightarrow 1L) \\
 \frac{P_{a,b} \Rightarrow c : C \# ([a : 1] \mid \vdash b : 1) \parallel \mathcal{L}_2}{P_{a,b} \Vdash c : C \# (a : 1 \mid \vdash b : 1) \parallel \mathcal{L}_2} (\parallel L) \quad \frac{\frac{\text{close } c \Vdash a : 1 \# (\cdot \mid \vdash [c : C]) \parallel \mathcal{R}_2}{P_{b,c} \Rightarrow a : 1 \# ([b : 1] \mid \vdash c : C) \parallel \mathcal{R}_3} (\Rightarrow 1L) \quad \frac{\text{close } c \Vdash \cdot \# (\cdot \mid \vdash [c : 1]) \parallel \mathcal{R}_1}{P_{b,c} \Vdash a : 1 \# (b : 1 \mid \vdash c : C) \parallel \mathcal{R}_3} (\parallel L)} (\parallel R) \\
 \frac{P_{a,b} \parallel_b P_{b,c} \Vdash \cdot \# (a : 1 \mid b : 1 \vdash c : C) \parallel \mathcal{L}_2 \parallel \mathcal{R}_3}{v(b : 1) \cdot (P_{a,b} \parallel_b P_{b,c}) \Vdash \cdot \# (a : 1 \mid \vdash c : C) \parallel (\mathcal{L}_2 \parallel \mathcal{R}_3) \setminus \{b\}} (\text{NEW})
 \end{array}$$

Assuming every branch reaches a leaf, the axioms specify the leaf's judgment's output (the trace set in blue). We thread these trace sets back down through the derivation tree, using the operations specified in each rule to build that rules output trace set. In this case:

$$\begin{aligned}
 \mathcal{L}_1 &= \{ (!; \text{close on } b) :: \varepsilon \} & \mathcal{L}_2 &= (?; \text{close on } a) :: \mathcal{L}_1 \\
 \mathcal{R}_1 &= \{ (!; \text{close on } c) :: \varepsilon \} & \mathcal{R}_2 &= (\zeta; \text{close on } a) :: \mathcal{R}_1 & \mathcal{R}_3 &= (?; \text{close on } b) :: \mathcal{R}_2 \\
 \mathcal{L}_2 \parallel \mathcal{R}_3 &= \{ (?; \text{close on } a) :: (\sigma; \text{close on } b) :: (!; \text{close on } c) :: \varepsilon \} \\
 (\mathcal{L}_2 \parallel \mathcal{R}_3) \setminus \{b\} &= \{ (?; \text{close on } a) :: (!; \text{close on } c) :: \varepsilon \}
 \end{aligned}$$

Typechecking succeeds if the resulting derivation is valid (including the side condition (21) for (PAR)).

Our rules determine a terminating typechecking algorithm when all choice types are indexed by finite label sets. Indeed, all constraint sets and their traces are finite, so trace operations terminate, and the alternating phases reduce the complexity of processes and their specifications. Most of the typechecking complexity lies in computing operations on traces. We conjecture that they could efficiently be implemented by representing traces as tries or radix trees.

## 7 SAFETY PROPERTIES

Our typechecking algorithm is sound: if we can typecheck a process against a process specification, then all executions of that process are permitted by its specification. Put differently, our typechecking algorithm ensures that well-typed processes communicate safely:

**THEOREM 7.1 (SOUNDNESS).** *The typechecking algorithm is sound: if any of*

$$(1) \ P \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T},$$

- (2)  $P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{T}$ , or  
 (3)  $P \Rightarrow \Pi \# (\Delta, [a : A] \mid I \vdash c : C) \parallel \mathfrak{T}$ ,

then  $\mathfrak{T}/_i = \llbracket P \rrbracket$  (the set  $\mathfrak{T}$  extends the executions of  $P$  with constraints) and  $\mathfrak{T} \subseteq \llbracket \Pi \# \Delta \mid I \vdash a : A \rrbracket$  (the constraint set  $\mathfrak{T}$  satisfies the specification  $\Pi \# \Delta \mid I \vdash a : A$ ).

**COROLLARY 7.2.** *If  $P \Vdash \cdot \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}$ , then  $\mathfrak{T} = \llbracket P \rrbracket$  and  $\llbracket P \rrbracket \subseteq \llbracket \cdot \# (\Delta \mid I \vdash a : A) \rrbracket$ .*

A key feature of MOST is its modular approach to process specification. Processes whose specifications impose no constraints on their environment can always be composed:

**PROPOSITION 7.3 (COMPOSITIONALITY).** *If  $P \Vdash \cdot \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1$  and  $Q \Vdash \cdot \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2$ , then  $P \parallel_a Q \Vdash \cdot \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2$ .*

## 8 EXTENDING MOST

MOST provides a foundation for the development of session-typed languages with rich specifications. In this section, we conjecturally sketch extensions along several axes that allow MOST to capture richer computational phenomena: selection, asynchrony, value dependency, and recursion.

### 8.1 Selection

Go [The Go Project 2024] includes a *select* statement that, given a collection of communication actions to perform, randomly performs one from the set of actions that are possible. For example, if processes  $P$  and  $Q$  attempt to communicate with a process  $R$ , then process  $R$  could use a select statement to communicate with the first of the two processes that is ready. We show how to extend MOST with a linear variant of a select statement building on ideas from differential linear logic [Ehrhard 2018] and its applications to concurrency [Rocha and Caires 2021, 2023]. Differential linear logic offers a means of *adding* proofs of the same sequent. In the intuitionistic case, if we ascribe the sequents of intuitionistic linear logic with proof terms (processes), we get the rule

$$\frac{\Gamma \vdash P :: a : A \quad \Gamma \vdash Q :: a : A}{\Gamma \vdash P + Q :: a : A}$$

The possible behaviours of  $P + Q$  are all those of  $P$  or  $Q$ , so the sum denotes  $\llbracket P + Q \rrbracket = \llbracket P \rrbracket \cup \llbracket Q \rrbracket$ . This clause satisfies the identities described by Rocha and Caires, namely, that process composition distributes over sums and that all processes are idempotent with regard to the sum, i.e., that  $P + P \equiv P$  for all  $P$ . Our discussion suggests the following typing rule for sums of MOST processes:

$$\frac{P_1 \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}_1 \quad P_2 \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}_2}{P_1 + P_2 \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}_1 \cup \mathfrak{T}_2} \text{ (SUM)}$$

Returning to our motivating example, suppose that we want to receive channels  $a$  and  $c$  over  $b$  and  $d$ , respectively, but that we do not want to impose a particular order on which of  $b$  or  $d$  is used first. The following sum of processes receives on whichever of  $b$  or  $d$  is ready first, non-deterministically breaking any ties, and communicates over the other before continuing as  $R'$ :

$$(a \leftarrow \text{receive } b; c \leftarrow \text{receive } d; R') + (c \leftarrow \text{receive } d; a \leftarrow \text{receive } b; R').$$

### 8.2 Asynchrony

Adapting MOST to use an asynchronous communication semantics is straightforward, but it comes at the cost of technical complexity. The intended semantics treats channels as unbounded FIFO buffers, where processes can always send (add a message to a buffer), and they can receive a message if one is buffered. We implement FIFO buffers using *continuation channels*, where each channel carries exactly one message, and each message consists of the datum plus the name of the (continuation) channel that will carry the next message. To preclude interference between channels, continuation channel names are globally fresh. To illustrate, consider the trace that describes sending on  $a$  the label  $l$ ,

receiving label  $k$ , and closing  $a$ . In the synchronous setting, we simply send the three messages on  $a$ , giving a trace  $(!; \text{label } l \text{ on } a) :: (?; \text{label } k \text{ on } a) :: (!; \text{close on } a) :: \varepsilon$ . In the asynchronous setting, the label  $l$  is paired with a fresh name  $a_1$  when sent on  $a$ ; label  $k$  is paired with a fresh name  $a_2$  and sent on  $a_1$ ; and the close message is sent on  $a_2$ . To enforce global freshness, ensure that trace sets remain finite, and preserve compositionality, we model continuation channels in traces by bound channel names. The corresponding trace is  $(!; \text{label } l \text{ on } a) :: a_1 . (?; \text{label } k \text{ on } a_1) :: a_2 . (!; \text{close on } a_2) :: \varepsilon$ , where  $a_1$  and  $a_2$  are bound. Modifying the semantic clauses to capture asynchronous communication is then a matter of adapting the trace operations of section 5 to handle continuation channels, and of threading bindings for continuation channels throughout the semantic clauses. For example, eq. (3) becomes  $\llbracket a.k; P \rrbracket = (!; \text{label } k \text{ on } a) :: a . \llbracket P \rrbracket$ , where  $a$  is bound in the traces in  $\llbracket P \rrbracket$ . To type processes in the asynchronous setting, we must extend the syntax of observed communications  $\pi$  to include the name of the continuation channel, and extend type reductions  $A/\pi$  to substitute the continuation channel for the carrier of  $\pi$  in  $A$ . Because type reduction is localized to the operation  $A/\pi$  and all reductions occur in lockstep in the typing rules, we never need to worry about a continuation channel name going out of scope, and no other changes are required.

### 8.3 Value-dependent session types

MOST can be extended to support value-dependent session types [Toninho, Caires, et al. 2011], where session types can depend on values drawn from an underlying dependent functional language. Value-dependent type formers  $\forall x : \tau . A$  and  $\exists x : \tau . A$  respectively specify servers that receive or send values  $v$  of type  $\tau$  and then communicate according to  $[v/x]A$ . We first extend our syntax:

Processes	$P ::= \dots$	send $M$ on $a$ ; $P$	Evaluate term $M$ and send its value on $a$
		let $x = \text{rcv } a$ in $P$	Bind received value to $x$ in $P$
Session types	$A ::= \dots$	$\forall x : \tau . A$	Dependent value receiving
		$\exists x : \tau . A$	Dependent value sending
		CASE $a \{ \text{val } x \Rightarrow A \}$	Value observation

Value observation CASE  $a \{ \text{val } x \Rightarrow A \}$  observes a transmitted value  $v$  on a channel  $a$  and reduces to the type  $[v/x]A$ . Its reduction is defined analogously to the other immediate reductions:

$$(\text{CASE } c \{ \text{val } x \Rightarrow A \})/\pi = \begin{cases} [M/x]A & \text{if } \pi = \text{val } M \text{ on } c \\ \text{CASE } c \{ \text{val } x \Rightarrow A/\pi \} & \text{if } \text{cc}(\pi) \neq c \end{cases}$$

Write  $\text{val}[\tau]$  for the sets of closed values of type  $\tau$ . We give meaning to value transmission by allowing terms to be embedded in traces:<sup>3</sup>

$$\llbracket \text{send } M \text{ on } a; P \rrbracket = (!; \text{val } M \text{ on } a) :: \llbracket P \rrbracket, \quad \llbracket \text{let } x = \text{rcv } a \text{ in } P \rrbracket = \bigcup_{v \in \text{val}[\tau]} (?; \text{val } v \text{ on } a) :: \llbracket P \rrbracket.$$

We then extend interleaving to use the underlying definitional equality on terms so that whenever  $M$  and  $v$  are definitionally equal,  $((!; \text{val } M \text{ on } a) :: t_1) \parallel ((?; \text{val } v \text{ on } a) :: t_2) = (\sigma; \text{val } v \text{ on } a) :: (t_1 \parallel t_2)$

To type processes that use values, we assume a typing judgment  $\Gamma \vdash_f M : \tau$  for the functional language and extend the process typing judgment to include  $\Gamma$ . The right process typing rules follow a similar pattern as for label transmission, and the other rules follow analogously:

$$\frac{P \Vdash \Gamma, x : \tau; \Pi/\pi \# (\Delta/\pi \vdash I/\pi \vdash a : A) \parallel \mathcal{T} \quad \pi = \text{val } x \text{ on } a}{\text{let } x = \text{rcv } a \text{ in } P \Rightarrow \Gamma; \Pi \# (\Delta \vdash I \vdash [a : \forall x : \tau . A]) \parallel \bigcup_{v \in \text{val}[\tau]} (?; \text{val } v \text{ on } a) :: [v/x]\mathcal{T}} (\Rightarrow \forall R)$$

$$\frac{P \Vdash \Gamma; \Pi/\pi \# (\Delta/\pi \vdash I/\pi \vdash a : [M/x]A) \parallel \mathcal{T} \quad \Gamma \vdash_f M : \tau \quad \pi = \text{val } M \text{ on } a}{\text{send } M \text{ on } a; P \Rightarrow \Gamma; \Pi \# (\Delta \vdash I \vdash [a : \exists x : \tau . A]) \parallel (!; \text{val } M \text{ on } a) :: \mathcal{T}} (\Rightarrow \exists R)$$

<sup>3</sup>Extending our semantics to capture evaluation in the underlying dependent type theory is beyond the scope of this sketch.

$$\frac{P \Vdash \Gamma, x : \tau; \Pi / \pi \# (\Delta / \pi \vdash I / \pi \vdash a : A) \parallel \mathcal{T} \quad \pi = \text{val } x \text{ on } a \quad \# \in \{\forall, \exists\}}{P \Rightarrow \Gamma; \Pi, c : (\#x : \tau.C) \# (\Delta \vdash I \vdash [a : \text{CASE } c \{ \text{val } x \Rightarrow A \}]) \parallel \bigcup_{v \in \text{val}[\tau]} (\dot{c}; \text{val } v \text{ on } a) :: [v/x] \mathcal{T}} (\Rightarrow \forall \exists [R])$$

#### 8.4 Recursion

MOST can be extended to support mutually recursive types and processes using coinductively defined typing judgments. We implement mutually recursive processes using process declarations  $f(\vec{x}) = P_f$  gathered in an implicit global context  $\Sigma_P$ . These declarations specify that the process named  $f$  is given by its implementation  $P_f$  involving channels  $\vec{x}$ . The syntax  $\text{call } f(\vec{a})$  calls process  $f$  with channel names  $\vec{a}$ , and it corresponds to running  $[\vec{a}/\vec{x}]P_f$ . Equirecursive types are given by type declarations  $X[\vec{x}] = A_X$  gathered in an implicit global context  $\Sigma_A$ . Channel name variables  $\vec{x}$  specify the channels that  $A_X$  can observe, and  $X[\vec{a}]$  is definitionally equal to  $[\vec{a}/\vec{x}]A_X$ . A complete program  $\mathbb{P}$  is a called process named paired with explicit declarations for each called name.

Processes	$P ::= \dots \mid \text{call } f(\vec{a})$	Call named process $f$ with channels $\vec{a}$
Session types	$A ::= \dots \mid X[\vec{x}]$	Equirecursive type names
Type declarations	$\Sigma_A ::= \cdot \mid \Sigma_A, X[\vec{x}] = A_X$	$X[\vec{x}]$ is definitionally equal to $A_X$
Process declarations	$\Sigma_P ::= \cdot \mid \Sigma_P, f(\vec{x}) = P_f$	Process named $f$ is implemented by $P_f$
Programs	$\mathbb{P} ::= \text{call } f(\vec{a}) \text{ where } \Sigma_P$	Complete or top-level program

We assume that recursive declarations are contractive [Pierce 2002, p. 300]. This standard assumption simplifies our theory and allows us to interpret equirecursive types as regular trees. To ensure type reduction  $A/\pi$  remains well defined, we interpret its definition coinductively instead of inductively.

*Example 8.1.* For each fixed type  $A$ , the type  $\text{list}_A = \oplus\{\text{nil} : 1, \text{cons} : A \otimes \text{list}_A\}$  specifies lists of channels of type  $A$ . The type  $\text{stream}_A = \&\{\text{head} : A, \text{tail} : \text{stream}_A\}$  specifies a stream of channels of type  $A$ . Given a process declaration  $u(x) = (\text{case } x \{ \text{head} \Rightarrow \text{close } x \mid \text{tail} \Rightarrow \text{call } u(x) \})$ , the process “ $\text{call } u(a)$ ” is a closed process providing  $a : \text{stream}_1$ .

*Example 8.2.* The types  $\text{Bits}$  and  $\text{IdBitSeq } a$  and the process  $\text{id}$  from sections 2.1 and 2.2 are encoded by the following declarations, where we use the label  $\$$  and type  $1$  to specify finite sequences:

$$\begin{aligned} \text{Bits} &= \oplus\{\$ : 1, \emptyset : \text{Bits}, 1 : \text{Bits}\} \\ \text{IdBitSeq}[x] &= \text{CASE } x \{ \$ \Rightarrow \oplus\{\$ : \text{CASE } x \{ \text{close} \Rightarrow 1 \}\} \\ &\quad \mid \emptyset \Rightarrow \oplus\{\emptyset : \text{IdBitSeq}[x]\} \mid 1 \Rightarrow \oplus\{1 : \text{IdBitSeq}[x]\} \} \\ \text{id}(x, y) &= \text{case } x \{ \$ \Rightarrow \text{wait } x; \text{close } y \mid \emptyset \Rightarrow y.\emptyset; \text{call id}(x, y) \mid 1 \Rightarrow y.1; \text{call id}(x, y) \}. \end{aligned}$$

We extend our semantics to support calling named processes. Let  $\mathcal{P}$  be the set of declared calling interfaces  $f(\vec{x})$ . A process environment is a map  $\rho : \mathcal{P} \rightarrow \mathbf{TSets}$  giving the meaning of declared processes, where  $\mathbf{TSets}$  is the lattice of sets of traces ordered by inclusion. We generalize  $\llbracket P \rrbracket$  from an element of  $\mathbf{TSets}$  to a continuous map  $\llbracket P \rrbracket : (\mathcal{P} \rightarrow \mathbf{TSets}) \rightarrow \mathbf{TSets}$ . Calling  $f(\vec{x})$  with channels  $\vec{a}$  means retrieving its meaning from the environment and instantiating it with the names  $\vec{a}$ :

$$\llbracket \text{call } f(\vec{a}) \rrbracket \rho = \{ [\vec{a}/\vec{x}]t \mid t \in \rho(f(\vec{x})) \}.$$

We adapt the existing clauses by threading through the environments, e.g.:  $\llbracket P \parallel_a Q \rrbracket \rho = \llbracket P \rrbracket \rho \parallel \llbracket Q \rrbracket \rho$ . To capture parallel composition in the presence of potentially infinite traces, we must extend  $\parallel$  to be a fair merge operator. This ensures that each element from each input traces appears at a finite depth in the interleavings. We omit the details due to space constraints.

Complete programs are processes executed in an environment where every called process name has a corresponding declaration. Their semantics are given by

$$\begin{aligned} \llbracket \text{call } f(\vec{a}) \text{ where } \Sigma_P \rrbracket &= \llbracket \text{call } f(\vec{a}) \rrbracket \llbracket \Sigma_P \rrbracket, \text{ where} \\ \llbracket f_1(\vec{x}_1) = P_1, \dots, f_n(\vec{x}_n) = P_n \rrbracket &= \text{FIX}(\lambda \rho \in (\mathcal{P} \rightarrow \mathbf{TSets}). [\rho \mid f_1(\vec{x}_1) \mapsto \llbracket P_1 \rrbracket \rho \mid \dots \mid f_n(\vec{x}_n) \mapsto \llbracket P_n \rrbracket \rho]), \end{aligned}$$

FIX is the continuous fixed-point operator on  $\mathcal{P} \rightarrow \mathbf{Tsets}$ , and  $[g \mid y_1 \mapsto v_1 \mid \dots \mid y_n \mapsto v_n](x)$  is  $v_i$  if  $x = y_i$  and  $g(x)$  otherwise. Intuitively,  $\llbracket \Sigma_P \rrbracket$  maps each  $f_i(\vec{x}_i)$  to the meaning of  $\llbracket P_i \rrbracket$  in the fixed point giving meaning to the mutually recursive declarations.

*Example 8.3.* If  $\Sigma_P = (f(x) = x.1; \text{ call } g(x)), (g(x) = x.0; \text{ call } f(x))$ , then

$$\begin{aligned} \llbracket \Sigma_P \rrbracket &= \text{FIX}(\lambda \rho. [\rho \mid f \mapsto (!; \text{ label } 1 \text{ on } x) :: \rho(g) \mid g \mapsto (!; \text{ label } 0 \text{ on } x) :: \rho(f)]) \\ \llbracket \text{ call } f(a) \text{ where } \Sigma_P \rrbracket &= \{ (!; \text{ label } 1 \text{ on } a) :: (!; \text{ label } 0 \text{ on } a) :: (!; \text{ label } 1 \text{ on } a) :: \dots \}. \end{aligned}$$

Process specifications still denote sets  $\llbracket \Pi \# \mathcal{G} \rrbracket$  of traces satisfying the specification. However, to capture recursive behaviour, we adapt its definition to use a coinductively defined elementhood relation. Elementhood is given by a coinductive reading of the rules defining  $t \in \llbracket \Pi \# \mathcal{G} \rrbracket$  generated by the obvious analogs of section 4.1, e.g.:

$$\frac{t \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \rrbracket \quad \pi = \text{close on } a}{(!; \text{ close on } a) :: t \in \llbracket \Pi \# \mathcal{G} \mid (\cdot \mid \cdot \vdash a : 1) \rrbracket} \quad \frac{t \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid \text{I}/\pi \vdash a : A_k) \rrbracket \quad \pi = \text{label } k \text{ on } a}{(!; \text{ label } k \text{ on } a) :: t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid \text{I} \vdash a : \oplus_{l \in L} A_l) \rrbracket}$$

In order to type mutually recursive processes, we give our typing rules a coinductive reading and treat each component as an **input** to the judgment. Then, we extend our rules with:

$$\frac{(\vec{a}/\vec{x})P_f \Vdash \Pi \# (\Delta \mid \text{I} \vdash c : C) \parallel \mathcal{T} \quad (f(\vec{x}) = P_f) \in \Sigma_P}{\text{ call } f(\vec{a}) \Vdash \Pi \# (\Delta \mid \text{I} \vdash c : C) \parallel \mathcal{T}} \text{ (CALL)}$$

This system can be given a finitary presentation by moving from arbitrary coinductive derivations to circular derivations (finite derivations with loops) [Derakhshan and Pfenning 2022; Fortier and Santocanale 2013]. We refer the reader to [Somayyaajula and Pfenning 2022] for a discussion of how to do so to achieve typechecking in finite time, and also a discussion of how to encode inductive and coinductive types using value dependency and (general) equirecursive types.

## 9 RELATED WORK

MOST builds on linear-logical foundations of session types pioneered by Caires and Pfenning [2010]. Indeed, the simply typed fragment of our system is based on a proofs-as-processes interpretation of multiplicative-additive intuitionistic linear logic (MAILL), where propositions correspond to binary session types that specify communications on communication channels. A key difference with our work is that such interpretations typically combine parallel composition and hiding into a single operation. This is because process composition corresponds to the cut rule of linear logic. In contrast, we have separate parallel composition and hiding operators, and we track internal channels. This separation ensures that composition does not hide channels observed by specifications, thereby ensuring that specifications remain well-defined after process composition. We can recover cut-style process composition with a derived rule. Our uniform process typing system can then be seen as a conservative extension of typical interpretations of identity-free MAILL. Another key difference is our use of focussing. Processes can be specified with observing types, but we can only check specific communication actions (e.g., sending a label) against weak head normal types (e.g., an internal choice). This means that to type check a process, we must reduce the type of its principal channel to weak head normal form, potentially generating constraints on ambient communications along the way. Our focussing approach ensures that we only reduce the types of principal channels. This avoids the premature or spurious generation of constraints on ambient channels that could affect constraint compatibility in process composition, and it ensures that type checking is deterministic.

There are various dependent type systems for binary session types. Toninho, Caires, et al. [2011] and Toninho and Yoshida [2018] interpret logical quantifiers as value-dependent session types in languages with functional programming features. As discussed in section 8.3, value-dependent session types depend on transmitted functional values. Variations include label-dependent session

types [Thiemann and Vasconcelos 2019], which reify labels as a first-class type, and arithmetic refinements [Das and Pfenning 2020], which offer type-level arithmetic using transmitted natural numbers. Like other binary session types, value-dependent session types take a channel-local approach: a type only considers communication on a single channel. In contrast, types in *MOST* specify protocols on individual channels while potentially depending on ambient communications.

Multiparty session types [Honda, Yoshida, et al. 2016] are an alternative approach to specifying systems of communicating processes. Instead of specifying communications on individual channels, multiparty session types specify interactions between multiple parties using a global type, and then for each party project a local type specifying its interactions with the other parties. Though multiparty session types can specify complex interactions, they are not compositional and typically require that entire systems be specified at once. To achieve compositionality, Stolze et al. [2023] introduced *partial multiparty sessions types*, which specify multiparty sessions where some parties may be missing. Determining when partial sessions can be composed and the resulting composition is technically complex. In contrast, *MOST* is designed to ensure compositional process specifications, and the composition of process specifications is determined by a handful of inference rules.

The dynamics of processes in proofs-as-processes interpretations of intuitionistic linear logic is often given by multiset rewriting semantics [Cervesato and Scedrov 2009]. We instead describe the behaviour of processes using a trace semantics. We do so to simplify relating the meaning of processes (a set of traces) with the meaning of process specifications (a set of allowed traces). It also provides a means of ensuring that processes and specifications are mutually compatible when typechecking their composition. We conjecture that our trace semantics captures the same observable communication behaviours [Atkey 2017; Kavanagh 2022] as multiset-based semantics. It is challenging to define trace semantics or ordered semantics for process calculi with name generation or  $\alpha$ -conversion. Some approaches, e.g., typed event structures for  $\pi$ -calculus [Varacca and Yoshida 2010], specify in advance all names that will be created; others use complex semantic composition operators involving renamings [Crafa et al. 2007]. In contrast, we use binding to range over all possible fresh names in a manner reminiscent of higher-order abstract syntax. This avoids unintended name clashes when interleaving traces during process composition. Our compact presentation also minimizes the number of traces considered by our typechecking algorithm. Traces with binding are similar to nominal sequences [Gabbay and Ghica 2012], which use a coabstraction operator to bind atoms appearing later in a sequence. In contrast, our traces bind not atoms, but names found in later messages. As a result, our traces more closely resemble abstract binding trees.

## 10 CONCLUSION

In this work we describe *MOST*, a message-observing binary session type system. It uses type-level processes to specify protocols that vary based on ambient communications. This allows us to express more precise safety guarantees that cannot be expressed by prior work.

Our main goal in designing *MOST* is to ensure compositionality. We achieve this using a novel trace-based semantics that uses traces with bindings to compactly handle higher-order communications. Our main technical results are type safety and compositionality for well-typed processes.

In the future, we plan to extend *MOST* with shared channels. This would allow *MOST* to capture shared data structures like shared queues, and shared services like databases.

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## A OMITTED RULES

The following rules are those of figs. 2 and 3, plus any rules omitted from those figures:

$\frac{P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{I}}{P \Rightarrow \Pi \# (\Delta, [a : A] \mid I \vdash c : C) \parallel \mathfrak{I}}$	Focussed reduction of $a : A$
--	-------------------------------

$$\frac{}{\text{close } a \Rightarrow \Pi \# (\cdot \mid \cdot \vdash [a : 1]) \parallel \{(!; \text{close on } a) :: \varepsilon\}} (\Rightarrow 1R)$$

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash c : C/\pi) \parallel \mathfrak{I} \quad \pi = \text{close on } a}{\text{wait } a; P \Rightarrow \Pi \# (\Delta, [a : 1] \mid I \vdash c : C) \parallel (?; \text{close on } a) :: \mathfrak{I}} (\Rightarrow 1L)$$

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash a : A_k) \parallel \mathfrak{I} \quad \pi = \text{label } k \text{ on } a}{a.k; P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : \oplus_{l \in L \cup \{k\}} A_l]) \parallel (!; \text{label } k \text{ on } a) :: \mathfrak{I}} (\Rightarrow \oplus R)$$

$$\frac{P_l \Vdash \Pi/\pi \# (\Delta/\pi, a : A_l \mid I/\pi \vdash c : C/\pi) \parallel \mathfrak{I}_l \quad \pi = \text{label } l \text{ on } a \quad (\forall l \in L)}{\text{case } a \{ l \Rightarrow P_l \}_{l \in L \cup L'} \Rightarrow \Pi \# (\Delta, [a : \oplus_{l \in L} A_l] \mid I \vdash c : C) \parallel \bigcup_{l \in L} (?; \text{label } k \text{ on } a) :: \mathfrak{I}_l} (\Rightarrow \oplus L)$$

$$\frac{P_l \Vdash \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash a : A_l) \parallel \mathfrak{I}_l \quad \pi = \text{label } l \text{ on } a \quad (\forall l \in L)}{\text{case } a \{ l \Rightarrow P_l \}_{l \in L \cup L'} \Rightarrow \Pi \# (\Delta \mid I \vdash [a : \&_{l \in L} A_l]) \parallel \bigcup_{l \in L} (?; \text{label } k \text{ on } a) :: \mathfrak{I}_l} (\Rightarrow \& R)$$

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi, a : A_k \mid I/\pi \vdash c : C/\pi) \parallel \mathfrak{I} \quad \pi = \text{label } k \text{ on } a}{a.k; P \Rightarrow \Pi \# (\Delta, [a : \&_{l \in L \cup \{k\}} A_l] \mid I \vdash c : C) \parallel (!; \text{label } k \text{ on } a) :: \mathfrak{I}} (\Rightarrow \& L)$$

$$\frac{P \Vdash (\Pi, \Delta_2, I_2)/\pi, a : A \# (\Delta_1/\pi \mid I_1/\pi \vdash b : B) \parallel \mathfrak{I}_1 \quad Q \Vdash (\Pi, \Delta_1, I_1)/\pi, b : B \# (\Delta_2/\pi \mid I_2/\pi \vdash a : A) \parallel \mathfrak{I}_2 \quad \pi = \text{chan } b \text{ on } a \quad \mathfrak{I}_1 \cdot \mathfrak{I}_2}{\text{send } a (b.P); Q \Rightarrow \Pi \# (\Delta_1, \Delta_2 \mid I_1, I_2 \vdash [a : (b : B) \otimes (a : A)]) \parallel (!; \text{chan on } a) :: b.(\mathfrak{I}_1 \parallel \mathfrak{I}_2)} (\Rightarrow \otimes R)$$

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi, b : B, a : A \mid I/\pi \vdash c : C/\pi) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } a}{b \leftarrow \text{receive } a; P \Rightarrow \Pi \# (\Delta, [a : (b : B) \otimes (a : A)] \mid I \vdash c : C) \parallel (?; \text{chan on } a) :: b.\mathfrak{I}} (\Rightarrow \otimes L)$$

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi, b : B \mid I/\pi \vdash a : A) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } a}{b \leftarrow \text{receive } a; P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : (b : B) \multimap (a : A)]) \parallel (?; \text{chan on } a) :: b.\mathfrak{I}} (\Rightarrow \multimap R)$$

$$\frac{P \Vdash (\Pi, \Delta_2, I_2)/\pi, a : A \# (\Delta_1/\pi \mid I_1/\pi \vdash b : B) \parallel \mathfrak{I}_1 \quad Q \Vdash (\Pi, \Delta_1, I_1)/\pi, b : B \# (\Delta_2/\pi, a : A \mid I_2/\pi \vdash c : C/\pi) \parallel \mathfrak{I}_2 \quad \pi = \text{chan } b \text{ on } a \quad \mathfrak{I}_1 \cdot \mathfrak{I}_2}{\text{send } a (b.P); Q \Rightarrow \Pi \# (\Delta_1, \Delta_2, [a : (b : B) \multimap (a : A)] \mid I \vdash c : C) \parallel (!; \text{chan on } a) :: b.(\mathfrak{I}_1 \parallel \mathfrak{I}_2)} (\Rightarrow \multimap L)$$

$$\frac{P \Rightarrow \Pi/\pi \# (\Delta/\pi \mid I/\pi \vdash [a : A]) \parallel \mathfrak{I} \quad \pi = \text{close on } c}{P \Rightarrow \Pi, c : 1 \# (\Delta \mid I \vdash [a : \text{CASE } c \{ \text{close} \Rightarrow A \}]) \parallel (;; \text{close on } c) :: \mathfrak{I}} (\Rightarrow 1[R])$$

$$\frac{P \Rightarrow \Pi/\pi \# (\Delta/\pi, [a : A] \mid I/\pi \vdash d : D/\pi) \parallel \mathfrak{I} \quad \pi = \text{close on } c}{P \Rightarrow \Pi, c : 1 \# (\Delta, [a : \text{CASE } c \{ \text{close} \Rightarrow A \}] \mid I \vdash d : D) \parallel (;; \text{close on } c) :: \mathfrak{I}} (\Rightarrow 1[L])$$

$$\frac{P \Rightarrow \Pi/\pi_l, c : C_l \# (\Delta/\pi_l \mid I/\pi_l \vdash [a : A_l]) \parallel \mathfrak{I}_l \quad \pi_l = \text{label } l \text{ on } c \quad (\forall l \in L) \quad L \subseteq L' \subseteq L'' \quad \odot \in \{\oplus, \&\}}{P \Rightarrow \Pi, c : \odot_{l \in L'} C_l \# (\Delta \mid I \vdash [a : \text{CASE } c \{ l \Rightarrow A_l \}_{l \in L'}]) \parallel \bigcup_{l \in L'} (;; \text{label } l \text{ on } c) :: \mathfrak{I}_l} (\Rightarrow \oplus \& [R])$$

$$\frac{P \Rightarrow \Pi/\pi_l, c : C_l \# (\Delta/\pi_l, [a : A_l] \mid I/\pi_l \vdash d : D/\pi_l) \parallel \mathfrak{I}_l \quad \pi_l = \text{label } l \text{ on } c \quad (\forall l \in L) \quad \odot \in \{\oplus, \&\}}{P \Rightarrow \Pi, c : \odot_{l \in L} C_l \# (\Delta, [a : \text{CASE } c \{ l \Rightarrow A_l \}_{l \in L \cup L'}] \mid I \vdash d : D) \parallel \bigcup_{l \in L} (;; \text{label } l \text{ on } c) :: \mathfrak{I}_l} (\Rightarrow \oplus \& [L])$$

$$\frac{P \Rightarrow \Pi/\pi, b : B, c : C \# (\Delta/\pi \mid I/\pi \vdash [a : A]) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } c \quad \odot \in \{\otimes, \multimap\}}{P \Rightarrow \Pi, c : (b : B) \odot (c : C) \# (\Delta \mid I \vdash [a : \text{CASE } c \{ \langle b \rangle \Rightarrow A \}]) \parallel (;; \text{chan on } c) :: b.\mathfrak{I}} (\Rightarrow \otimes \multimap [R])$$

$$\frac{P \Rightarrow \Pi/\pi, b : B, c : C \# (\Delta/\pi, [a : A] \mid I/\pi \vdash d : D/\pi) \parallel \mathfrak{I} \quad \pi = \text{chan } b \text{ on } c \quad \odot \in \{\otimes, \multimap\}}{P \Rightarrow \Pi, c : (b : B) \odot (c : C) \# (\Delta, [a : \text{CASE } c \{ \langle b \rangle \Rightarrow A \}] \mid I \vdash d : D) \parallel (;; \text{chan on } c) :: b.\mathfrak{I}} (\Rightarrow \otimes \multimap [L])$$

## B OMITTED SEMANTIC CLAUSES

The following clauses were omitted from section 4.1.

Where  $\pi = \text{label } k \text{ on } a$ ,  $m = \text{label } k \text{ on } a$ , and  $k \in L$ :

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : \&_{l \in L} A_l) \rrbracket \supseteq (?; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash a : A_k) \rrbracket \quad (22)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta, a : \&_{l \in L} A_l \mid I \vdash c : C) \rrbracket \supseteq (!; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi, a : A_k \mid I/\pi \vdash c : C/\pi) \rrbracket \quad (23)$$

$$\llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, a : \&_{l \in L} A_l \vdash c : C) \rrbracket \supseteq (\sigma; m) :: \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi, a : A_k \vdash c : C/\pi) \rrbracket \quad (24)$$

Where  $\pi = (\text{chan } b \text{ on } a)$  with  $b$  fresh, and  $m = \text{chan on } a$ :

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, a : (b : B) \otimes (a : A) \vdash c : C) \rrbracket \\ & \supseteq (\sigma; \pi) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi, b : B, a : A \vdash c : C/\pi) \rrbracket \end{aligned} \quad (25)$$

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : (b : B) \multimap (a : A)) \rrbracket \\ & \supseteq (?; m) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi, b : B \mid I/\pi \vdash a : A) \rrbracket \end{aligned} \quad (26)$$

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, a : (b : B) \multimap (a : A) \vdash c : C) \rrbracket \\ & \supseteq (\sigma; \pi) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi, b : B, a : A \vdash c : C/\pi) \rrbracket \end{aligned} \quad (27)$$

$$\begin{aligned} & \llbracket \Pi \# \mathcal{G} \mid (\Delta_1, \Delta_2, a : (b : B) \multimap (a : A) \mid I_1, I_2 \vdash c : C) \rrbracket \\ & \supseteq (!; m) :: b. \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta_1/\pi \mid I_1/\pi \vdash b : B) \mid (\Delta_2/\pi, a : A \mid I_2/\pi \vdash c : C/\pi) \rrbracket \end{aligned} \quad (28)$$

## C PROOFS

PROPOSITION 3.3 (TERMINATION). *For all processes  $P$  and traces  $t \in \llbracket P \rrbracket$ ,  $t$  is finite.*

PROOF OF PROPOSITION 3.3. By induction on the structure of  $P$ , observing that each semantic clause extends a finite trace by a finite number of actions.  $\square$

To help characterize the behaviour of reduction on traces with the behaviour of type reduction in proposition C.4, we introduce a reduction relation on contexts and specifications, and a few technical lemmas (lemmas C.1 and C.2 and proposition C.3). We assume that bound names are unique and disjoint from free names. The relation is defined by the following schemata, which hold only if both sides of the arrow are defined:

$$\Pi, a : \mathbf{1} \xrightarrow{\text{close on } a} \Pi / (\text{close on } a) \quad (29)$$

$$\Pi, a : \odot_{l \in L} A_l \xrightarrow{\text{label } l \text{ on } a} \Pi / (\text{label } l \text{ on } a), a : A_l \quad (l \in L, \odot \in \{ \oplus, \& \}) \quad (30)$$

$$\Pi, a : (b : B) \odot (a : A) \xrightarrow{\text{chan } b \text{ on } a} \Pi / (\text{chan } b \text{ on } a), b : B, a : A \quad (\odot \in \{ \otimes, \multimap \}) \quad (31)$$

The relation  $\xrightarrow{(\dot{\iota}, \pi)}$  on pairs of well-formed specifications is defined as:

$$\Pi \# \mathcal{G} \xrightarrow{(\dot{\iota}, \pi)} \Pi' \# \mathcal{G}/\pi \quad \text{if } \Pi \xRightarrow{\pi} \Pi' \quad (32)$$

We invite the reader to compare reduction (32) with the clauses generating traces of the form  $(\dot{\iota}; m) :: \vec{x}. t$  in section 4.1.

LEMMA C.1 (PARTIAL COMMUTATIVITY OF REDUCTION). *Let  $\pi_1$  and  $\pi_2$  be observed communications such that  $\text{fc}(\pi_1)$  and  $\text{fc}(\pi_2)$  are disjoint.*

- (1) *Both  $A/\pi_1$  and  $A/\pi_2$  are defined if and only if both  $(A/\pi_1)/\pi_2$  and  $(A/\pi_2)/\pi_1$  are defined.*
- (2) *If  $(A/\pi_1)/\pi_2$  and  $(A/\pi_2)/\pi_1$  are both defined, then they are equal.*

PROOF OF LEMMA C.1. It is immediate that if  $(A/\pi_1)/\pi_2$  and  $(A/\pi_2)/\pi_1$  are both defined, then so are  $A/\pi_1$  and  $A/\pi_2$ . Assume that  $A/\pi_1$  and  $A/\pi_2$  are both defined. We show by induction on the structure of  $A$  that  $(A/\pi_1)/\pi_2$  and  $(A/\pi_2)/\pi_1$  are both defined and equal.

The weak-head normal cases follow readily by the induction hypotheses.

Assume next that  $A = \text{CASE } c \{ \text{close} \Rightarrow B \}$ . We proceed by case analysis on  $\pi_1$ . Assume first that  $\pi_1 = \text{close}$  on  $c$ . In this case,  $\text{cc}(\pi_2) \neq c$ , so  $A/\pi_2 = \text{CASE } c \{ \text{close} \Rightarrow B/\pi_2 \}$  is defined, so  $B/\pi_2$  is defined. Then  $(A/\pi_1)/\pi_2 = B/\pi_2 = (A/\pi_2)/\pi_1$  are both defined and equal. Otherwise, if neither  $\pi_1$  or  $\pi_2$  is close on  $c$ , then  $\text{cc}(\pi_1) \neq c$  and  $\text{cc}(\pi_2) \neq c$ . It follows that  $B/\pi_i$  is defined for  $i = 1, 2$ . We then deduce by the induction hypothesis:

$$(A/\pi_1)/\pi_2 = \text{CASE } c \{ \text{close} \Rightarrow (B/\pi_1)/\pi_2 \} = \text{CASE } c \{ \text{close} \Rightarrow (B/\pi_2)/\pi_1 \} = (A/\pi_2)/\pi_1.$$

Assume next that  $A = \text{CASE } c \{ l \Rightarrow A_l \}_{l \in L}$ . We proceed by case analysis on  $\pi_1$ . Assume first that  $\pi_1 = \text{label } l$  on  $c$ , where  $l \in L$ . In this case,  $(A/\pi_1)/\pi_2 = A_l/\pi_2 = (A/\pi_2)/\pi_1$  are defined and equal. Otherwise,  $\text{cc}(\pi_1) \neq c$  and  $\text{cc}(\pi_2) \neq c$ . In this case,  $A_l/\pi_i$  is defined for  $l \in L$  and  $i = 1, 2$ . We deduce by the induction hypothesis that the following are defined and equal:

$$(A/\pi_1)/\pi_2 = \text{CASE } c \{ l \Rightarrow (A_l/\pi_1)/\pi_2 \}_{l \in L} = \text{CASE } c \{ l \Rightarrow (A_l/\pi_2)/\pi_1 \}_{l \in L} = (A/\pi_2)/\pi_1.$$

The case  $A = \text{CASE } c \{ \langle b \rangle \Rightarrow B \}$  is analogous.  $\square$

The following lemma is used to relate two different reductions in the proof of the diamond property (proposition C.3):

LEMMA C.2. *If  $\Pi/\pi_1$  is defined,  $\Pi \xRightarrow{\pi_2} \Gamma$ , and  $\text{fc}(\pi_1) \cap \text{fc}(\pi_2) = \emptyset$ , then  $\Pi/\pi_1 \xRightarrow{\pi_2} \Gamma/\pi_1$ .*

PROOF OF LEMMA C.2. By case analysis on the transition  $\Pi \xRightarrow{\pi_2} \Gamma$ .

CASE (29). Assume the transition is given by  $\Pi = \Delta, a : \mathbf{1} \xRightarrow{\pi_2} \Delta/\pi_2 = \Gamma$  where  $\pi_2 = \text{close}$  on  $a$ . Then  $\Delta/\pi_1$  and  $\Delta/\pi_2$  are both defined, so  $(\Delta/\pi_1)/\pi_2 = (\Delta/\pi_2)/\pi_1$  are both defined by lemma C.1. It follows from (29) that:

$$\Pi/\pi_1 = \Delta/\pi_1, a : \mathbf{1} \xRightarrow{\pi_2} (\Delta/\pi_1)/\pi_2 = (\Delta/\pi_2)/\pi_1 = \Gamma/\pi_1.$$

CASE (30). Assume the transition is given by  $\Pi = \Delta, a : \odot_{l \in L} A_l \xRightarrow{\pi_2} \Delta/\pi_2, a : A_l = \Gamma$  with  $\pi_2 = \text{label } l$  on  $a$ , and  $\odot \in \{ \oplus, \& \}$ . Then  $\Delta/\pi_1$  and  $\Delta/\pi_2$  are both defined, so  $(\Delta/\pi_1)/\pi_2 = (\Delta/\pi_2)/\pi_1$  are both defined by lemma C.1. It follows from (30) that:

$$\Pi/\pi_1 = \Delta/\pi_1, a : \odot_{l \in L} A_l/\pi_1 \xRightarrow{\pi_2} (\Delta/\pi_1)/\pi_2, a : A_l/\pi_1 = (\Delta/\pi_2, a : A_l)/\pi_1 = \Gamma/\pi_1.$$

CASE (31). Assume the transition is given by  $\Pi = \Delta, a : (b : B) \odot (a : A) \xRightarrow{\pi_2} \Delta/\pi_2, b : B, a : A = \Gamma$  with  $\pi_2 = \text{chan } b$  on  $a$ , and  $\odot \in \{ \otimes, \multimap \}$ . Then  $\Delta/\pi_1$  and  $\Delta/\pi_2$  are both defined, so  $(\Delta/\pi_1)/\pi_2 = (\Delta/\pi_2)/\pi_1$  are both defined by lemma C.1. It follows from (31) that:

$$\begin{aligned} \Pi/\pi_1 &= \Delta/\pi_1, a : (b : B/\pi_1) \odot (a : A/\pi_1) \\ &\xRightarrow{\pi_2} (\Delta/\pi_1)/\pi_2, b : B/\pi_1, a : A/\pi_1 = (\Delta/\pi_2, b : B, a : A)/\pi_1 = \Gamma/\pi_1. \quad \square \end{aligned}$$

PROPOSITION C.3 (DIAMOND PROPERTY). *If  $\Pi \xRightarrow{\pi_1} \Pi_1$  and  $\Pi \xRightarrow{\pi_2} \Pi_2$  where  $\text{fc}(\pi_1) \cap \text{fc}(\pi_2) = \emptyset$ , then there exists a  $\Pi_3$  such that  $\Pi_1 \xRightarrow{\pi_2} \Pi_3$  and  $\Pi_2 \xRightarrow{\pi_1} \Pi_3$ .*

PROOF OF PROPOSITION C.3. If  $\Pi \xRightarrow{\pi_1} \Pi_1$  and  $\Pi \xRightarrow{\pi_2} \Pi_2$  where  $\text{fc}(\pi_1) \cap \text{fc}(\pi_2) = \emptyset$ , then  $\Pi = \Delta, a_1 : A_1, a_2 : A_2$  for some  $\Delta$  and  $a_i : A_i$  where  $a_i : A_i \xRightarrow{\pi_i} \Delta_i$  for  $i = 1, 2$ . Then

$$\begin{aligned}\Pi_1 &= (\Delta, a_2 : A_2) / \pi_1, \Delta_1, \\ \Pi_2 &= (\Delta, a_1 : A_1) / \pi_2, \Delta_2.\end{aligned}$$

By lemma C.2,

$$\begin{aligned}(\Delta, a_2 : A_2) / \pi_1 &\xRightarrow{\pi_2} (\Delta / \pi_1) / \pi_2, \Delta_2 / \pi_1 \\ (\Delta, a_1 : A_1) / \pi_2 &\xRightarrow{\pi_1} (\Delta / \pi_2) / \pi_1, \Delta_1 / \pi_2.\end{aligned}$$

In particular,  $\Delta_1 / \pi_2$  and  $\Delta_2 / \pi_1$  are defined. This implies that the following transitions are defined:

$$\begin{aligned}\Pi_1 &\xRightarrow{\pi_2} (\Delta / \pi_1) / \pi_2, \Delta_2 / \pi_1, \Delta_1 / \pi_2 \\ \Pi_2 &\xRightarrow{\pi_1} (\Delta / \pi_2) / \pi_1, \Delta_2 / \pi_1, \Delta_1 / \pi_2.\end{aligned}$$

The two right sides are equal by lemma C.1, and we take them to be  $\Pi_3$ . □

The following proposition relates trace reductions and type reductions:

PROPOSITION C.4. *If  $t \in \llbracket \Pi \# \mathcal{G} \rrbracket$ ,  $\Pi \# \mathcal{G} \xRightarrow{(\iota, \pi)} \Pi' \# \mathcal{G} / \pi$ , and  $t / \pi$  is defined, then  $t / \pi \in \llbracket \Pi' \# \mathcal{G} / \pi \rrbracket$ .*

PROOF OF PROPOSITION C.4. By induction on the derivation  $t \in \llbracket \Pi \# \mathcal{G} \rrbracket$ .

The base case occurs when  $t = \varepsilon$ , in which case the result is immediate.

The inductive steps occur when  $t = (s; m) :: \vec{x}. t'$  for some  $s, m, \vec{x}$  and  $t'$ . Each inductive case corresponds to a different possible choice of  $(s, m)$ . We give a few illustrative cases.

Assume first that  $s = \iota$  and  $\pi = (m \propto \vec{x})$ .<sup>4</sup> By inversion,  $t \in \llbracket \Pi \# \mathcal{G} \rrbracket$  because  $t' \in \llbracket \Pi' \# \mathcal{G} / \pi \rrbracket$ . Observe that  $t / \pi = t'$ . This gives the result.

In all other cases, the fact that  $t / \pi$  is defined implies that  $\text{cc}(\pi) \neq \text{cc}(m)$ . This in turn implies that  $t' / \pi$  is defined. Moreover, inversion on  $t \in \llbracket \Pi \# \mathcal{G} \rrbracket$  implies that  $t' \in \llbracket \Pi_0 \# \mathcal{G}_0 \rrbracket$  for some  $\Pi_0$  and  $\mathcal{G}_0$  such that  $\Pi, \overline{\mathcal{G}} \xRightarrow{(m \propto \vec{x})} \Pi_0, \overline{\mathcal{G}_0}$ , where we write  $\overline{\mathcal{G}}$  for the union of the contexts appearing in  $\mathcal{G}$ . By the diamond property (proposition C.3), it follows that there exists a  $\Pi'_0$  such that  $\Pi_0 \# \mathcal{G}_0 \xRightarrow{(\iota, \pi)} \Pi'_0 \# \mathcal{G}_0 / \pi$ , and such that the following two reductions are defined:

$$\begin{aligned}\Pi, \overline{\mathcal{G}} &\xRightarrow{(m \propto \vec{x})} \Pi_0, \overline{\mathcal{G}_0} \xRightarrow{\pi} \Pi'_0, \overline{\mathcal{G}_0} / \pi \\ \Pi, \overline{\mathcal{G}} &\xRightarrow{\pi} \Pi', \overline{\mathcal{G}} / \pi \xRightarrow{(m \propto \vec{x})} \Pi'_0, \overline{\mathcal{G}_0} / \pi.\end{aligned}$$

Observe that  $\Pi_0, \overline{\mathcal{G}_0} \xRightarrow{\pi} \Pi'_0, \overline{\mathcal{G}_0} / \pi$  induces a reduction  $\Pi_0 \# \mathcal{G}_0 \xRightarrow{(\iota, \pi)} \Pi'_0 \# \mathcal{G}_0 / \pi$ . By the induction hypothesis on  $t' \in \llbracket \Pi_0 \# \mathcal{G}_0 \rrbracket$  and this reduction, we deduce  $t' / \pi \in \llbracket \Pi'_0 \# \mathcal{G}_0 / \pi \rrbracket$ . Case analysis on  $s$  and  $m$  with the reduction  $\Pi', \overline{\mathcal{G}} / \pi \xRightarrow{(m \propto \vec{x})} \Pi'_0, \overline{\mathcal{G}_0} / \pi$  implies  $((s; m) :: \vec{x}. (t' / \pi)) \in \llbracket \Pi' \# \mathcal{G} / \pi \rrbracket$ . But  $t / \pi = ((s; p) :: \vec{x}. (\tau / \pi))$ , so this gives the result. □

<sup>4</sup>Recall that  $(m \propto \vec{x})$  is defined on page 17.

Recall from page 18 that a trace is safely constrained if any carrier channel associated with a constraint sign is not also associated with input, output, or synchronization.

LEMMA C.5. *If  $(o; m) :: \vec{x}. t_1$  and  $(\zeta; m) :: \vec{x}. t_2$  are safely constrained, then*

$$(o; m) :: \vec{x}. ((t_1/\zeta) \parallel (t_2/\zeta)) \subseteq (((o; m) :: \vec{x}. t_1) / \zeta) \parallel (((\zeta; m) :: \vec{x}. t_2) / \zeta).$$

PROOF OF LEMMA C.5. We remark that the hypothesis that both traces are safely constrained is essential. Indeed, the following traces are not safely constrained (they send/receive on  $a$  while also imposing a constraint on  $a$ )

$$\begin{aligned} (!; \text{label } l \text{ on } a) :: (\zeta; \text{close on } a) :: \varepsilon \\ (\zeta; \text{label } l \text{ on } a) :: (?; \text{close on } a) :: \varepsilon, \end{aligned}$$

and they do not satisfy the desired identity:

$$\begin{aligned} (!; \text{label } l \text{ on } a) :: (((\zeta; \text{close on } a) :: \varepsilon) / \zeta) \parallel (((?; \text{close on } a) :: \varepsilon) / \zeta) \\ = (!; \text{label } l \text{ on } a) :: (\varepsilon \parallel ((?; \text{close on } a) :: \varepsilon)) \\ = \{ (!; \text{label } l \text{ on } a) :: (?; \text{close on } a) :: \varepsilon \} \\ \not\subseteq \emptyset \\ = ((!; \text{label } l \text{ on } a) :: \varepsilon) \parallel ((?; \text{close on } a) :: \varepsilon) \\ = (((!; \text{label } l \text{ on } a) :: (\zeta; \text{close on } a) :: \varepsilon) / \zeta) \parallel (((\zeta; \text{label } l \text{ on } a) :: (?; \text{close on } a) :: \varepsilon) / \zeta). \end{aligned}$$

Assume that both traces are safely constrained. We examine the traces on the right side of the inclusion and observe:

$$\begin{aligned} ((o; m) :: \vec{x}. t_1) / \zeta &= (o; m) :: \vec{x}. (t_1/\zeta) \\ ((\zeta; m) :: \vec{x}. t_2) / \zeta &= t_2/\zeta. \end{aligned}$$

The hypothesis that the traces are safely constrained implies that  $\text{cc}(m)$  does not appear free in  $(\zeta; m) :: \vec{x}. t_2$ . It follows that the interleaving on the right side of the inclusion, which by the last two equalities is  $((o; m) :: \vec{x}. (t_1/\zeta)) \parallel (t_2/\zeta)$ , is either undefined or given by either a base case (when  $t_2/\zeta = \varepsilon$ ) or a commuting case.

If it is given by a base case, then the result follows easily:

$$\begin{aligned} (o; m) :: \vec{x}. ((t_1/\zeta) \parallel (t_2/\zeta)) \\ = (o; m) :: \vec{x}. ((t_1/\zeta) \parallel \varepsilon) \\ = \{ (o; m) :: \vec{x}. (t_1/\zeta) \} \\ = (o; m) :: \vec{x}. (t_1/\zeta) \parallel \varepsilon \\ = (((o; m) :: \vec{x}. t_1) / \zeta) \parallel (((\zeta; m) :: \vec{x}. t_2) / \zeta). \end{aligned}$$

If it is given by a commuting case, then observe that  $(t_2/\zeta)/(m \propto \vec{x}) = t_2/\zeta$  because  $\text{cc}(m)$  does not appear free in  $t_2$ . Using the definition of  $\parallel$  in the commuting case, we then compute:

$$\begin{aligned} (o; m) :: \vec{x}. ((t_1/\zeta) \parallel (t_2/\zeta)) \\ = \{ (o; m) :: \vec{x}. t \mid t \in ((t_1/\zeta) \parallel (t_2/\zeta)) \} \\ = \{ (o; m) :: \vec{x}. t \mid (t_2/\zeta)/(m \propto \vec{x}) \simeq t'_2 \wedge t \in t_1/\zeta \parallel t'_2 \} \\ \subseteq ((o; m) :: \vec{x}. (t_1/\zeta)) \parallel t_2/\zeta \\ = (((o; m) :: \vec{x}. t_1) / \zeta) \parallel (((\zeta; m) :: \vec{x}. t_2) / \zeta). \end{aligned}$$

This case analysis is exhaustive, so we conclude the result.  $\square$

LEMMA C.6. *If  $t$  is safely constrained, then  $(t/(m \ltimes \vec{x}))/\dot{\iota}$ ,  $(t/\dot{\iota})/(m \ltimes \vec{x})$ , and  $t/\dot{\iota}$  are all defined and equal whenever  $t/(m \ltimes \vec{x})$  is defined.*

PROOF OF LEMMA C.6. The hypothesis that  $t$  is safely constrained is essential. Indeed, consider the unsafely constrained trace  $t = (\dot{\iota}; \text{close on } a) :: (?; \text{close on } a) :: \varepsilon$ . Then  $t/\text{close on } a$  is defined but  $(t/\dot{\iota})/\text{close on } a = ((?; \text{close on } a) :: \varepsilon)/\text{close on } a$  is undefined.

Assume that  $t$  is safely constrained and that  $t/(m \ltimes \vec{x})$  is defined. It is immediate that traces  $(t/(m \ltimes \vec{x}))/\dot{\iota}$  and  $t/\dot{\iota}$  are defined. We proceed by induction on the length of  $t$  to show that  $(t/(m \ltimes \vec{x}))/\dot{\iota} = (t/\dot{\iota})/(m \ltimes \vec{x}) = t/\dot{\iota}$ . The base case occurs when  $t = \varepsilon$ , in which case the result is immediate. Assume next that  $t = (s'; m') :: \vec{y}. t'$ . We consider two subcases, based on whether or not  $\text{cc}(m) = \text{cc}(m')$ .

Assume first that  $\text{cc}(m) = \text{cc}(m')$ . In this case, the fact that  $t/(m \ltimes \vec{x})$  is defined implies  $s' = \dot{\iota}$ . Then, where the middle equality is given by the induction hypothesis:

$$(t/(m \ltimes \vec{x}))/\dot{\iota} = t'/\dot{\iota} = (t'/\dot{\iota})/(m \ltimes \vec{x}) = (t/\dot{\iota})/(m \ltimes \vec{x}),$$

and  $(t/(m \ltimes \vec{x}))/\dot{\iota} = t'/\dot{\iota} = t/\dot{\iota}$ . These transitively give the equalities we wished to show.

Assume otherwise that  $\text{cc}(m) \neq \text{cc}(m')$ . We consider two subcases: if  $s' = \dot{\iota}$  or  $s' \neq \dot{\iota}$ . If  $s' = \dot{\iota}$ , then:

$$(t/(m \ltimes \vec{x}))/\dot{\iota}$$

by definition:

$$\begin{aligned} &= ((s'; m') :: \vec{y}. (t'/(m \ltimes \vec{x}))) / \dot{\iota} \\ &= (t'/(m \ltimes \vec{x}))/\dot{\iota} \end{aligned}$$

by the induction hypothesis:

$$\begin{aligned} &= (t'/\dot{\iota})/(m \ltimes \vec{x}) \\ &= (t/\dot{\iota})/(m \ltimes \vec{x}), \end{aligned}$$

which establishes the first equality. The second equality is similarly established by appealing to the induction hypothesis:

$$(t/(m \ltimes \vec{x}))/\dot{\iota} = (t'/(m \ltimes \vec{x}))/\dot{\iota} = t'/\dot{\iota} = t/\dot{\iota}.$$

If  $s' \neq \dot{\iota}$ , then the result follows by a similarly straightforward analysis.  $\square$

PROPOSITION 5.5. *Given safely constrained traces  $t_1$  and  $t_2$ ,  $(t_1 \parallel t_2)/\dot{\iota} \subseteq (t_1/\dot{\iota}) \parallel (t_2/\dot{\iota})$ .*

PROOF OF PROPOSITION 5.5. By lexicographic induction on the lengths of traces  $t_1$  and  $t_2$ . The base cases are clear. If  $t_1 = \varepsilon$ , then

$$(\varepsilon \parallel t_2)/\dot{\iota} = \{ t_2 \}/\dot{\iota} = \{ t_2/\dot{\iota} \} = \varepsilon \parallel (t_2/\dot{\iota}) = (\varepsilon/\dot{\iota}) \parallel (t_2/\dot{\iota}).$$

The analysis for  $t_2 = \varepsilon$  is symmetric.

Assume next that both traces are non-empty. We proceed by case analysis on which clause in the definition determines  $t_1 \parallel t_2$ .

The first clause is the synchronizing case in which the two traces send and receive the same message:

$$t_1 \parallel t_2 = (!; m) :: \vec{x}. t'_1 \parallel ((?; m) :: \vec{x}. t'_2) = (\sigma; m) :: \vec{x}. (t'_1 \parallel t'_2)$$

By the induction hypothesis,  $(t'_1 \parallel t'_2) / \dot{\iota} \subseteq (t'_1 / \dot{\iota}) \parallel (t'_2 / \dot{\iota})$ . By definition of  $\cdot / \dot{\iota}$  and monotonicity:

$$\begin{aligned}
 & (t_1 \parallel t_2) / \dot{\iota} \\
 &= ((\sigma; m) :: \vec{x} . (t'_1 \parallel t'_2)) / \dot{\iota} \\
 &= (\sigma; m) :: \vec{x} . ((t'_1 \parallel t'_2) / \dot{\iota}) \\
 &\subseteq (\sigma; m) :: \vec{x} . ((t'_1 / \dot{\iota}) \parallel (t'_2 / \dot{\iota})) \\
 &= (((!; m) :: \vec{x} . t'_1) / \dot{\iota}) \parallel (((?; m) :: \vec{x} . t'_2) / \dot{\iota}) \\
 &= (t_1 / \dot{\iota}) \parallel (t_2 / \dot{\iota}).
 \end{aligned}$$

This is what we wanted to show.

The second clause is the principal case in which, without loss of generality, the head of  $t_1$  is  $(s, m_1)$  and the head of  $t_2$  is  $(\dot{\iota}, m_2)$ , where both  $m_1$  and  $m_2$  have the same carrier channel. It has two subcases. In the first subcase,  $m_1 \neq m_2$  or the two traces bind different numbers of variables, and  $t_1 \parallel t_2$  is empty. It follows that  $(t_1 \parallel t_2) / \dot{\iota}$  is empty, so the result holds trivially. The second subcase is when the interleaving is defined:

$$((s; m) :: \vec{x} . t'_1) \parallel ((\dot{\iota}; m) :: \vec{x} . t'_2) = (s; m) :: \vec{x} . (t'_1 \parallel t'_2).$$

Assume first that  $s = \dot{\iota}$ , then:

$$\begin{aligned}
 & (((s; m) :: \vec{x} . t'_1) \parallel ((\dot{\iota}; m) :: \vec{x} . t'_2)) / \dot{\iota} \\
 &= ((s; m) :: \vec{x} . (t'_1 \parallel t'_2)) / \dot{\iota} \\
 &= (t'_1 \parallel t'_2) / \dot{\iota}
 \end{aligned}$$

which by the induction hypothesis,

$$\begin{aligned}
 &\subseteq t'_1 / \dot{\iota} \parallel t'_2 / \dot{\iota} \\
 &= (t_1 / \dot{\iota}) \parallel (t_2 / \dot{\iota}).
 \end{aligned}$$

Assume otherwise that  $s \neq \dot{\iota}$ : We then compute, using the induction hypothesis, monotonicity, and the definition of  $\parallel$ :

$$\begin{aligned}
 & (((s; m) :: \vec{x} . t'_1) \parallel ((\dot{\iota}; m) :: \vec{x} . t'_2)) / \dot{\iota} \\
 &= ((s; m) :: \vec{x} . (t'_1 \parallel t'_2)) / \dot{\iota} \\
 &= (s; m) :: \vec{x} . ((t'_1 \parallel t'_2) / \dot{\iota}) \\
 &\subseteq (s; m) :: \vec{x} . ((t'_1 / \dot{\iota}) \parallel (t'_2 / \dot{\iota}))
 \end{aligned}$$

which by lemma C.5:

$$\subseteq (t_1 / \dot{\iota}) \parallel (t_2 / \dot{\iota}).$$

This completes the analysis of the synchronizing cases.

Next, we show the result for the commuting cases, where the heads of each trace perform actions on different channels. In this case,  $t_i = (s_i; m_i) :: \vec{x}_i . t'_i$  for  $1 \leq i \leq 2$  with  $\text{cc}(m_1) \neq \text{cc}(m_2)$ . We compute:

$$\begin{aligned}
 & (t_1 \parallel t_2) / \dot{\iota} \\
 &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid t_j / (m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in t'_i \parallel T'_j \} / \dot{\iota}
 \end{aligned}$$

using the fact the operation  $\cdot/\iota$  is idempotent:

$$\begin{aligned} &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . (t/\iota) \mid t_j/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in t'_i \parallel T'_j \} / \iota \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid t_j/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i \parallel T'_j) / \iota \} / \iota \end{aligned}$$

by the induction hypothesis:

$$\begin{aligned} &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid t_j/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i / \iota) \parallel (T'_j / \iota) \} / \iota \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid (t_j/(m_i \propto \vec{x}_i)) / \iota \simeq T'_j \wedge t \in (t'_i / \iota) \parallel T'_j \} / \iota \end{aligned}$$

Let  $X$  be the last set in this sequence. We proceed by case analysis on  $s_1$  and  $s_2$ .

If  $s_1 = s_2 = \iota$ , then by lemma C.6,  $X$  is:

$$\begin{aligned} X &\subseteq \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid (t_j/\iota)/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} / \iota \\ &\subseteq \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid t_j/\iota \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} / \iota \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ t \mid t_j/\iota \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ t \mid t \in (t'_i/\iota) \parallel (t'_j/\iota) \} \\ &= (t'_1/\iota) \parallel (t'_2/\iota) \\ &= (t_1/\iota) \parallel (t_2/\iota), \end{aligned}$$

which is what we wanted to show.

If neither of  $s_1$  or  $s_2$  is  $\iota$ , then by lemma C.6,  $X$  is:

$$\begin{aligned} X &\subseteq \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid (t_j/\iota)/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} / \iota \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid (t_j/\iota)/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} \\ &= \bigcup_{1 \leq i \neq j \leq 2} \{ (s_i; m_i) :: \vec{x}_i . t \mid ((s_j; m_j) :: \vec{x}_j . (t'_j/\iota))/(m_i \propto \vec{x}_i) \simeq T'_j \wedge t \in (t'_i/\iota) \parallel T'_j \} \\ &= ((s_1; m_1) :: \vec{x}_1 . (t'_1/\iota)) \parallel ((s_2; m_2) :: \vec{x}_2 . (t'_2/\iota)) \\ &= (t_1/\iota) \parallel (t_2/\iota). \end{aligned}$$

If, without loss of generality,  $s_1 = \iota$  and  $s_2 \neq \iota$ , then  $X$  is:

$$\begin{aligned} X &= \{ (\iota; m_1) :: \vec{x}_1 . t \mid (t_2/(m_1 \propto \vec{x}_1))/\iota \simeq T'_2 \wedge t \in (t'_1/\iota) \parallel T'_2 \} / \iota \cup \\ &\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\iota \simeq T'_1 \wedge t \in (t'_2/\iota) \parallel T'_1 \} / \iota, \end{aligned}$$

which by lemma C.6:

$$\begin{aligned}
&\subseteq \{ (\zeta; m_1) :: \vec{x}_1 . t \mid t_2/\zeta \simeq T'_2 \wedge t \in (t'_1/\zeta) \parallel T'_2 \} / \zeta \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\zeta \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} / \zeta \\
&= \{ t \mid t_2/\zeta \simeq T'_2 \wedge t \in (t'_1/\zeta) \parallel T'_2 \} \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\zeta \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} / \zeta \\
&= \{ t \mid t \in (t'_1/\zeta) \parallel (t_2/\zeta) \} \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\zeta \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} / \zeta \\
&= (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\zeta \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} / \zeta \\
&= (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/(m_2 \propto \vec{x}_2))/\zeta \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \},
\end{aligned}$$

which by lemma C.6:

$$\begin{aligned}
&= (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t_1/\zeta)/(m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} \\
&= (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t'_1/\zeta)/(m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \}
\end{aligned}$$

At this point, observe that if  $T'_1$  is defined, then the carrier of the head of  $t'_1/\zeta$ , if it exists, cannot be  $\text{cc}(m_2)$ . Indeed, the head of  $t'_1/\zeta$  has sign  $?$ ,  $!$  or  $\sigma$ , and if its carrier were  $\text{cc}(m_2)$ , then the reduction by  $(m_2 \propto \vec{x}_2)$  would be undefined. By lemma C.6, this last union is:

$$\begin{aligned}
&= (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad \{ (s_2; m_2) :: \vec{x}_2 . t \mid (t'_1/\zeta)/(m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in (t'_2/\zeta) \parallel T'_1 \} / \zeta
\end{aligned}$$

By the just-above observation, the following interleaving must be given by a commuting case:

$$\begin{aligned}
&\subseteq (t'_1/\zeta) \parallel (t_2/\zeta) \cup \\
&\quad t'_1/\zeta \parallel ((s_2; m_2) :: \vec{x}_2 . (t'_2/\zeta)) \\
&= (t'_1/\zeta) \parallel (t_2/\zeta) \\
&= (t_1/\zeta) \parallel (t_2/\zeta).
\end{aligned}$$

This case analysis is exhaustive and completes the proof.  $\square$

**PROPOSITION 5.6.** *If  $t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, \Gamma \vdash a : A) \rrbracket$  and  $\Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : A)$  is well-formed, then  $(t \setminus \text{dom}(\Gamma)) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : A) \rrbracket$ .*

**PROOF OF PROPOSITION 5.6.** Recall that  $t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, \Gamma \vdash a : A) \rrbracket$  is inductively defined in section 4.1 using a subset relationship. We proceed by induction on this derivation.

The base case, induced by  $\llbracket \Pi \# \cdot \rrbracket \supseteq \{ \varepsilon \}$  holds vacuously.

For the inductive steps, we show several representative cases.

Assume that  $t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, \Gamma \vdash a : \oplus_{l \in L} A_l) \rrbracket$  because  $t = (!; \text{label } k \text{ on } a) :: t'$  with  $t' \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid (I, \Gamma)/\pi \vdash a : A_k) \rrbracket$ , where  $\pi = \text{label } k \text{ on } a$ . By the induction hypothesis,  $(t' \setminus \text{dom}(\Gamma)) \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash a : A_k) \rrbracket$ . Then by the same clause,  $(t \setminus \text{dom}(\Gamma)) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash a : \oplus_{l \in L} A_l) \rrbracket$ .

Assume that  $t \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I, \Gamma, a : \oplus_{l \in L} A_l \vdash c : C) \rrbracket$  because  $t = (\sigma; \text{label } k \text{ on } a) :: t'$  with  $t' \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid (I, \Gamma)/\pi, a : A_k \vdash c : C/\pi) \rrbracket$ , where  $\pi = \text{label } k \text{ on } a$ . This case involves

two subcases: the first where  $a$  is deleted with  $\Gamma$ , and where  $a$  is not deleted. The second subcase is analogous to the previous case, so assume that  $a$  is deleted. We must show that

$$t \setminus \text{dom}(\Gamma, a : \oplus_{l \in L} A_l) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash c : C) \rrbracket.$$

Observe that the specifications

$$\Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid (I, \Gamma)/\pi, a : A_k \vdash c : C/\pi) \quad \text{and} \quad \Pi \# \mathcal{G} \mid (\Delta \mid I, \Gamma, a : A_k \vdash c : C)$$

are syntactically equal by the well-formedness assumption (reduction by an observed communication behaves as the identity on types that do not mention the communication's carrier channel). By the induction hypothesis,

$$t' \setminus \text{dom}(\Gamma, a : A_k) \in \llbracket \Pi/\pi \# \mathcal{G}/\pi \mid (\Delta/\pi \mid I/\pi \vdash c : C/\pi) \rrbracket$$

By the above syntactic observation,

$$t' \setminus \text{dom}(\Gamma, a : A_k) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash c : C) \rrbracket.$$

By definition of the deletion operation,

$$t \setminus \text{dom}(\Gamma, a : \oplus_{l \in L} A_l) \in \llbracket \Pi \# \mathcal{G} \mid (\Delta \mid I \vdash c : C) \rrbracket.$$

The remaining cases are analogous.  $\square$

**PROPOSITION 5.7.** *If  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$  and  $t_2 \in \llbracket \Pi, \overline{\mathcal{G}_1} \# \mathcal{G}_2 \rrbracket$ , then  $t_1 \parallel t_2 \subseteq \llbracket \Pi \# \mathcal{G}_1 \mid \mathcal{G}_2 \rrbracket$ .*

**PROOF OF PROPOSITION 5.7.** By lexicographic induction on the lengths of  $t_1$  and  $t_2$ . The base cases where either trace is  $\varepsilon$  are obvious. Assume next that  $t_i = (s_i; m_i) :: \vec{x}_i . \tau_i$  for  $i = 1, 2$ , and set  $\pi_i = (m_i \propto \vec{x}_i)$ . We proceed by case analysis on which clause defines  $t_1 \parallel t_2$ .

The first synchronizing case, where  $m_1 = m_2$  and  $\vec{x}_1 = \vec{x}_2$  and, without loss of generality,  $s_1 = !$  and  $s_2 = ?$ , is impossible. Indeed, it would require  $\mathcal{G}_1$  and  $\mathcal{G}_2$  to type the same channel, but this would render the specification  $\Pi \# \mathcal{G}_1 \mid \mathcal{G}_2$  ill-formed.

The second synchronizing case, where  $m_1 = m_2$  and  $\vec{x}_1 = \vec{x}_2$ , and without loss of generality,  $s_2 = \zeta$  is possible. We proceed by case analysis on  $s_1$  and  $m_1$ . If  $s_1 = ?$  and  $m_1 = \text{label } a \text{ on } k$ , then

- (1)  $t_1 = (!; \text{label } a \text{ on } k) :: \tau_1$
- (2)  $t_2 = (\zeta; \text{label } a \text{ on } k) :: \tau_2$

Inversion on the derivations  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$  and  $t_2 \in \llbracket \Pi, \overline{\mathcal{G}_1} \# \mathcal{G}_2 \rrbracket$  gives:

- (3)  $\mathcal{G}_1 = \mathcal{G}'_1 \mid (\Delta_1 \mid I_1 \vdash a : \oplus_{l \in L} A_l)$
- (4)  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}'_1 \mid (\Delta_1 \mid I_1 \vdash a : \oplus_{l \in L} A_l) \rrbracket$
- (5)  $t_2 \in \llbracket \Pi, \overline{\mathcal{G}'_1}, \Delta_1, I_1, a : \oplus_{l \in L} A_l \# \mathcal{G}_2 \rrbracket$
- (6)  $\tau_1 \in \llbracket (\Pi, \overline{\mathcal{G}_2})/\pi_1 \# \mathcal{G}'_1/\pi_1 \mid (\Delta_1/\pi_1 \mid I_1/\pi_1 \vdash a : A_k) \rrbracket$
- (7)  $\tau_2 \in \llbracket (\Pi, \overline{\mathcal{G}'_1}, \Delta_1, I_1)/\pi_1, a : A_k \# \mathcal{G}_2/\pi_1 \rrbracket$

By the induction hypothesis:

- (8)  $\tau_1 \parallel \tau_2 \subseteq \llbracket \Pi/\pi_1 \# \mathcal{G}'_1/\pi_1 \mid (\Delta_1/\pi_1 \mid I_1/\pi_1 \vdash a : A_k) \mid \mathcal{G}_2/\pi_1 \rrbracket$

Then by definition of trace sets for specifications,

- (9)  $(!; \text{label } a \text{ on } k) :: (\tau_1 \parallel \tau_2) \subseteq \llbracket \Pi \# \mathcal{G}'_1 \mid (\Delta_1 \mid I_1 \vdash a : \oplus_{l \in L} A_l) \mid \mathcal{G}_2 \rrbracket$

But this is exactly the desired inclusion  $t_1 \parallel t_2 \subseteq \llbracket \Pi \# \mathcal{G}_1 \mid \mathcal{G}_2 \rrbracket$ . The analysis is analogous for the other subcases  $s_1 \in \{!, \sigma, \zeta\}$ . It is also analogous for other values of  $m_1$ .

Finally, we consider the commuting cases where  $\text{cc}(m_1) \neq \text{cc}(m_2)$ . In this case,

$$\begin{aligned} & (s_1; m_1) :: \vec{x}_1 . t_1 \parallel (s_2; m_2) :: \vec{x}_2 . t_2 \\ &= \{ (s_1; m_1) :: \vec{x}_1 . t \mid t_2 / (m_1 \propto \vec{x}_1) \simeq T'_2 \wedge t \in t_1 \parallel T'_2 \} \cup \\ & \cup \{ (s_2; m_2) :: \vec{x}_2 . t \mid t_1 / (m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in T'_1 \parallel t_2 \} \end{aligned}$$

We must show that each of these two subsets is contained in  $\llbracket \Pi \# \mathcal{G}_1 \mid \mathcal{G}_2 \rrbracket$ . We do so for the first; the second inclusion will follow by symmetry. If  $T'_2$  is undefined, then the inclusion is immediate (the set is empty). If  $T'_2$  is defined, then we consider two subcases: whether  $s_1 = \dot{\iota}$  or  $s_1 \neq \dot{\iota}$ .

If  $s_1 = \dot{\iota}$ , then by inversion on  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$ :

- (1)  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$ ,
- (2)  $t'_1 \in \llbracket \Pi', \overline{\mathcal{G}'_2} \# \mathcal{G}_1 / \pi_1 \rrbracket$  for some  $\Pi', \overline{\mathcal{G}'_2}$  such that
- (3)  $\Pi, \overline{\mathcal{G}_2} \xRightarrow{\pi_1} \Pi', \overline{\mathcal{G}'_2}$ .

That  $T'_2 = t_2 / \pi_1$  is defined implies that  $\text{cc}(\pi_1) \notin \text{dom}(\mathcal{G}_2)$ , so  $\text{cc}(\pi_1) \in \text{dom}(\Pi)$ . It follows that  $\Pi, \overline{\mathcal{G}_2} \xRightarrow{\pi_1} \Pi', \overline{\mathcal{G}'_2}$  because:

- (4)  $\Pi \xRightarrow{\pi_1} \Pi'$  and
- (5)  $\overline{\mathcal{G}'_2} = \overline{\mathcal{G}_2} / \pi_1$ .

These facts jointly imply

$$(6) \quad \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \xRightarrow{(\dot{\iota}, \pi_1)} \Pi', \overline{\mathcal{G}_2 / \pi_1} \# \mathcal{G}_1 / \pi_1.$$

By proposition C.4,  $T'_2 \in \llbracket \Pi', \overline{\mathcal{G}_2 / \pi_1} \# \mathcal{G}_1 / \pi_1 \rrbracket$ . By the induction hypothesis,

$$t'_1 \parallel T'_2 \subseteq \llbracket \Pi' \# \mathcal{G}_1 / \pi_1 \mid \mathcal{G}_2 / \pi_1 \rrbracket.$$

By the same clause giving  $t_1 \in \llbracket \Pi, \overline{\mathcal{G}_2} \# \mathcal{G}_1 \rrbracket$ , we deduce

$$(\dot{\iota}; m_1) :: \vec{x}_1 . (t'_1 \parallel T'_2) \subseteq \llbracket \Pi' \# \mathcal{G}_1 / \pi_1 \mid \mathcal{G}_2 / \pi_1 \rrbracket,$$

which is what we wanted to show.

The case where  $s_1 \neq \dot{\iota}$  is analogous. This completes the case analyses so we conclude the result.  $\square$

**PROPOSITION 5.8.** *If  $t_1 \in \llbracket \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \rrbracket$  and  $t_2 \in \llbracket \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \rrbracket$ , then  $t_1 \parallel t_2 \subseteq \llbracket \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \rrbracket$ .*

**PROOF OF PROPOSITION 5.8.** By induction on definition of  $t_1 \parallel t_2$ .

The base cases where traces  $t_1$  or  $t_2$  are  $\varepsilon$  are impossible by inversion on the derivations of  $t_1 \in \llbracket \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \rrbracket$  and  $t_2 \in \llbracket \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \rrbracket$ .

In the inductive cases,  $t_i = (s_i; m_i) :: \vec{x}_i . t'_i$ .

Assume first that we are in the principal synchronizing case:  $\text{cc}(m_1) = \text{cc}(m_2)$  and  $\vec{x}_1 = \vec{x}_2$ , and without loss of generality,  $s_1 = !$  and  $s_2 = ?$ . We proceed by case analysis on  $m_1$ . If  $m_1 = \text{label } a \text{ on } k$ , then

- (1)  $t_1 = (!; \text{label } a \text{ on } k) :: \tau_1$
- (2)  $t_2 = (?; \text{label } a \text{ on } k) :: \tau_2$
- (3)  $A = \oplus_{l \in L} A_l$  with  $k \in L$ .

Set  $\pi = (\text{label } a \text{ on } k \propto \emptyset)$ . Inversion on the derivations  $t_1 \in \llbracket \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \rrbracket$  and  $t_2 \in \llbracket \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \rrbracket$  gives:

$$(4) \quad \tau_1 \in \llbracket (\Pi, \Delta_2, I_2, c : C) / \pi \# (\Delta_1 / \pi \mid I_1 / \pi \vdash a : A_k) \rrbracket$$

$$(5) \tau_2 \in \llbracket (\Pi, \Delta_1, I_1)/\pi \# (\Delta_2/\pi, a : A_k \mid I_2/\pi \vdash c : C/\pi) \rrbracket$$

By the induction hypothesis,  $\tau_1 \parallel \tau_2 \subseteq \llbracket \Pi/\pi \# (\Delta_1, \Delta_2)/\pi \mid (I_1, I_2)/\pi, a : A_k \vdash c : C/\pi \rrbracket$ . It then follows that  $(\sigma; \text{label } a \text{ on } k) :: (\tau_1 \parallel \tau_2) \subseteq \llbracket \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \rrbracket$ . But this is exactly what we wanted to show, for  $t_1 \parallel t_2 = (\sigma; \text{label } a \text{ on } k) :: (\tau_1 \parallel \tau_2)$ . The case analyses for other choices of  $m_1$  are analogous.

Assume next that it is the synchronizing case dealing with constraints. In particular,  $m_1 = m_2$ ,  $\vec{x}_1 = \vec{x}_2$ , and assume that  $s_2 = \iota$  (this is not without loss of generality, but the case where  $s_1 = \iota$  is analogous). Set  $\pi = (m_1 \propto \vec{x}_1)$ . By inversion on the elementhood relation for  $t_2$ , we know that  $\text{cc}(m_2) \neq a$ . We proceed by case analysis on  $s_1$  and  $m_1$ . Assume first that  $s_1 = ?$  and  $m_1 = \text{label } k \text{ on } b$ . Then by inversion on elementhood for  $t_1$ , we have  $\Delta_1 = \Delta, b : \oplus_{l \in L} B_l$  with  $k \in L$ , and by inversion on elementhood for both traces:

- (1)  $t_1 \in \llbracket \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \rrbracket$
- (2)  $t'_1 \in \llbracket (\Pi, \Delta_2, I_2, c : C)/\pi \# (\Delta/\pi, b : B_k \mid I_1/\pi \vdash a : A/\pi) \rrbracket$
- (3)  $t_2 \in \llbracket \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \rrbracket$
- (4)  $t'_2 \in \llbracket (\Pi, I_1, \Delta)/\pi, b : B_k \# (\Delta_2, a : A \mid I_2 \vdash c : C)/\pi \rrbracket$

By the induction hypothesis,

$$t'_1 \parallel t'_2 \subseteq \llbracket \Pi/\pi \# ((\Delta, \Delta_2)/\pi, b : B_k \mid (I_1, I_2, a : A)/\pi \vdash c : C/\pi) \rrbracket.$$

By the same clause giving elementhood for  $t_1$ ,

$$t_1 \parallel t_2 = (s_1; m_1) :: \vec{x}_1 \cdot (t'_1 \parallel t'_2) \subseteq \llbracket \Pi \# (\Delta_1, \Delta, b : \oplus_{l \in L} B_l \mid I_1, a : A, I_2 \vdash c : C) \rrbracket.$$

This is exactly what we wanted to show. The case analyses for other choices of  $s_1$  and  $m_1$  are equally mundane.

Finally, assume that  $t_1 \parallel t_2$  is given by a commuting case, where  $\text{cc}(m_1) \neq \text{cc}(m_2)$ . In this case,

$$\begin{aligned} & (s_1; m_1) :: \vec{x}_1 \cdot t'_1 \parallel (s_2; m_2) :: \vec{x}_2 \cdot t'_2 \\ &= \{ (s_1; m_1) :: \vec{x}_1 \cdot t \mid t_2/(m_1 \propto \vec{x}_1) \simeq T'_2 \wedge t \in t'_1 \parallel T'_2 \} \cup \\ & \cup \{ (s_2; m_2) :: \vec{x}_2 \cdot t \mid t_1/(m_2 \propto \vec{x}_2) \simeq T'_1 \wedge t \in T'_1 \parallel t'_2 \} \end{aligned}$$

We must show that each of these two subsets is contained in  $\llbracket \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \rrbracket$ . We do so for the first subset; the second inclusion is analogous. If  $T'_2$  is undefined, then the inclusion is immediate (the set is empty). Assume next that  $T'_2$  is defined; this implies that  $\text{cc}(m_1) \neq a$ . Again, we proceed by case analysis on  $s_1$  and  $m_1$ , and give an illustrative case. Assume first that  $s_1 = \sigma$  and  $m_1 = \text{label } k \text{ on } b$ . By inversion on elementhood for  $t_1$ ,

- (1)  $I_1 = I'_1, b : \odot_{l \in L} B_l$  with  $k \in L$  and  $\odot \in \{ \oplus, \& \}$ ;
- (2)  $t'_1 \in \llbracket (\Pi, \Delta_2, I_2, c : C)/\pi \# (\Delta_1/\pi \mid I'_1/\pi, b : B_k \vdash a : A/\pi) \rrbracket$ .

By proposition C.4,

$$(3) T'_2 \in \llbracket (\Pi, \Delta_1, I'_1)/\pi, b : B_k \# (\Delta_2, a : A \mid I_2 \vdash c : C)/\pi \rrbracket.$$

By the induction hypothesis,

$$t'_1 \parallel T'_2 \subseteq \llbracket \Pi/\pi \# ((\Delta_1, \Delta_2)/\pi \mid (I'_1, I_2, a : A)/\pi, b : B_k \vdash c : C/\pi) \rrbracket.$$

By the same clause giving the elementhood relationship for  $t_1$ , we deduce

$$(s_1; m_1) :: \vec{x}_1 \cdot (t'_1 \parallel T'_2) \subseteq \llbracket \Pi \# (\Delta_1, \Delta_2 \mid I'_1, b : \odot_{l \in L} B_l, I_2, a : A \vdash c : C) \rrbracket,$$

which is what we wanted to show. The remaining cases are analogous. This completes the case analyses so we conclude the result.  $\square$

**THEOREM 7.1 (SOUNDNESS).** *The typechecking algorithm is sound: if any of*

- (1)  $P \Vdash \Pi \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}$ ,
- (2)  $P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : A]) \parallel \mathfrak{T}$ , or
- (3)  $P \Rightarrow \Pi \# (\Delta, [a : A] \mid I \vdash c : C) \parallel \mathfrak{T}$ ,

then  $\mathfrak{T}/\dot{\iota} = \llbracket P \rrbracket$  (the set  $\mathfrak{T}$  extends the executions of  $P$  with constraints) and  $\mathfrak{T} \subseteq \llbracket \Pi \# \Delta \mid I \vdash a : A \rrbracket$  (the constraint set  $\mathfrak{T}$  satisfies the specification  $\Pi \# \Delta \mid I \vdash a : A$ ).

PROOF OF THEOREM 7.1. By induction on the derivation.

CASE (NEW). The result is immediate by the induction hypothesis, proposition 5.6, and eq. (8).

CASE (PAR). Recall

$$\frac{P \Vdash \Pi, \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1 \quad Q \Vdash \Pi, \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2 \quad \mathfrak{T}_1 \dot{\iota} : \mathfrak{T}_2}{P \parallel_a Q \Vdash \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2} \text{(PAR)}$$

By the induction hypothesis,  $\mathfrak{T}_1/\dot{\iota} = \llbracket P \rrbracket$  and  $\mathfrak{T}_2/\dot{\iota} = \llbracket Q \rrbracket$ . By definition of  $\mathfrak{T}_1 \dot{\iota} : \mathfrak{T}_2$ ,

$$\llbracket P \parallel_a Q \rrbracket = \llbracket P \rrbracket \parallel \llbracket Q \rrbracket = (\mathfrak{T}_1/\dot{\iota}) \parallel (\mathfrak{T}_2/\dot{\iota}) = (\mathfrak{T}_1 \parallel \mathfrak{T}_2)/\dot{\iota}.$$

The inclusion  $\mathfrak{T}_1/\dot{\iota} \parallel \mathfrak{T}_2/\dot{\iota} \subseteq \llbracket \Pi \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \rrbracket$  is given by proposition 5.8.

CASE ( $\Vdash$ -R). The result is immediate by the induction hypothesis.

CASE ( $\Vdash$ -L). The result is immediate by the induction hypothesis.

CASE ( $\Rightarrow$ 1R). Recall

$$\frac{}{\text{close } a \Rightarrow \Pi \# (\cdot \mid \cdot \vdash [a : 1]) \parallel \{(!; \text{close on } a) :: \varepsilon\}} (\Rightarrow 1R)$$

The inclusion  $\mathfrak{T} = \{(!; \text{close on } a) :: \varepsilon\} \subseteq \llbracket \Pi \# \cdot \mid \cdot \vdash a : 1 \rrbracket$  is easily checked. Moreover,  $\mathfrak{T} \setminus \text{fc}(\Pi) = \mathfrak{T} = \llbracket \text{close } a \rrbracket$  by eq. (1), so we are done.

CASE ( $\Rightarrow \oplus$ R). The equality is by the induction hypothesis and eq. (3). The inclusion is by the induction hypothesis and eq. (9).

CASE ( $\Rightarrow \&$ R). The equality is by the induction hypothesis and eq. (4). The inclusion is by the induction hypothesis and eq. (22).

CASE ( $\Rightarrow \oplus$ L). Analogous to ( $\Rightarrow \&$ R).

CASE ( $\Rightarrow \&$ L). Analogous to ( $\Rightarrow \oplus$ R).

CASE ( $\Rightarrow \otimes$ R). By the induction hypothesis,  $\mathfrak{T}_1/\dot{\iota} = \llbracket P \rrbracket$  and  $\mathfrak{T}_2/\dot{\iota} = \llbracket Q \rrbracket$ . By eq. (5),

$$\begin{aligned} & \llbracket \text{send } a (b.P); Q \rrbracket \\ &= \{(!; \text{chan on } a) :: b.t \mid t_P \in \llbracket P \rrbracket, t_Q \in \llbracket Q \rrbracket, t \in t_P \parallel t_Q\} \\ &= \{(!; \text{chan on } a) :: b.t \mid t_P \in \mathfrak{T}_1/\dot{\iota}, t_Q \in \mathfrak{T}_2/\dot{\iota}, t \in t_P \parallel t_Q\} \\ &= \{(!; \text{chan on } a) :: b.t \mid t \in \mathfrak{T}_1/\dot{\iota} \parallel \mathfrak{T}_2/\dot{\iota}\} \end{aligned}$$

which by  $\mathfrak{T}_1 \dot{\iota} : \mathfrak{T}_2$ ,

$$\begin{aligned} &= \{(!; \text{chan on } a) :: b.t \mid t \in (\mathfrak{T}_1 \parallel \mathfrak{T}_2)/\dot{\iota}\} \\ &= (!; \text{chan on } a) :: b.((\mathfrak{T}_1 \parallel \mathfrak{T}_2)/\dot{\iota}) \\ &= ((!; \text{chan on } a) :: b.(\mathfrak{T}_1 \parallel \mathfrak{T}_2))/\dot{\iota} \end{aligned}$$

The desired inclusion is given by the induction hypothesis, proposition 5.7, and eq. (16).

CASE ( $\Rightarrow \neg \circ$ R). Recall

$$\frac{P \Vdash \Pi/\pi \# (\Delta/\pi, b : B \mid I/\pi \vdash a : A) \parallel \mathfrak{T} \quad \pi = \text{chan } b \text{ on } a}{b \leftarrow \text{receive } a; P \Rightarrow \Pi \# (\Delta \mid I \vdash [a : (b : B) \neg \circ (a : A)]) \parallel \{(?; \text{chan on } a) :: b.\mathfrak{T}\}} (\Rightarrow \neg \circ R)$$

The equality is by the induction hypothesis and eq. (6). The inclusion is by the induction hypothesis and eq. (26).

CASE ( $\Rightarrow \otimes$ L). Analogous to ( $\Rightarrow \neg \circ$ R).

CASE  $(\Rightarrow \neg \circ L)$ . Analogous to  $(\Rightarrow \otimes R)$ .

CASE  $(\Rightarrow 1L)$ . The desired inclusion is given by the induction hypothesis and eq. (13). The desired equality is given by the induction hypothesis and eq. (2).

CASE  $(\Rightarrow 1[R])$ ,  $(\Rightarrow 1[L])$ ,  $(\Rightarrow \oplus \& [R])$ ,  $(\Rightarrow \oplus \& [L])$ ,  $(\Rightarrow \otimes \neg \circ [R])$ , or  $(\Rightarrow \otimes \neg \circ [L])$ . The inclusion is given by the induction hypothesis and the appropriate choice of eqs. (18) to (20). The equality is immediate by the induction hypothesis and the definition of  $(-)/\dot{\iota}$ .  $\square$

LEMMA C.7. If  $t \in \llbracket \cdot \# \mathcal{G} \rrbracket$ , then  $t/\dot{\iota} = t$ .

PROOF OF LEMMA C.7. By induction on the derivation  $t \in \llbracket \cdot \# \mathcal{G} \rrbracket$ .  $\square$

COROLLARY 7.2. If  $P \Vdash \cdot \# (\Delta \mid I \vdash a : A) \parallel \mathfrak{T}$ , then  $\mathfrak{T} = \llbracket P \rrbracket$  and  $\llbracket P \rrbracket \subseteq \llbracket \cdot \# (\Delta \mid I \vdash a : A) \rrbracket$ .

PROOF OF COROLLARY 7.2. Immediate by lemma C.7 and theorem 7.1.  $\square$

PROPOSITION 7.3 (COMPOSITIONALITY). If  $P \Vdash \cdot \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1$  and  $Q \Vdash \cdot \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2$ , then  $P \parallel_a Q \Vdash \cdot \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2$ .

PROOF OF PROPOSITION 7.3. Assume

- $P \Vdash \cdot \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1$  and
- $Q \Vdash \cdot \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2$ .

By induction on the two derivations, we can weaken them to

- $P \Vdash \Delta_2, I_2, c : C \# (\Delta_1 \mid I_1 \vdash a : A) \parallel \mathfrak{T}_1$  and
- $Q \Vdash \Delta_1, I_1 \# (\Delta_2, a : A \mid I_2 \vdash c : C) \parallel \mathfrak{T}_2$ .

To show  $P \parallel_a Q \Vdash \cdot \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2$ , it is sufficient by corollary 7.2 and (PAR) to show that  $\mathfrak{T}_1 \dot{\iota} \mathfrak{T}_2$ , i.e., that

$$\forall t_1 \in \mathfrak{T}_1. \forall t_2 \in \mathfrak{T}_2. (t_1/\dot{\iota} \parallel t_2/\dot{\iota}) = (t_1 \parallel t_2)/\dot{\iota}.$$

Indeed, if  $\mathfrak{T}_1 \dot{\iota} \mathfrak{T}_2$ , then  $P \parallel_a Q \Vdash \cdot \# (\Delta_1, \Delta_2 \mid I_1, a : A, I_2 \vdash c : C) \parallel \mathfrak{T}_1 \parallel \mathfrak{T}_2$  by (PAR). We show that  $\mathfrak{T}_1 \dot{\iota} \mathfrak{T}_2$ . By corollary 7.2 and lemma C.7,  $t_i/\dot{\iota} = t_i$  for all  $t_i \in \mathfrak{T}_i$  with  $i = 1, 2$ . We deduce

$$(t_1/\dot{\iota} \parallel t_2/\dot{\iota}) = t_1 \parallel t_2 = (t_1 \parallel t_2)/\dot{\iota},$$

where the last equality stems from the fact that  $\cdot/\dot{\iota}$  is the identity on traces with no constraints, and interleaving two traces without constraints does not introduce any constraints. This is what we wanted to show.  $\square$

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