Selective-Area Epitaxy of Bulk-Insulating $(Bi_xSb_{1-x})_2Te_3$ Films and Nanowires by Molecular Beam Epitaxy

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The selective-area epitaxy (SAE) is a useful technique to grow epitaxial films with a desired shape on a pre-patterned substrate. Although SAE of patterned topological-insulator (TI) thin films has been performed in the past, there has been no report of SAE-grown TI structures that are bulkinsulating. Here we report the successful growth of Hall-bars and nanowires of bulk-insulating TIs using the SAE technique. Their transport properties show that the quality of the selectively-grown structures is comparable to that of bulk-insulating TI films grown on pristine substrates. In SAEgrown TI nanowires, we were able to observe Aharonov-Bohm-like magnetoresistance oscillations that are characteristic of the quantum-confined topological surface states. The availability of bulkinsulating TI nanostructures via the SAE technique opens the possibility to fabricate intricate topological devices in a scalable manner.

Keywords: topological insulator, nanowire, selective area epitaxy, Aharonov-Bohm oscillations, electrostatic gating

I. INTRODUCTION

Topological insulators (TIs) have a high application potential when used in devices for spintronics or topological quantum computing [1]. Such device applications often require patterned TI films. In this context, the selective area epitaxy (SAE) of TIs has been prolific in recent years, allowing for the realization of high-precision and scalable nanostructures. Examples include TI nanoribbons [2–4], nanorings [5], in-situ fabricated Josephson junctions [6, 7], and TI-based qubit circuits [8]. The SAE technique relies on a prepatterned growth template consisting of two material systems. One of these materials is single-crystalline and lattice-matched to the epilayer to be selectively grown, whereas the second material is often amorphous and does not allow the heterogeneous nucleation of the epilayer to take place.

The TI material $(Bi_xSb_{1-x})_2Te_3$ is of particular interest for applications, because it can be made bulkinsulating with a careful tuning of the growth condition and the Sb/Bi ratio [9, 10]. For the SAE growth of $(Bi_xSb_{1-x})_2Te_3$, a natural choice would be to combine Si(111) substrate (which is known to support the epitaxial growth of this material) with its native oxide SiO_2 acting as the growth mask. While SAE is indeed possible on Si(111)-SiO₂ templates [11], Jalil *et al.* showed that a better dimensional control can be achieved at the nanoscale when amorphous Si_3N_4 is used as the growth mask instead [12]. However, for the epitaxial growth on the Si(111) surface a few challenges remain: (i) X-ray diffraction has shown that the crystal quality of $(Bi_xSb_{1-x})_2Te_3$ thin films grown on Si(111) is inferior when compared to other growth substrates [13, 14]. (ii) Atom-probe tomography has revealed Sb accumulation

at the film-substrate interface, which may give rise to a parasitic two-dimensional electron gas, since Sb is a well-known n-type dopant in Si [15].

It is useful to emphasize that bulk-insulation is important for TI materials in order to access the topological phenomena, which often requires the chemical potential to be tuned close to the Dirac point of the topological surface state. For example, in the case of a TI proximitized by a superconductor to realize topological superconductivity, the Majorana zero modes appearing in such a system become more stable as the chemical potential gets closer to the Dirac point [16, 17]. While bulk-insulation can in principle be achieved in TI thin films by taking $(Bi_xSb_{1-x})_2Te_3$ as the material platform, it actually requires a fine tuning of the conditions of the molecular beam epitaxy (MBE) [9, 10, 18]. Since a successful SAE requires another fine tuning of the MBE conditions, it is very difficult to find the right condition to achieve both SAE and bulk-insulation. As a result, there has been no report of bulk-insulating TI structure grown via the SAE technique [2–8, 12, 15].

In this work, we succeeded for the first time in the SAE of bulk-insulating $(Bi_xSb_{1-x})_2Te_3$ structures (Hallbar and nanowire) on patterned $Al_2O_3(0001)$ -Si₃N₄ substrates. The advantage of the epitaxial growth on sapphire is that it does not suffer from the issues mentioned above for Si substrates. Moreover, the SAE can be achieved in a simple bilayer structure, whereas Jalil et al. had to rely on a thin SiO_2 buffer layer between the Si(111) surface and Si_3N_4 mask to mitigate the tensile strain exerted by the Si_3N_4 layer on the Si interface [12]. Strain is not an issue for sapphire substrates owing to its large Young's modulus. By optimizing the MBE growth condition, we achieved a reasonably high carrier mobility of $\sim 1000 \text{ cm}^2/\text{Vs}$ comparable to plain films grown on pristine substrates [18]. This is nontrivial, because the growth substrate for SAE is first completely covered with Si_3N_4 and then trenches are patterned in a nanofab-

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FIG. 1. Schematic of a substrate used for SAE, consisting of a sapphire (Al_2O_3) substrate and a patterned silicon nitride (Si_3N_4) mask with a Hall-bar design (front) and a multiterminal nanowire (back). Dimensions are not to scale. The MBE film (yellow) grows only on the exposed sapphire region within the Si_3N_4 mask openings. A top-gate voltage V_G is applied to gold electrodes fabricated on top of an ALDdeposited Al₂O₃ dielectric (exemplary shown here only for parts of the nanowire). For transport measurements an ac current is applied to the Hall-bar between the current electrodes. The longitudinal voltage V_x is acquired together with the Hall voltage $V_{\rm v}$ using lockin amplifiers. Upper left inset: AFM image of the MBE film selectively grown in one of the larger pad areas. Upper right inset: SEM image of a 120-nmwide nanowire. Only few isolated crystallites are found on the Si_3N_4 mask (black).

rication process. In our experiment, we found that the realization of bulk-insulation for nano-sized structures requires re-optimisation of the growth conditions that turned out to depend on the feature size. In the bulkinsulating TI nanowires obtained via SAE, our transport experiments found Aharonov-Bohm (AB)-like magnetoresistance oscillations in magnetic fields applied parallel to the nanowire, that are characteristic of the quasione-dimensional transport through size-quantized topological surface states [19–22], confirming their topological nature.

II. RESULTS AND DISCUSSION

By applying a two-step temperature profile to the prepatterned substrate during the MBE growth (details provided in Methods), the SAE growth of $(Bi_xSb_{1-x})_2Te_3$ Hall-bars and nanowires is achieved. Figure 1 shows a schematic of the Al₂O₃ substrate with Si₃N₄ mask, as well as an atomic-force-microscopy (AFM) and scanningelectron-microscopy (SEM) images of the selectively-grown $(Bi_xSb_{1-x})_2Te_3$ structures. The film in the Hallbar is continuous with triangular terraces, indicating a good epitaxy. This growth of continuous $(Bi_xSb_{1-x})_2Te_3$ is maintained even when the lateral dimensions of the exposed areas in the growth mask are constrained into nanowires as seen from the SEM image. Due to a high degree of selectivity, only a few isolated crystallites are found on the $\rm Si_3N_4$ mask.

To characterise the electronic properties of the $(Bi_xSb_{1-x})_2Te_3$ film selectively-grown in the Hall-bar structure, the longitudinal sheet resistance R_{xx} and the Hall resistance R_{yx} were measured as a function of temperature T, perpendicular magnetic field B, and top-gate voltage V_G . The $R_{xx}(T)$ behavior shown in the inset of Fig. 2(a) is typical for a bulk-insulating $(Bi_xSb_{1-x})_2Te_3$ film [18]. The $R_{xx}(V_G)$ behavior shown in the main panel of Fig. 2(a) shows a clear peak with the gate-tunabiliy of R_{xx} by a factor of 2, which further confirms the bulkinsulation and indicates that the chemical potential can be gate-tuned across the Dirac point of the topological surface state.

For each data point in the gating curve, we measured $R_{\rm xx}$ and $R_{\rm yx}$ as a function of the perpendicular magnetic field up to 9 T. By employing the two-band analysis used in Refs. [18, 23, 24], we analysed the sheet carrier density n_{2D} and the mobility μ of the carriers contributing to the transport at each $V_{\rm G}$ [see Fig. S1 in the Supporting information (SI) for the raw data] and the results are shown in Figs. 2(b) and 2(c). We identified a single *n*-type band for $V_{\rm G} > -4$ V, while both *n*- and *p*-type carriers contribute to the transport for $V_{\rm G} \leq -4$ V. The mobility of the *n*type carriers is of the order of $1000 \text{ cm}^2/\text{Vs}$ which likely comes from the surface electrons in both top and bottom surfaces [18]. At $V_{\rm G} \leq -4$ V, the top surface is tuned into the p-type regime where holes from both surface (2D) and bulk (3D) contribute to the transport (because of the close proximity of the bulk valence-band edge to the Dirac point of the surface state [9, 10], while the bottom surface is still in the *n*-type regime and keep contributing high-mobility electrons. This behavior is entirely consistent with what was reported for top-gated $(Bi_xSb_{1-x})_2Te_3$ films grown on pristine sapphire substrates [18].

While the $(Bi_xSb_{1-x})_2Te_3$ film grown in the Hall-bar structure analyzed above has shown bulk-insulation with the tunability of the chemical potential across the Dirac point, the 160-nm-wide nanowire structure grown on the very same substrate turned out to be different. Within the range of $V_{\rm G}$ that can be applied without breaking the dielectric, the Dirac point cannot be accessed and the $R_{\rm xx}(V_{\rm G})$ indicated that the nanowire was always in the *p*-type regime [Fig. 2(d)]. This suggests that the MBEgrowth condition depends on the size of the patterned features and the actual composition of $(Bi_xSb_{1-x})_2Te_3$ is different between the Hall-bar and nanowire structures, which is consistent with a past report on the SAE [12]. The lower value of the sheet resistance in the nanowire [Fig. 2(d)] compared to that in the $10-\mu$ m-wide Hall-bar device on the same substrate [Fig. 2(d)] also supports this conclusion. Since this nanowire is bulk-conducting, the AB-like magnetoresistance oscillations characteristic of bulk-insulating TI nanowires [19] are not recognizable in parallel magnetic fields [see inset of Fig. 2(d) and ad-



FIG. 2. Transport characterization of the $(Bi_xSb_{1-x})_2Te_3$ devices grown on the SAE substrate 1. (a) V_G -dependence of R_{xx} of the Hall-bar at 2 K. Inset: Temperature dependence of R_{xx} at $V_G = 0$ V, showing a typical bulk-insulating behavior. (b,c) n_{2D} and mobility obtained for the Hall-bar as a function of V_G at 2 K from the two-band analysis of the $R_{yx}(B)$ data shown in the SI. (d) V_G -dependence of R_{xx} (taken at 40 mK in the parallel magnetic field of 7 T) of a 160-nm-wide nanowire on the same substrate, showing a *p*-type behavior within the experimental gating range. Inset: magnetoresistance of the same nanowire in parallel magnetic fields up to 7 T measured at $V_G = 20$ V and T = 40 mK.

ditional data in SI].

To achieve bulk-insulation in the SAE-grown nanowires, the MBE growth conditions were reoptimised for a second wafer (substrate 2), decreasing the beam-equivalent-pressure ratio of Sb/Bi from 2.1 to 1.8 (see Methods for details). For a long $(Bi_xSb_{1-x})_2Te_3$ nanowire grown on substrate 2, three sections with different lengths were investigated. Figures 3(a-b) show the $V_{\rm G}$ -dependence of the resistance R normalized by the section length L for the three sections 1, 2 and 3. All three section can be tuned to the Dirac point with $V_{\rm G} \approx 2.4 \, {\rm V}$ and their resistances scale linearly with the wire length as expected for nanowires in the diffusive regime, demonstrating the homogeneity of the grown nanowire.

In this bulk-insulating nanowire, we observed the characteristic AB-like magnetoresistance oscillations in parallel magnetic fields stemming from the magnetic-flux-dependence of the Dirac subbands formed in TI nanowires due to the quantum size effect [19–22]. The phase shift of these oscillations upon gating is commonly observed in bulk-insulating TI nanowires [21, 22, 25], and it is also the case here: After subtracting a smooth background from the raw data to obtain ΔR (see SI for details), we find oscillations whose phase shifts with $V_{\rm G}$ [Figs. 3(d-f)]. Note that the oscillations and the

phase shift have considerable irregularity, which points to strong disorder. This is understandable, because the bulk-insulation in $(Bi_xSb_{1-x})_2Te_3$ is achieved by compensation doping, which leaves charged doners and acceptors [24, 26–31].

Interestingly, the degree to which disorder affects the regularity of flux-dependent oscillations seems to correlate with the nanowire length. While the mapping of $\Delta R(B, V_{\rm G})$ shown in Fig. 3(d) for the shortest section is significantly disordered, a more regular pattern emerges upon increasing the wire length, as one can see in Figs. 3(e-f). One can also see this qualitative change in the individual curves of $\Delta R(B)$ for fixed V_G values [Figs. 3(g-i)]. This length dependence probably arises because the random resistance fluctuations due to disorder are averaged out in longer sections. In the longest 5.7- μm section where the AB-like oscillations were most regular, we identify the dominant periodicity of ~ 1.6 T, which corresponds to a cross-sectional area of $2.6 \times 10^{-15} \,\mathrm{m}^2$. This value is only slightly smaller than the geometrical cross-section of the wire, $2.9 \times 10^{-15} \text{ m}^2$, which was determined via AFM and SEM observations of the nanowire thickness (24 nm) and width (120 nm), respectively. The flux-relevant cross-section that is reduced from the geometrical value was previously attributed to the penetration depth of the surface-state wave function and a finite



FIG. 3. Comparison of the transport properties of three different sections of the 120-nm-wide bulk-insulating $(\text{Bi}_{x}\text{Sb}_{1-x})_{2}\text{Te}_{3}$ nanowire SAE-grown on substrate 2. (a-c) V_{G} -dependence of the resistance R normalized by the section length L measured at 260 mK in 0 T for (a) $L = 1.2 \,\mu\text{m}$, (b) $L = 3.4 \,\mu\text{m}$, and (c) $L = 5.7 \,\mu\text{m}$. The schematic of the measurement set-up is shown in the top. (d-f) 2D maps of the magnetoresistance oscillations in parallel magnetic fields as functions of B and V_{G} after subtracting a smooth background (see SI for details) for the three sections. (g-i) Background-subtracted magnetoresistance curves as a function of the parallel magnetic field, measured at $V_{\text{G}} = 2.9 \,\text{V}$, 3.1 V, and 3.3 V for the three sections; the positions of these V_{G} values are marked in (d-f) with black dashed lines.

oxide shell on the surface [2, 32].

III. CONCLUSION

We have shown that bulk-insulation can be achieved in TI structures that are SAE-grown on a patterned sapphire substrate with a Si_3N_4 growth mask. The high carrier mobility and the tunability of the chemical potential across the Dirac point demonstrate the suitability of this technique to engineer more complex devices. We found that the optimum growth conditions to achieve bulk-insulation changes with the feature size, but it was still possible to optimize the conditions to grow a bulkinsulating $(Bi_xSb_{1-x})_2Te_3$ nanowire with the width of only 120 nm. The AB-like magnetoresistance oscillations observed in such a nanowire confirm the quasi-1D transport through the quantum-confined topological surface state. Interestingly, our data suggest that the disorder effect, which causes random fluctuations of the wire resistance, tends to be averaged out in longer wires. The present result opens the possibility to fabricate TI-based devices to exploit various topological phenomena in a scalable manner.

IV. METHODS

MBE growth & device fabrication:

The SAE growth templates were created by depositing a 50-nm Si₃N₄ layer by hot-wire chemical vapour deposition (HW-CVD) on top of $1 \times 1 \text{ cm}^2$ sapphire (0001) substrates. Subsequently, the growth masks were defined using electron-beam lithography, and transferred into the Si₃N₄ layer by reactive-ion etching with CF₄. The resist was removed in acetone and isopropanol. Next, the template was exposed to a mild Ar and O_2 plasma to remove any remaining resist leftovers.

The growth templates were then loaded into the MBE chamber (MBE Komponenten, Octoplus 400) and annealed up to 900°C in ultra-high vacuum to clean off any adsorbents. Good thermal contact between the substrate and sample holder was found to be crucial for successful SAE growth, and was achieved by spot-welding all four corners of the substrate to the Mo sample holder using Ta strips. For the MBE growth of $(Bi_xSb_{1-x})_2Te_3$, high-purity Bi, Sb, and Te source materials (99.9999%, Thermo Fisher) were co-evaporated onto the substrate. For substrate 1, the beam-equivalent-pressures (BEP) for Bi, Sb, and Te were 2.20×10^{-8} , 4.60×10^{-8} , and 1.00×10^{-6} mBar respectively, which yielded a bulkinsulating Hall-bar, but p-type metallic nanowire. Te was supplied in excess, while Bi and Sb acted as the limiting species. The BEP ratio of Te/(Bi+Sb) was ~15 and Sb/Bi was ~ 2.1 . For substrate 2, the BEP for Bi, Sb, and Te were 2.70×10^{-8} , 4.90×10^{-8} , and 1.00×10^{-6} mBar $[Te/(Bi+Sb) \approx 13 \text{ and } Sb/Bi \approx 1.8]$, where the reduced Sb/Bi BEP ratio yielded bulk-insulating nanowires with widths in the range of 100 nm. The substrate temperature was kept at 245°C for the first 3 min of the MBE deposition, during which a continuous seed layer of $(Bi_xSb_{1-x})_2Te_3$ formed on the exposed sapphire (0001) surface. Almost no nucleation of $(Bi_xSb_{1-x})_2Te_3$ crystallites occurred on the Si_3N_4 growth mask as the sticking coefficients for Bi, Sb, and Te at 245°C were too low for this surface. Next, the substrate temperature was slowly increased to 285° C in 13 min (i.e. 3° C/min), while the deposition continued. At 285°C the substrate was kept for an additional 20 min in the fluxes of Bi, Sb, and Te, after which the substrate was left to cool down naturally to room temperature in zero flux. The higher growth temperature of 285°C lead to a better morphology of the $(Bi_xSb_{1-x})_2Te_3$ film than would be obtained from growing with a constant temperature profile of 245°C. Note, however, that a lower substrate temperature of 245°C at the start was needed for the formation of a continuous seed layer of $(Bi_xSb_{1-x})_2Te_3$ which does not take place at 285° C on sapphire (0001) (and Si₃N₄) for the Bi, Sb, and Te fluxes used in our MBE system.

Within a few minutes of taking the films out of the MBE chamber, they were capped with a 40-nm thick Al_2O_3 capping layer grown by atomic layer deposition (ALD) at 80°C using Ultratec Savannah S200 to avoid

degradation in air. Next, the $(Bi_xSb_{1-x})_2Te_3$ contact arms of the Hall-bar and nanowire devices were metallized by sputter-depositing 5 nm of Pt and 45 nm of Au. For this process, optical lithography was used and the Al₂O₃ capping layer was selectively removed in the contact areas by an aluminum etchant (Transene Type-D) heated to 50°C. Lastly, a top-gate electrode was defined on top of the 40-nm thick Al₂O₃ layer by electron-beam lithography and sputter-depositing 5 nm Pt + 45 nm Au. The dimensions of the devices studied in this work are summarized in the supplement.

Measurements:

The characterization of the selectively-grown Hall-bar device (substrate 1) was carried out in a Quantum Design physical property measurement system (PPMS), which can be operated in a temperature range from 2 K to 350 K and in magnetic fields up to 9 T. The measurements of the nanowire device (substrate 1) were performed in a dry dilution refrigerator (Oxford Instruments TRITON 200, base temperature \sim 40mK) equipped with a 8-T superconducting magnet. The measurements of the magnetoresistance oscillations in the nanowire on substrate 2 were performed in a ³He cryostat (Oxford Instrument Heliox) at a base temperature of 250 mK in magnetic fields up to 10 T. The nanowire (Hall-bar) devices were measured using standard lock-in techniques in a fourterminal configuration with an ac excitation current of 10 nA (1 μ A) and a lock-in frequency of 13.37 Hz.

Data and materials availability:

Raw data used in the generation of main and supplementary figures are available in Zenodo with the identifier 10.5281/zenodo.10964435.

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Supporting Information for "Selective-Area Epiaxy of Bulk-Insulating $(Bi_xSb_{1-x})_2Te_3$ Films and Nanowires by Molecular Beam Epitaxy"

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I. SUMMARY OF DEVICES

The following SAE-grown $(Bi_xSb_{1-x})_2Te_3$ structures are reported in this paper:

Device	Substrate	Length	Width	Thickness	Expected AB-period
Hall-bar	1	$64\mu{ m m}$	$10\mu{ m m}$	$20\mathrm{nm}$	-
Nanowire	1	$8.0\mu{ m m}$	$160\mathrm{nm}$	$20\mathrm{nm}$	$1.3\mathrm{T}$
Nanowire, Section 1	2	$1.2\mu{ m m}$	$120\mathrm{nm}$	$24\mathrm{nm}$	$1.5\mathrm{T}$
Nanowire, Section 2	2	$3.4\mu{ m m}$	$120\mathrm{nm}$	$24\mathrm{nm}$	$1.5\mathrm{T}$
Nanowire, Section 3	2	$5.7\mu{ m m}$	$120\mathrm{nm}$	$24\mathrm{nm}$	$1.5\mathrm{T}$

II. TWO-BAND ANALYSIS OF THE HALL DATA

In Fig. 2 of the main text, the $V_{\rm G}$ -dependencies of the sheet carrier density $n_{\rm 2D}$ and mobility μ of the *n*- and *p*-type channels identified in the Hall-bar device are shown. To extract these values, the magnetic-field dependence of the Hall resistance $R_{\rm yx}$ shown in Figs. S1(a-b) were fitted using the two-band model [1–3]

$$R_{yx}t = \frac{R_1\rho_2^2 + R_2\rho_1^2 + R_1R_2(R_1 + R_2)B^2}{(\rho_1 + \rho_2)^2 + (R_1 + R_2)^2B^2}B,$$
(S1)

where R_i and ρ_i are the Hall coefficient and the resistivity of the *i*-th channel, respectively, and *t* is the thickness. Additionally, we used the measured sheet resistance R_{xx} at B = 0 to constrain the fitting parameters ρ_1 and ρ_2 with the relation

$$R_{xx}(B=0) = \frac{\rho_1 \rho_2}{t(\rho_1 + \rho_2)}.$$
(S2)

The values of n_{2D} and μ for each channel can be calculated from $n_{2D}^i = t/(eR_i)$ and $\mu_i = t/(en_{2D}^i\rho_i)$. Figure S1(c) shows the $R_{yx}(B)$ behavior (purple line) at 2 K for $V_G = -10$ V, where the two-band nature of the transport was most apparent. The fit (dashed line) reproduces the data reasonably well. The $R_{xx}(B = 0)$ values used for constraining the fits are shown in Fig. 2(a) of the main text.

Figure S1(d) shows the $R_{yx}(B)$ behavior at $V_G = 0$ taken for 2 – 350 K. At this V_G value, the $R_{yx}(B)$ behavior is essentially linear and single-band analysis yields the *T*-dependence of n_{2D} shown in the inset.

III. ADDITIONAL MAGNETORESISTANCE DATA FOR THE METALLIC NANOWIRE

In Fig. 2(d) of the main text, the magnetic-field dependence of the sheet resistance R_{xx} at $V_G = 20$ V is shown for the 8.0- μ m-long nanowire device on substrate 1. Even though the Dirac point was not reached, electrostatic gating experiments at higher V_G were not carried out, because the ALD-deposited Al₂O₃ layer is at risk of dielectric breakdown when exposed to electric fields higher than ~ 0.5 V/nm. Additional magnetoresistance data at lower V_G values are shown in Fig. S2. One can see some features showing up as V_G is increased (i.e. as the chemical potential is moved towards the Dirac point), but they are irregular and no periodic oscillations could be identified.

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FIG. S1. (a,b) $R_{yx}(B)$ curves for the Hall-bar device on substrate 1 measured at 2 K for V_G from -12 to -4 V (a) and from -2 to -10 V (b); the V_G value for each curve corresponds to the data points in Fig. 2 of the main text. (c) Two-band-model fit to the $V_G = -10$ V data using Eq. S1. (d) $R_{yx}(B)$ curves at $V_G = 0$ V for temperatures from 2 to 350 K; inset shows the extracted *T*-dependence of n_{2D} .



FIG. S2. Additional data for the bulk-conducting nanowire device on substrate 1. (a) $V_{\rm G}$ dependence of the resistance R normalized by the length L measured at 40 mK in the parallel magnetic field of 7 T. (b) Magnetic-field dependence of R/L measured in parallel magnetic fields at various $V_{\rm G}$ values at 40 mK. No regular oscillations can be identified.

When analyzing Aharonov-Bohm (AB)-like oscillations in TI nanowires, because the weak antilocalization (WAL) effect [2] and positive magnetoresistance are superimposed on these oscillations, it is customary to subtract a smooth background from the magnetoresistance data [4–7] to make the oscillations more apparent. In this work, the smooth background was determined by applying a LOESS (locally estimated scatterplot smoothing) filter to the R(B) data between 0 and ± 10 T with an averaging window of 1497 data points, which corresponds to a span of about 7.5 T. Figure S3 shows an example of the raw data, LOESS-generated background, and background-subtracted data ΔR divided by the length L. The data in the range from -2 to 2 T [shaded area in Fig. S3] are strongly affected by the WAL, but the higher-field portion of the data show oscillation with a well-defined period of ~1.6 T, which matches well to the cross-sectional area of the nanowire as discussed in the main text.



FIG. S3. Example of the background-subtraction procedure. (a) R(B) data at the Dirac point ($V_G = 2.4 \text{ V}$) for the $L = 3.4 \,\mu\text{m}$ section of the nanowire on substrate 2 (blue) normalized by the length L, measured at 260 mK. The background determined by LOESS-filtering is shown in black. The blue-shaded range is strongly affected by the weak antilocalization (WAL) effect. (b) Background-subtracted data in which the oscillation period of ~1.6 T can be identified at high magnetic fields.

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